

- [54] **ARITHMETIC PROCESSOR FOR USE IN ELECTRONIC COMPUTERS**
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- [73] Assignees: Matsushita Electric Industrial Co., Ltd., Japan; Solutions are Everything, Inc., Colo.
- [**] Term: 14 Years
- [21] Appl. No.: 179,112
- [22] Filed: Apr. 8, 1988
- [30] Foreign Application Priority Data
- | | | |
|--------------------|-------|----------|
| Dec. 15, 1987 [JP] | Japan | 62-51132 |
|--------------------|-------|----------|
- [52] U.S. Cl. D14/102
- [58] Field of Search D14/102, 100, 109; 361/390-395

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[57] CLAIM

The ornamental design for an arithmetic processor for use in electronic computers, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of an arithmetic processor for use in electronic computers station showing our new design;
FIG. 2 is a top plan view;
FIG. 3 is a rear elevational view;
FIG. 4 is a bottom view;
FIG. 5 is a right side elevational view;
FIG. 6 is a left side elevational view;
FIG. 7 is a front, top and right side perspective view;
FIG. 8 is a sectional view taken along line 8—8 of FIG. 1; and
FIG. 9 is a sectional view taken along line 9—9 of FIG. 1.
The internal mechanism removed in FIGS. 8 and 9 for convenience of illustration.

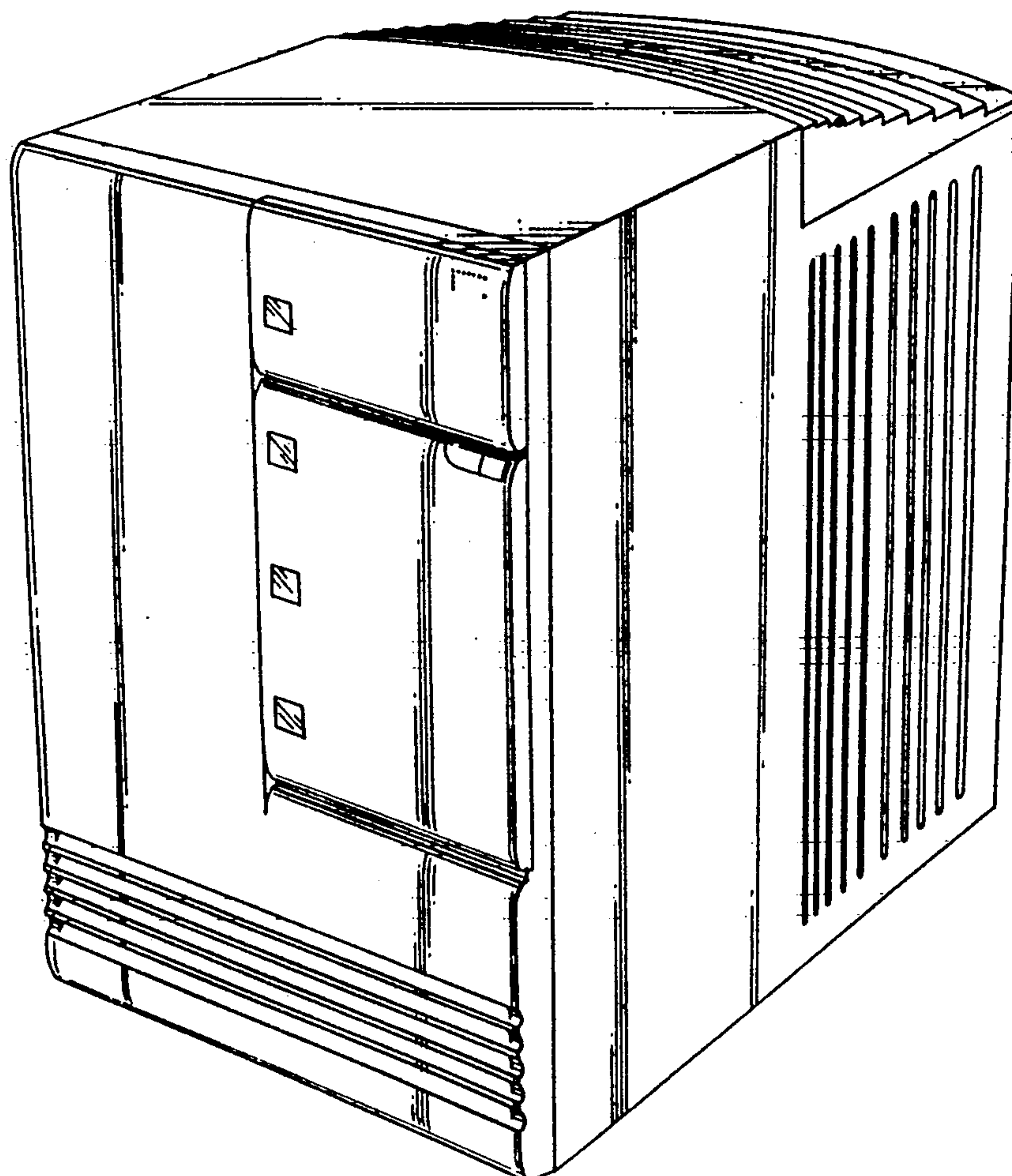


FIG. 2

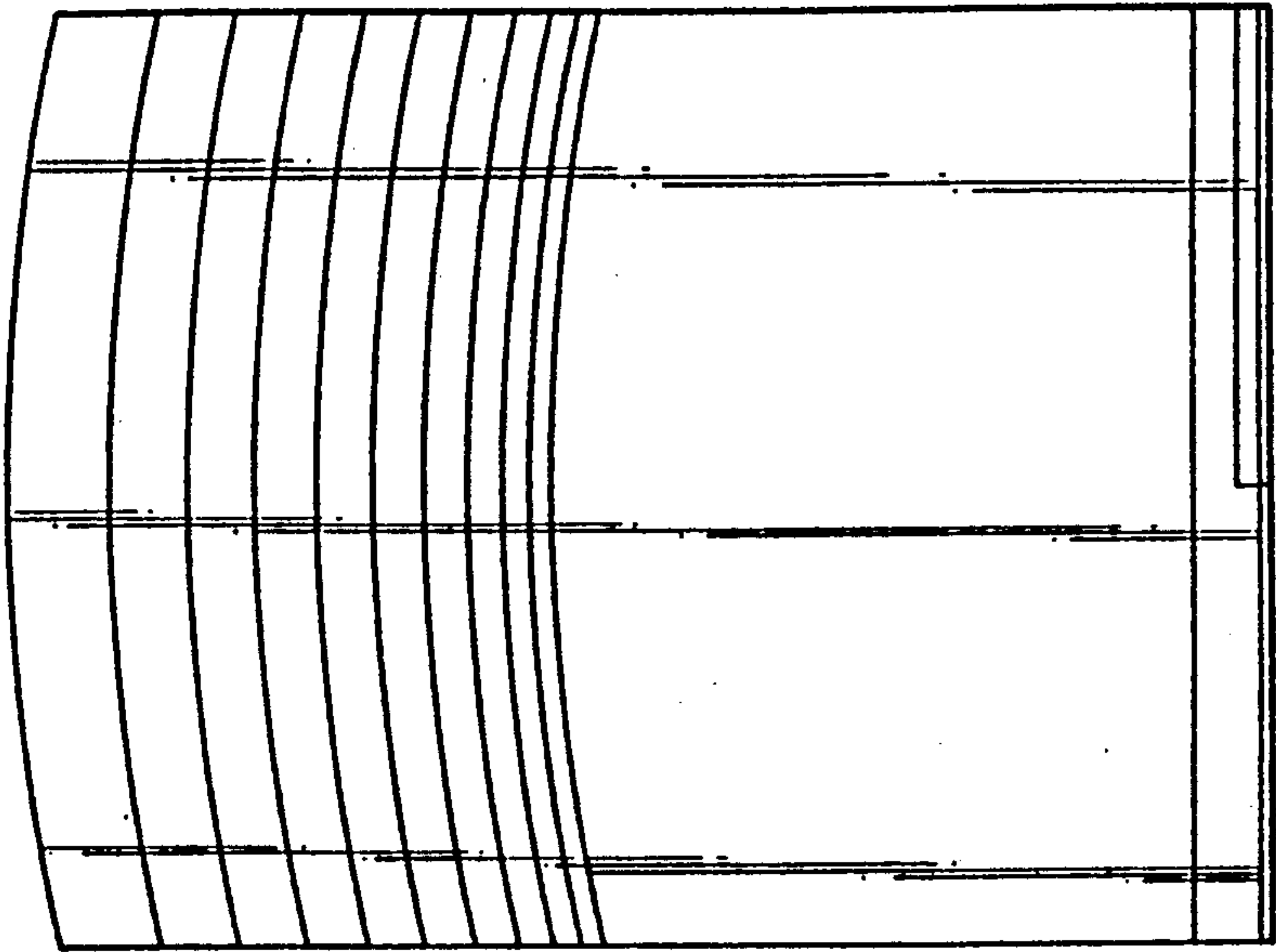


FIG. 1

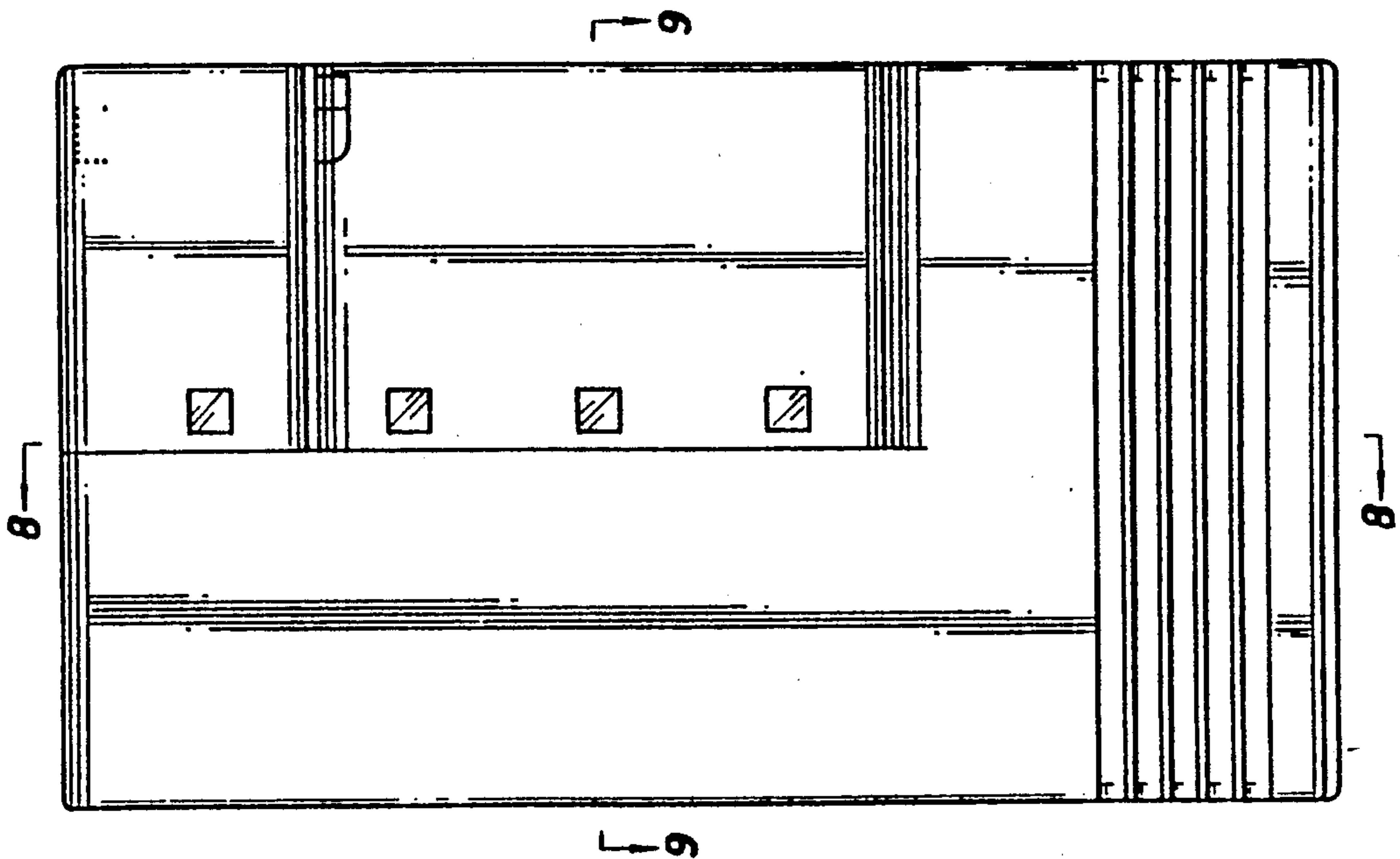


FIG. 4

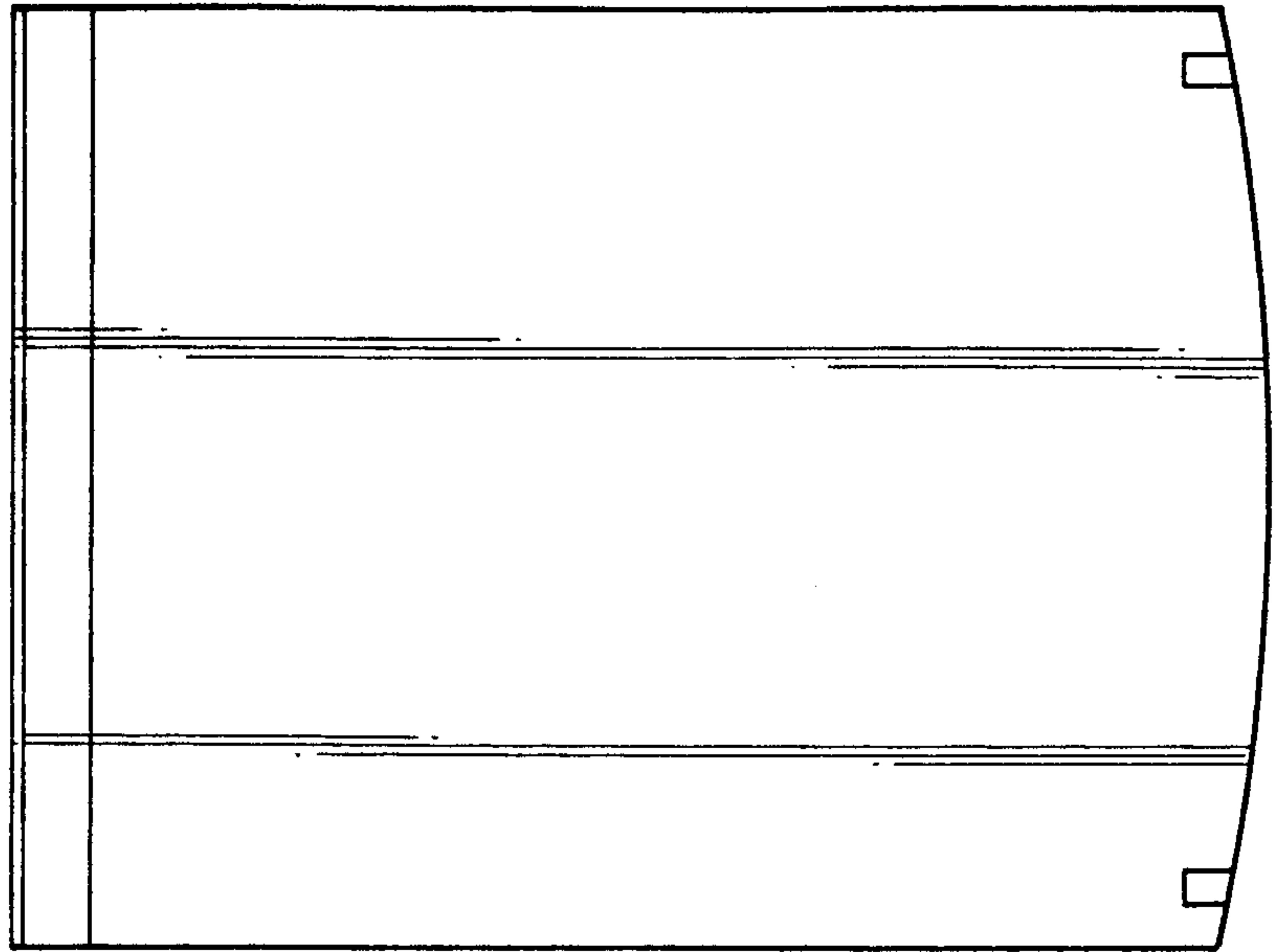


FIG. 3

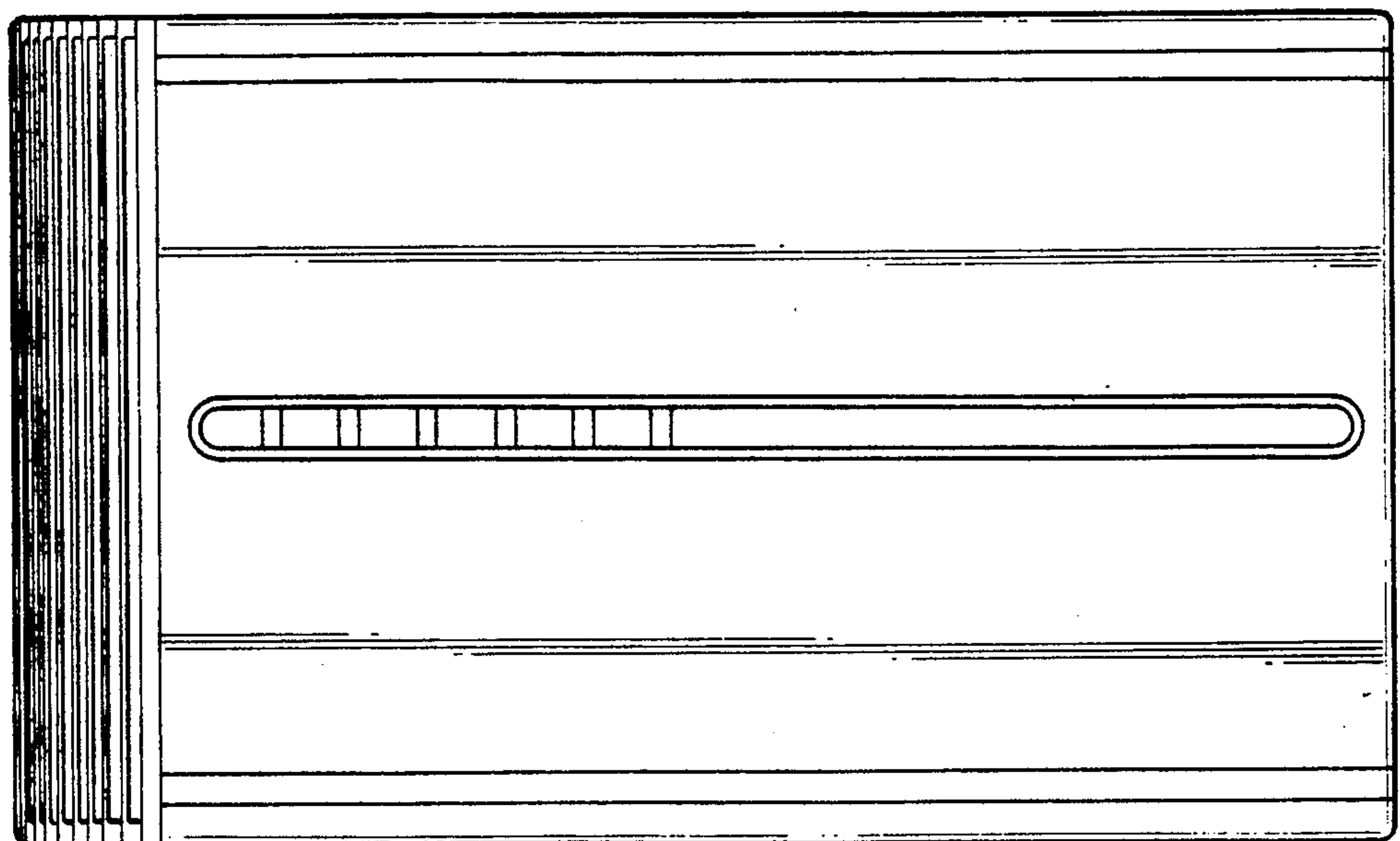


FIG. 6

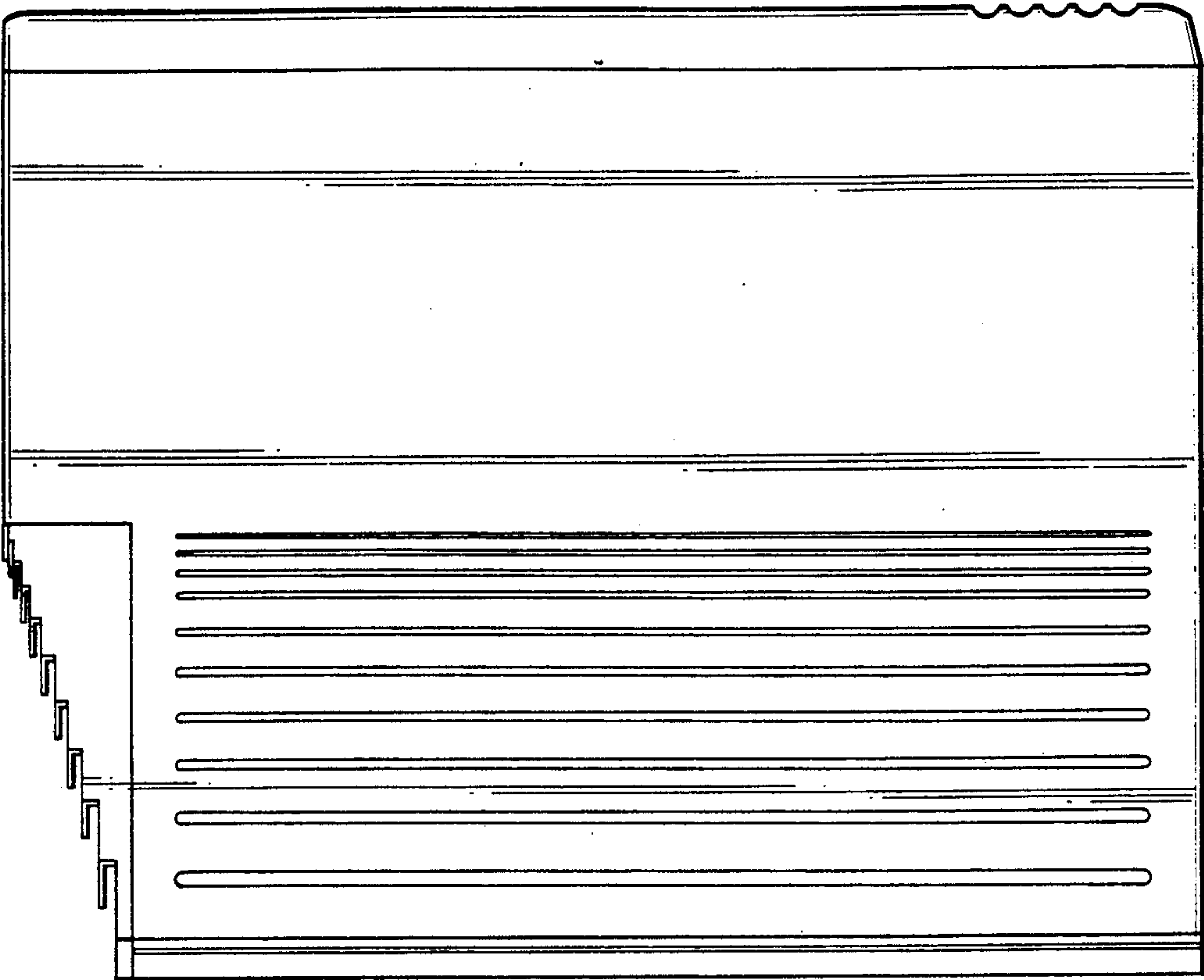


FIG. 5

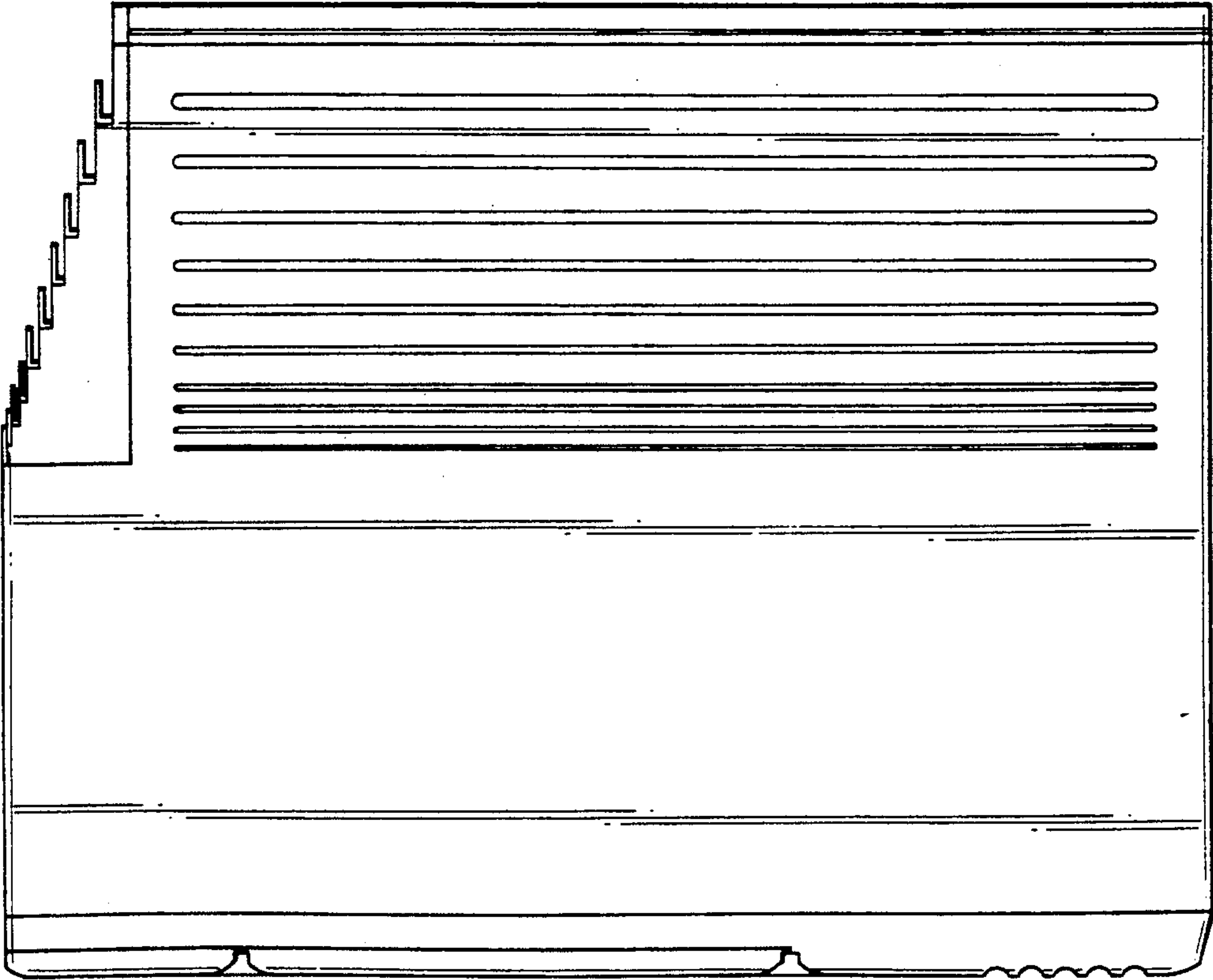


FIG. 7

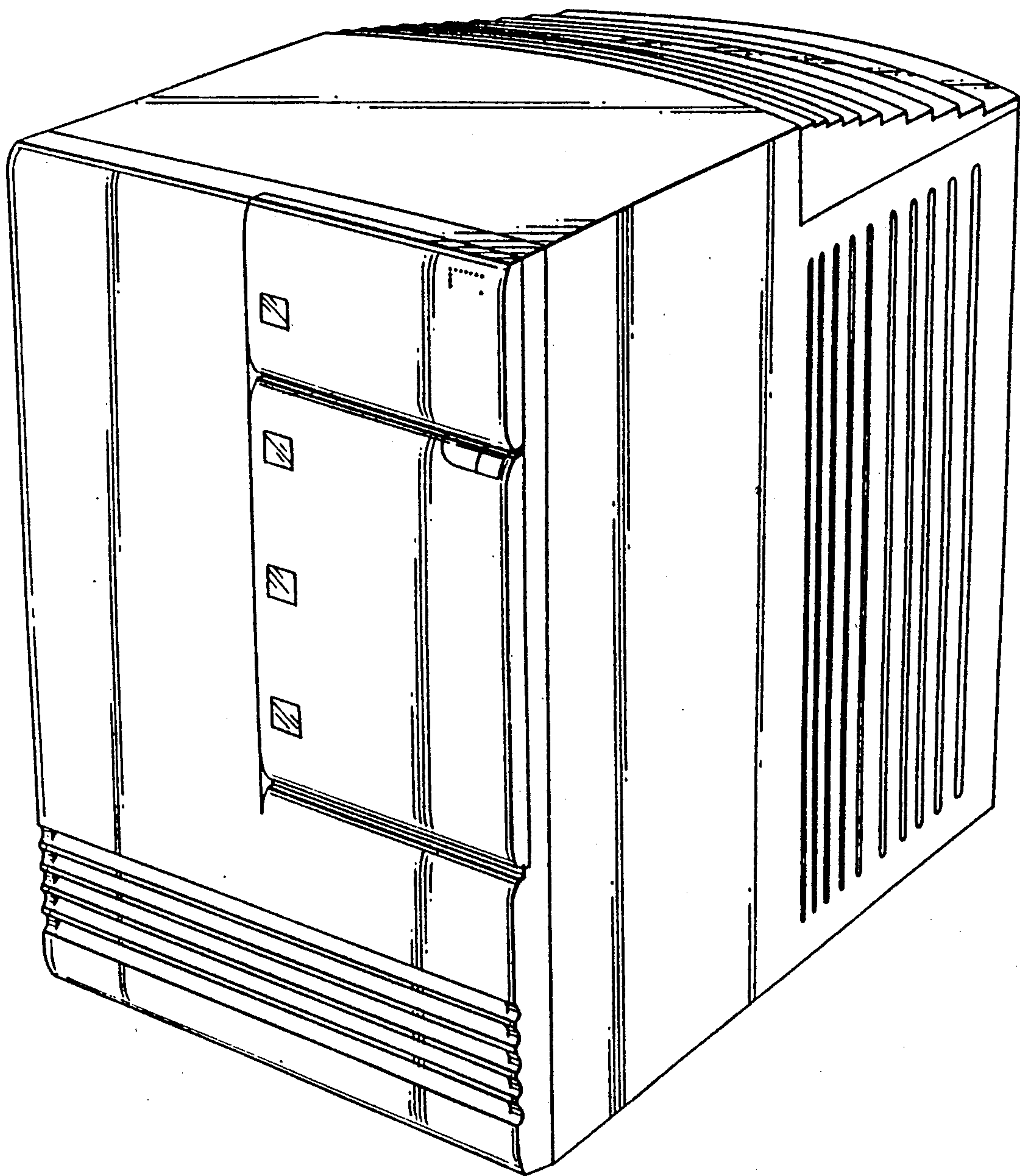


FIG. 9

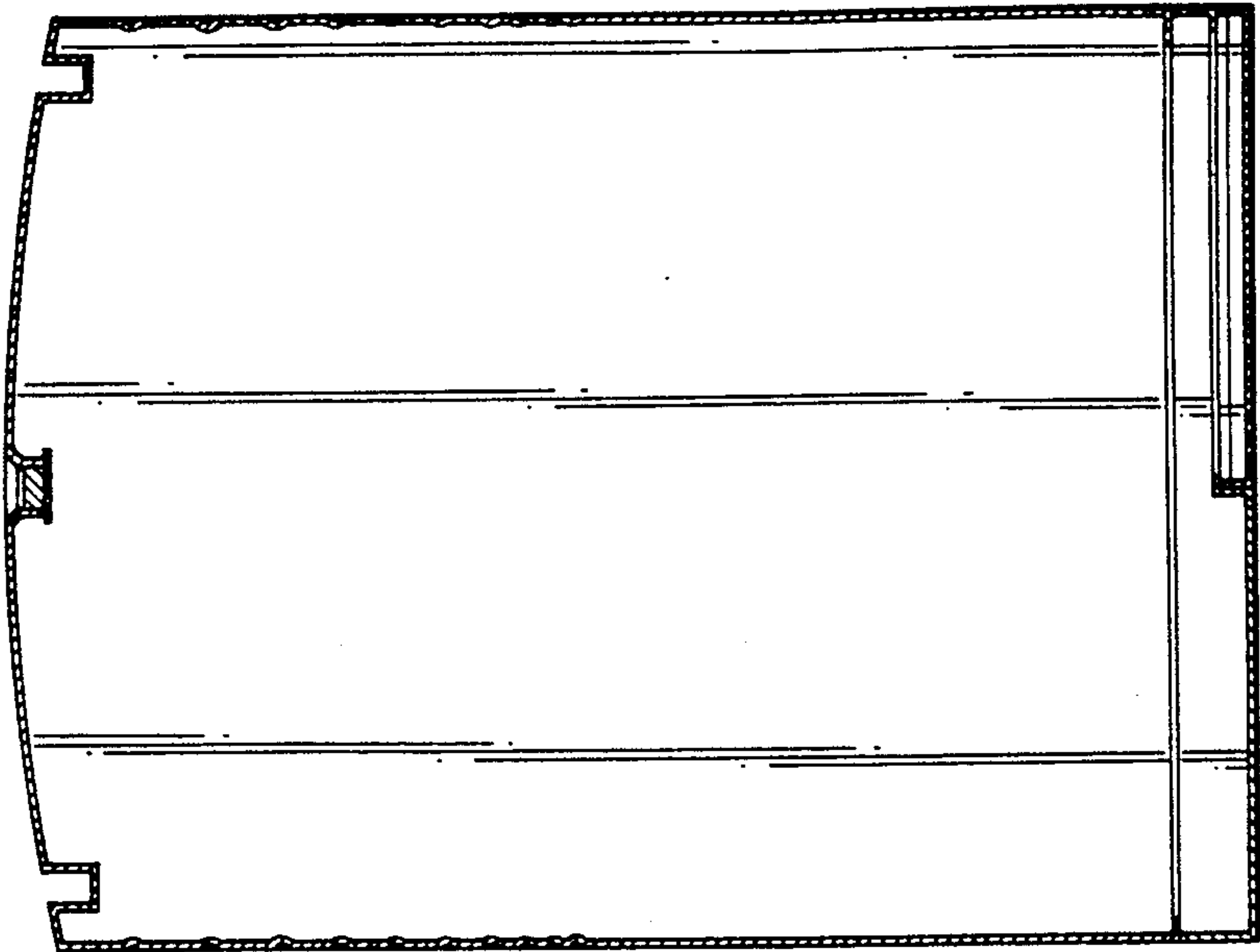


FIG. 8

