

United States Patent [19]

Aoki et al.

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[45] Date of Patent: **** Jun. 18, 1991**

[54] **DIGITAL CLOCK**

[75] Inventors: **Sanae Aoki; Tsunataka Konoike**, both of Tokyo, Japan

[73] Assignee: **Tanaka Manufacturing Company Limited**, Tokyo, Japan

[**] Term: **14 Years**

[21] Appl. No.: **88,104**

[22] Filed: **Aug. 21, 1987**

[52] U.S. Cl. **D10/15; D10/22**

[58] Field of Search **D10/1-39, D10/122-126; 368/82-84, 239-242, 276-277, 280-282, 285**

[56] **References Cited**

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[57] **CLAIM**

The ornamental design for a digital clock, as shown and described.

DESCRIPTION

FIG. 1 is a top front isometric view of a digital clock showing my new design;
 FIG. 2 is a front elevational view, the rear being a mirror image thereof;
 FIG. 3 is a right side elevational view, the left side being a mirror image thereof;
 FIG. 4 is a top plan view thereof;
 FIG. 5 is a bottom plan view thereof; and
 FIG. 6 is a right side elevational view thereof in an alternate position.
 The view from the rear is identical to the view from the front; the view from the opposite side is identical to the view from the one side.

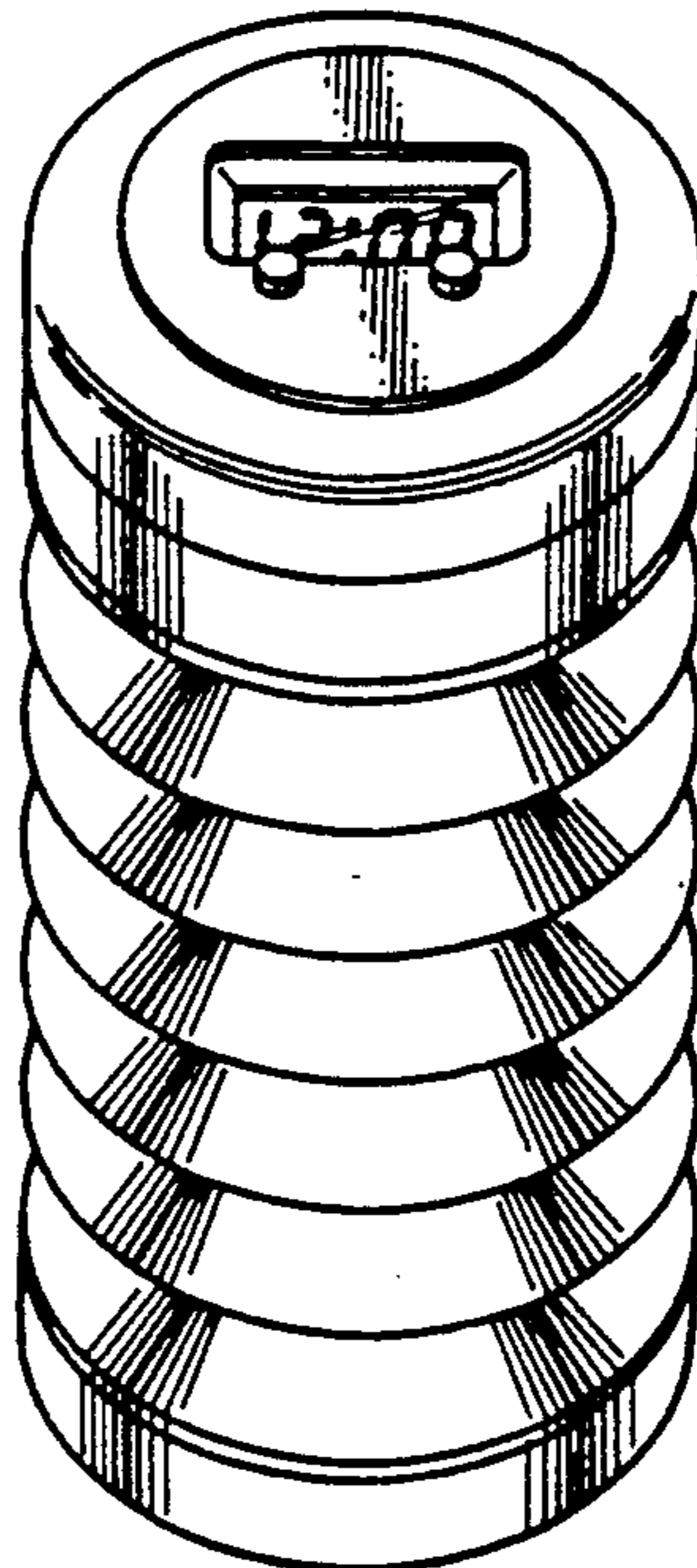


FIG. 1

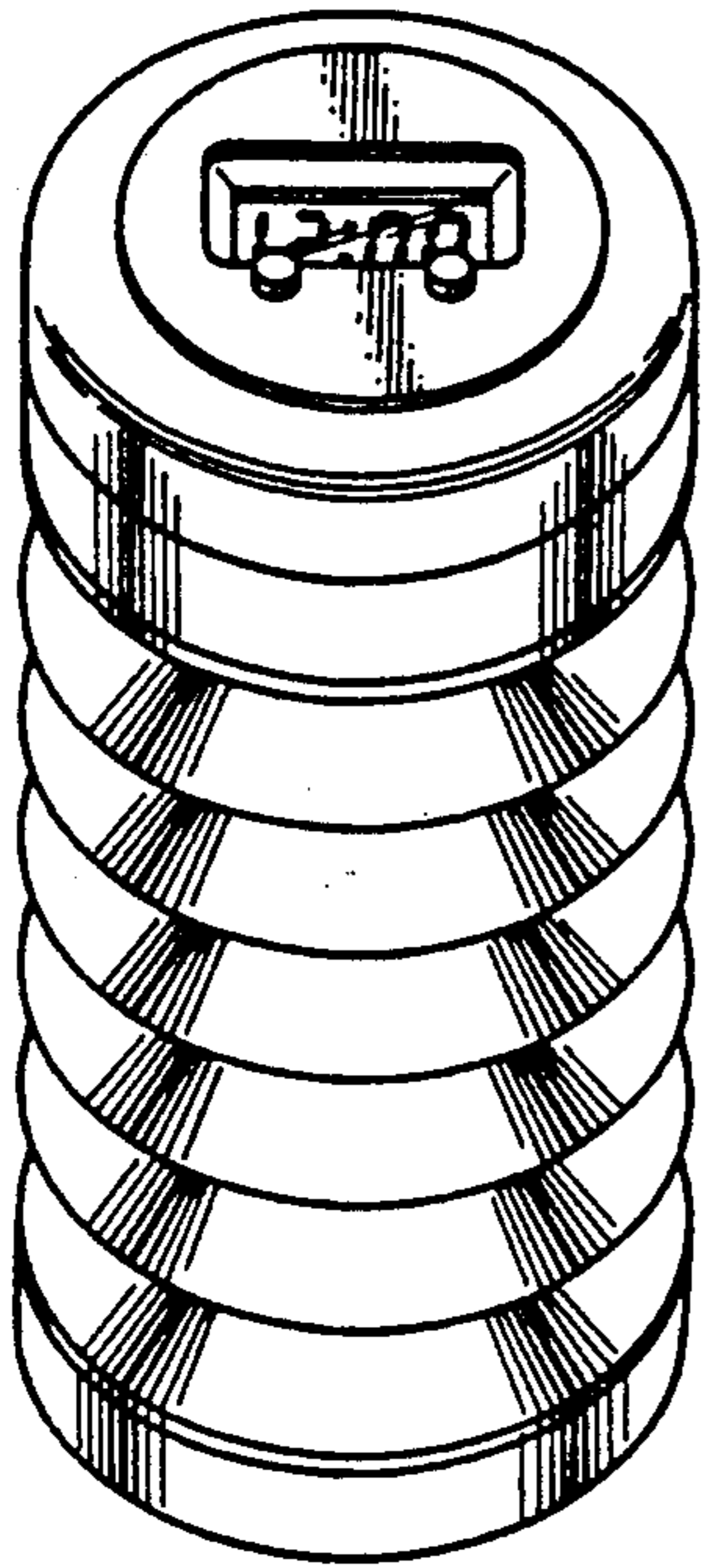


FIG. 2

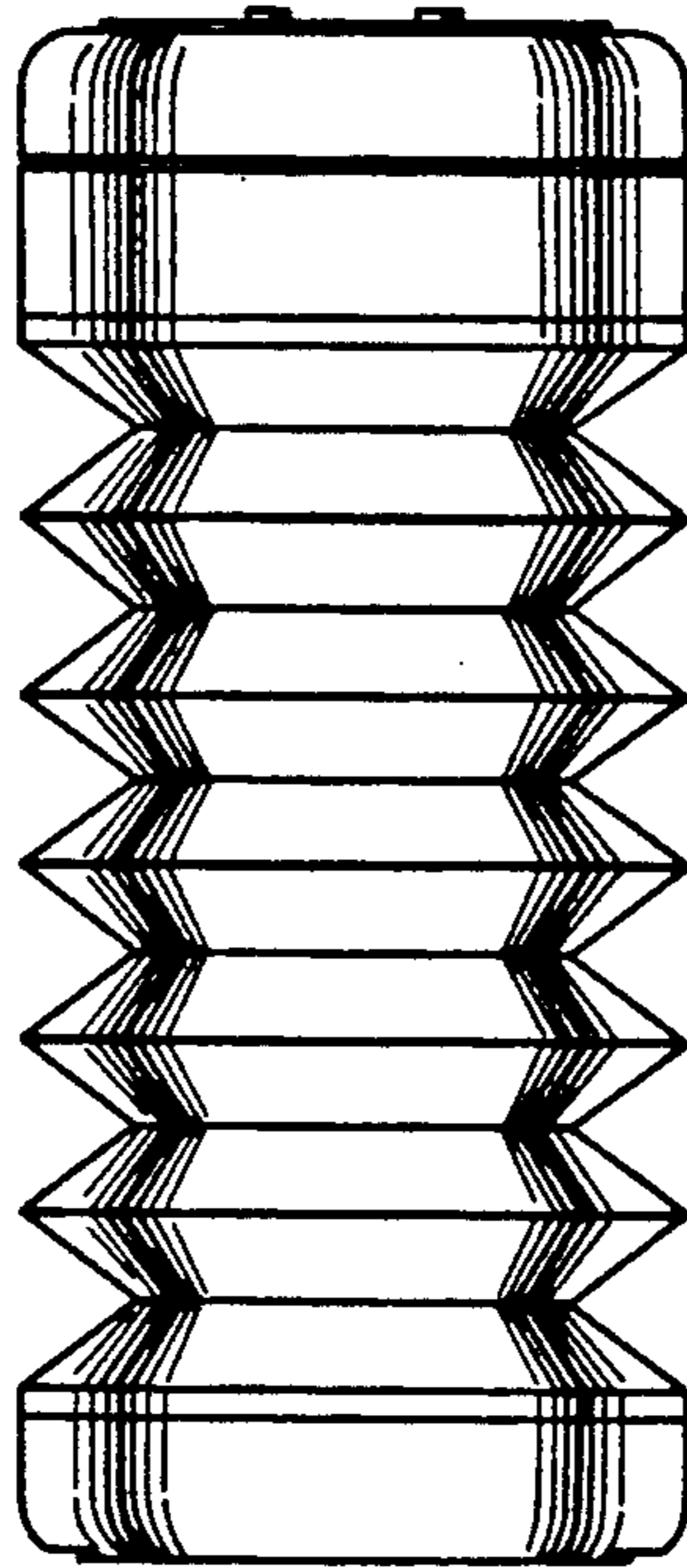


FIG. 3

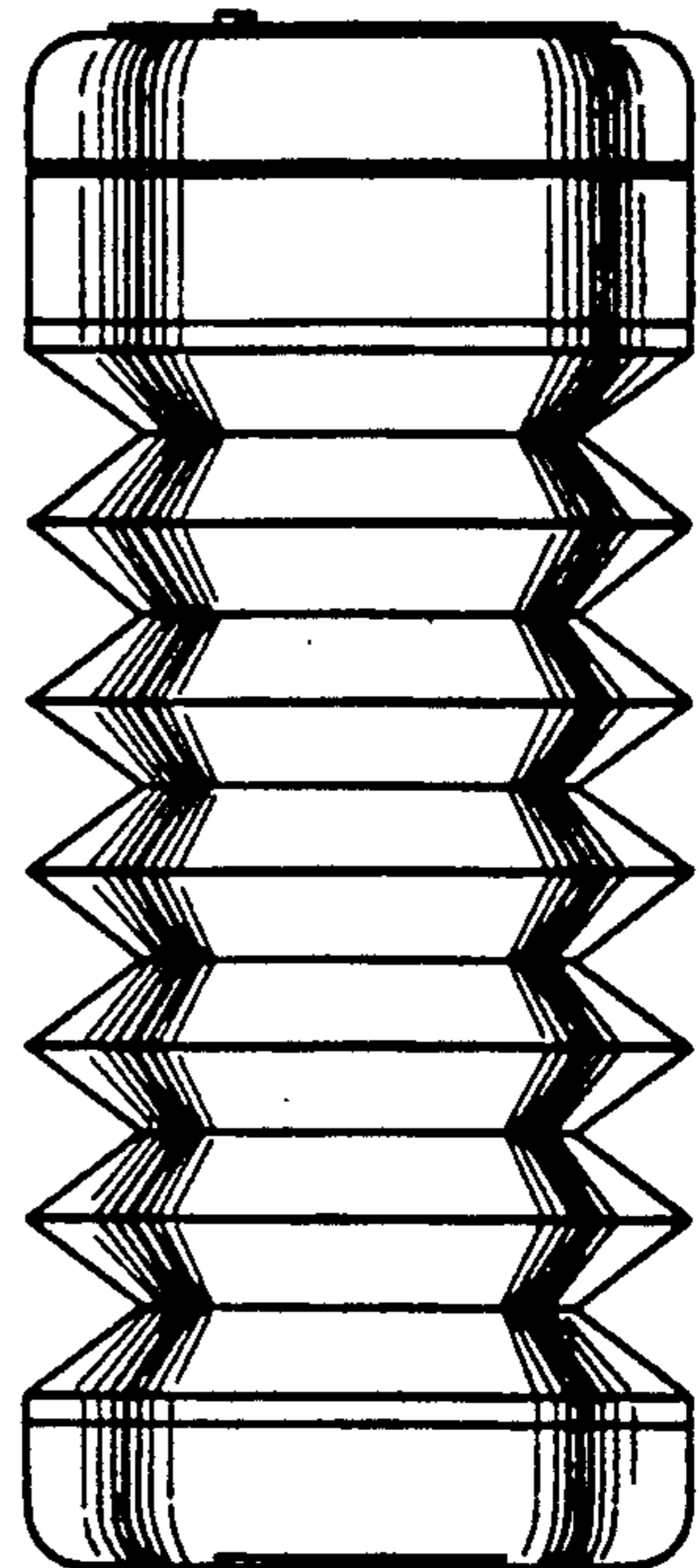


FIG. 4

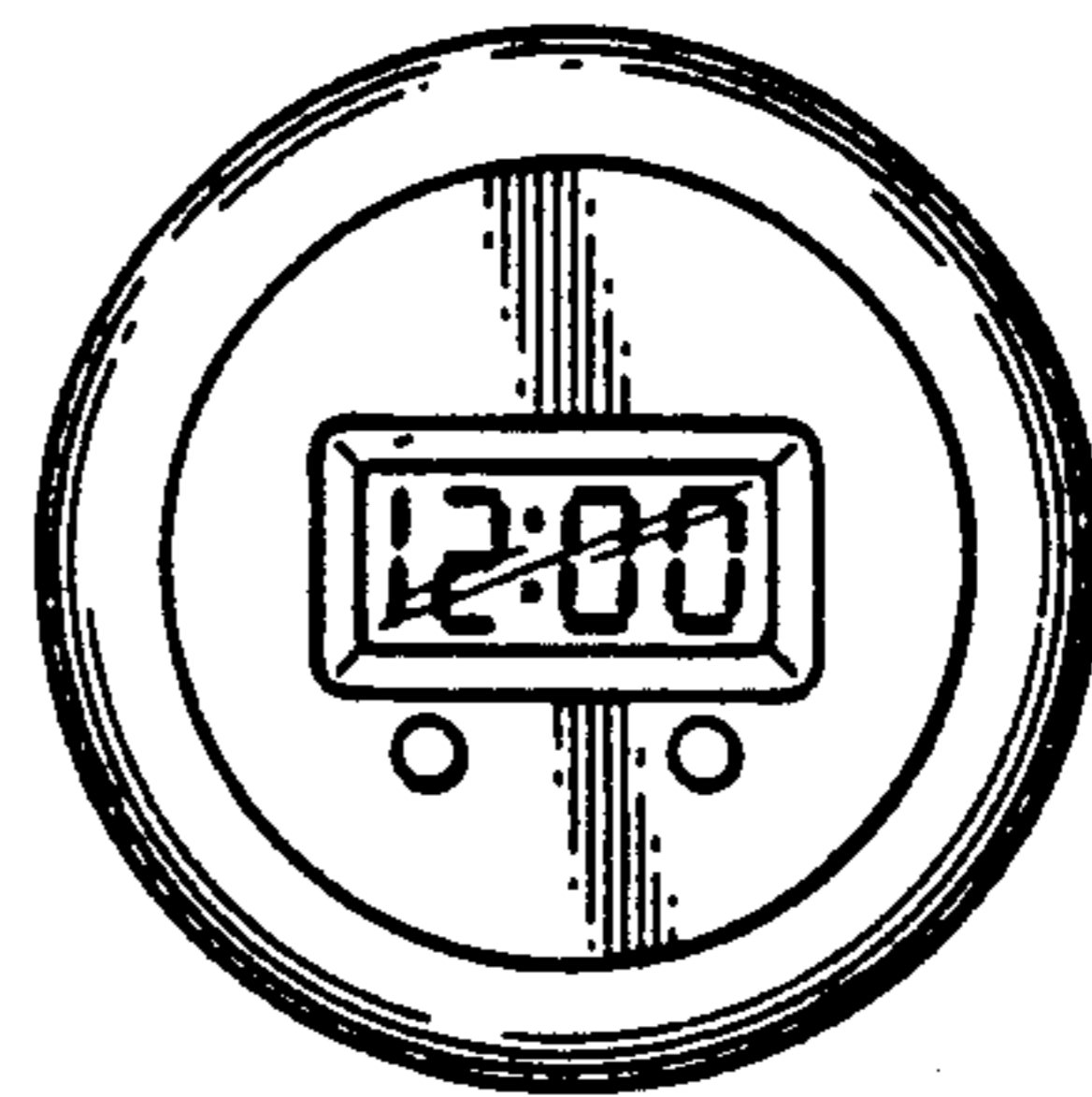


FIG. 6

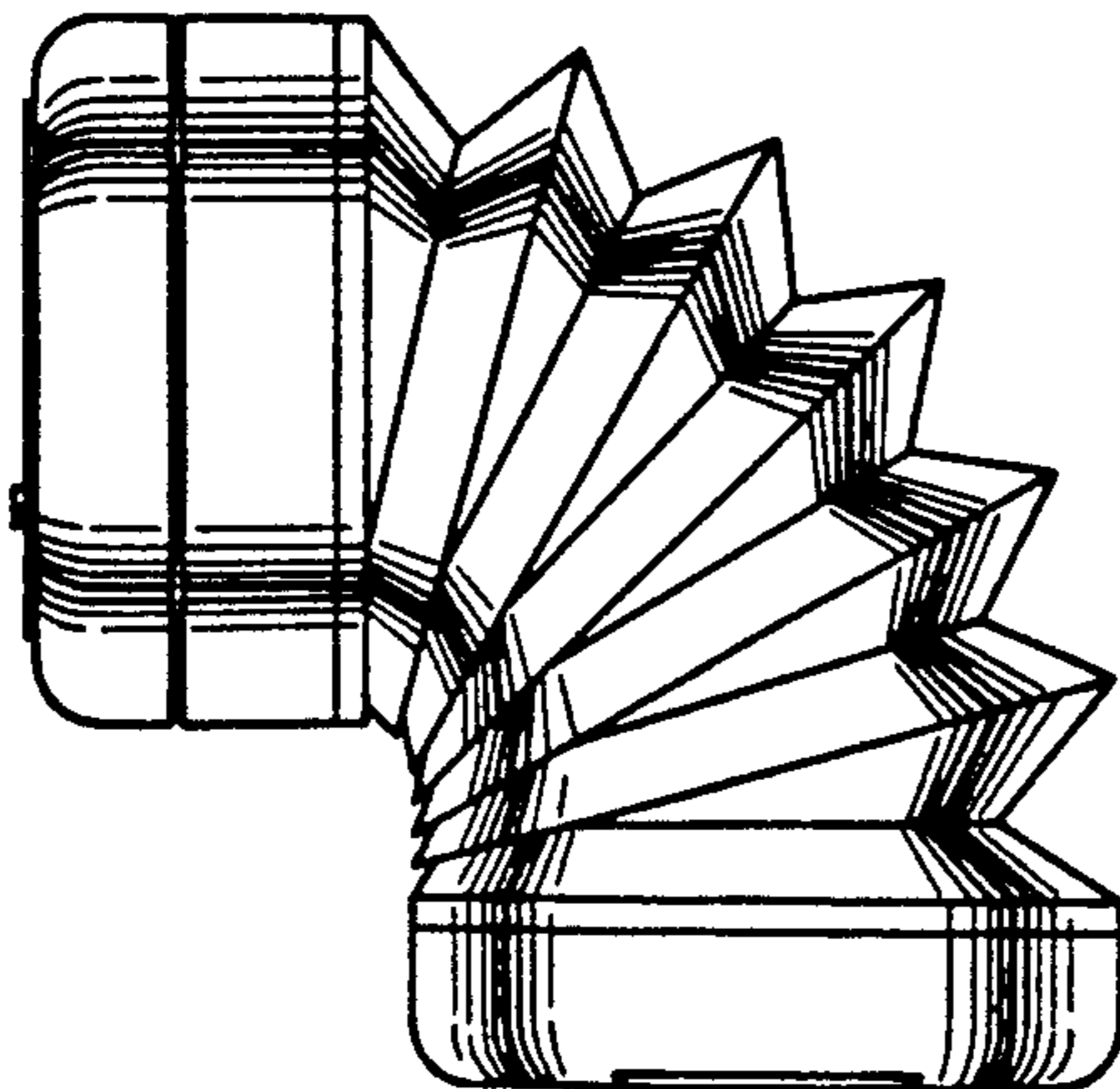


FIG. 5

