

[54] **COMPUTER PROCESSOR MODULE**

[75] **Inventor: James E. Sacherman, Palo Alto, Calif.**

[73] **Assignee: Wyse Technology, Inc., San Jose, Calif.**

[\*\*] **Term: 14 Years**

[21] **Appl. No.: 194,581**

[22] **Filed: May 16, 1988**

[52] **U.S. Cl. .... D14/107; D14/100**

[58] **Field of Search .... D14/100, 102, 107-109; D6/422-428, 432; D13/184; 200/5 A, 5 R, 6 A, 6 R; 360/97-99; 361/390-395, 415, 426; 312/208**

[56] **References Cited**  
**U.S. PATENT DOCUMENTS**

- D. 259,041 4/1981 Ault et al. .... D14/100
- D. 268,931 5/1983 Ellis et al. .... D14/107

- D. 277,004 1/1985 Cridland et al. .... D14/100
- D. 302,266 7/1989 Kelly et al. .... D14/100

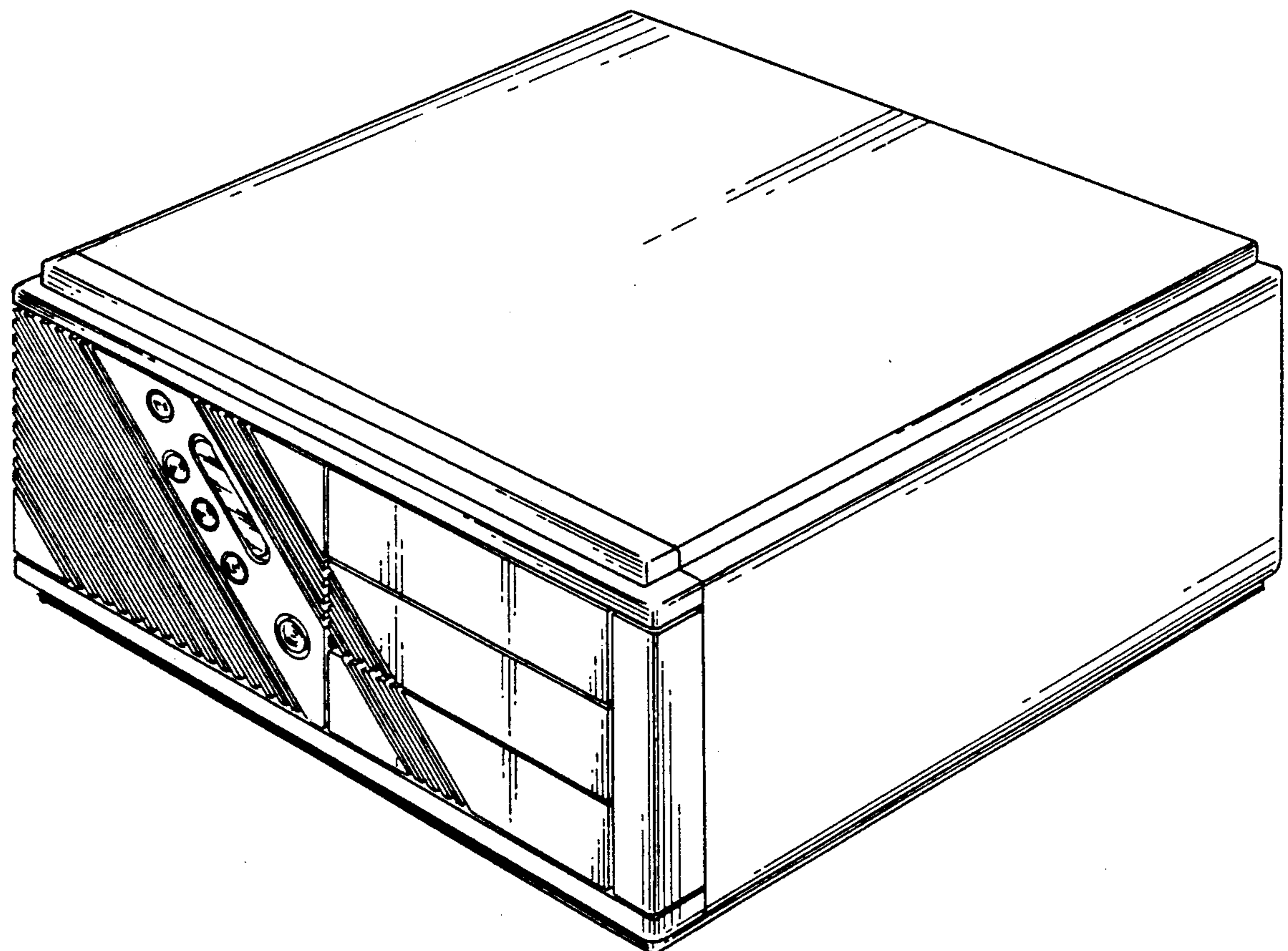
*Primary Examiner—Wallace R. Burke*  
*Assistant Examiner—Freda S. Nunn*  
*Attorney, Agent, or Firm—Harrison & Eakin*

[57] **CLAIM**

The ornamental design for a computer processor module, as shown and described.

**DESCRIPTION**

FIG. 1 is a front elevational view of a computer processor module showing my new design, the rear elevational being plain and unornamented; FIG. 2 is a left side elevational view thereof on a reduced scale; FIG. 3 is a right side elevational view thereof on a reduced scale. FIG. 4 is a top plan view thereof on a reduced scale. FIG. 5 is a top, front, right side perspective view thereof on a reduced scale.



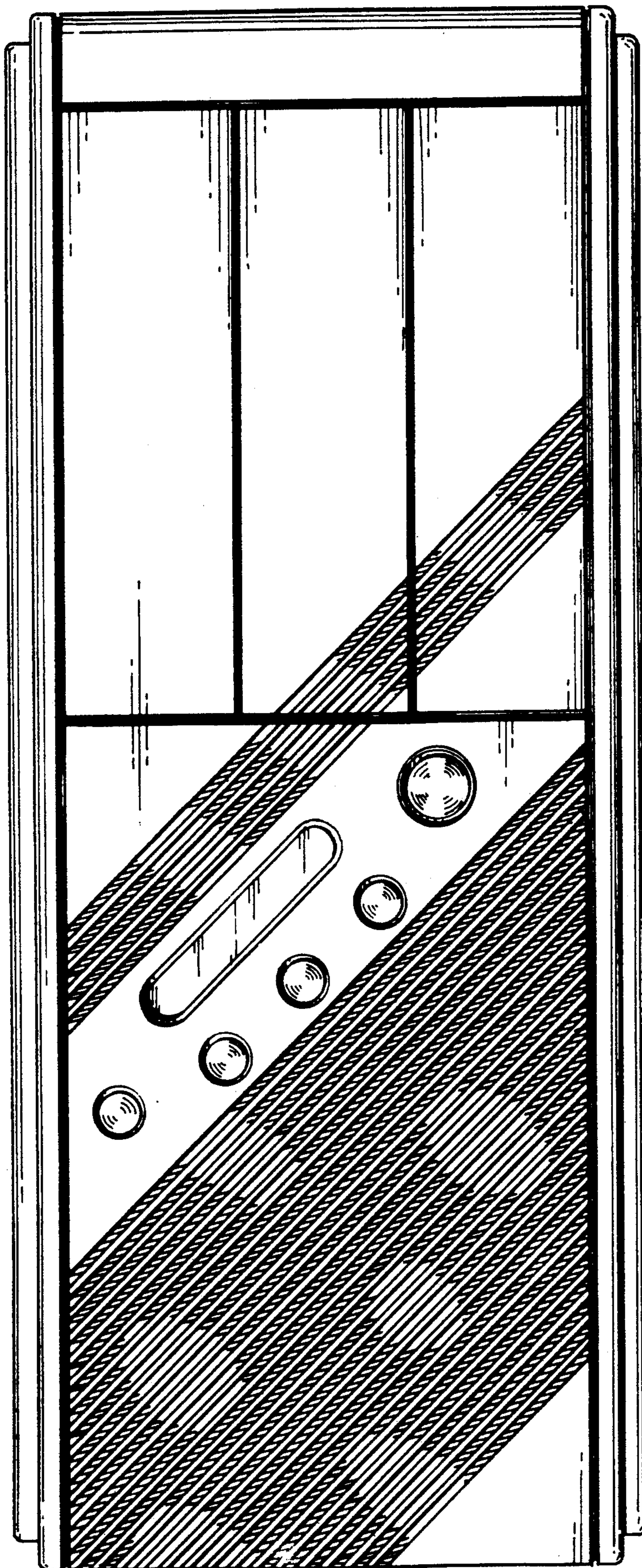


FIG. 1

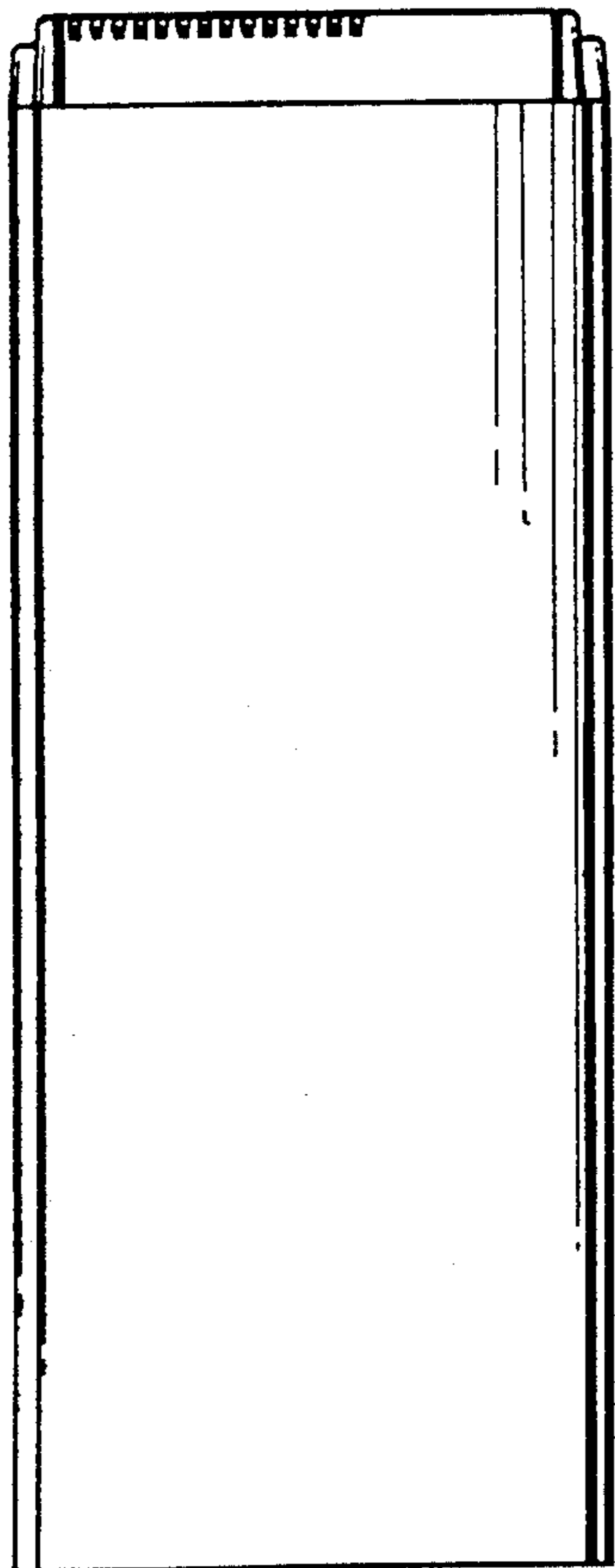


FIG. 2

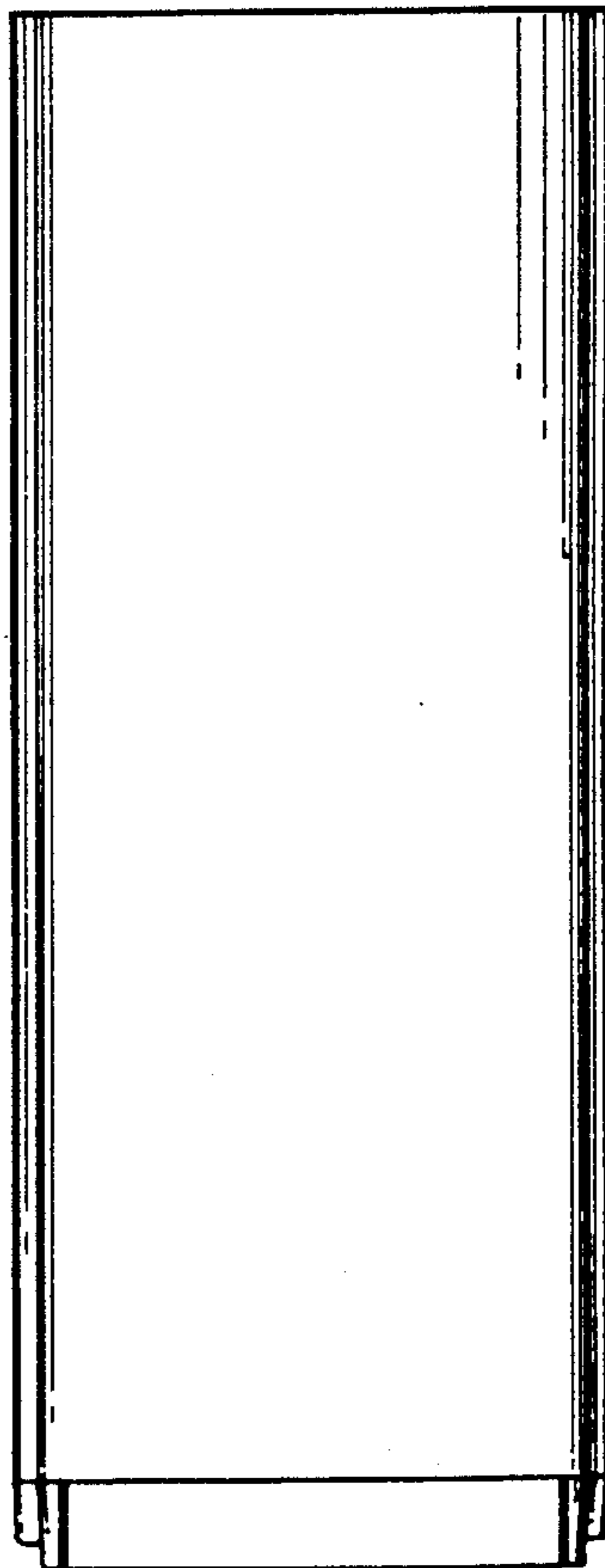


FIG. 3

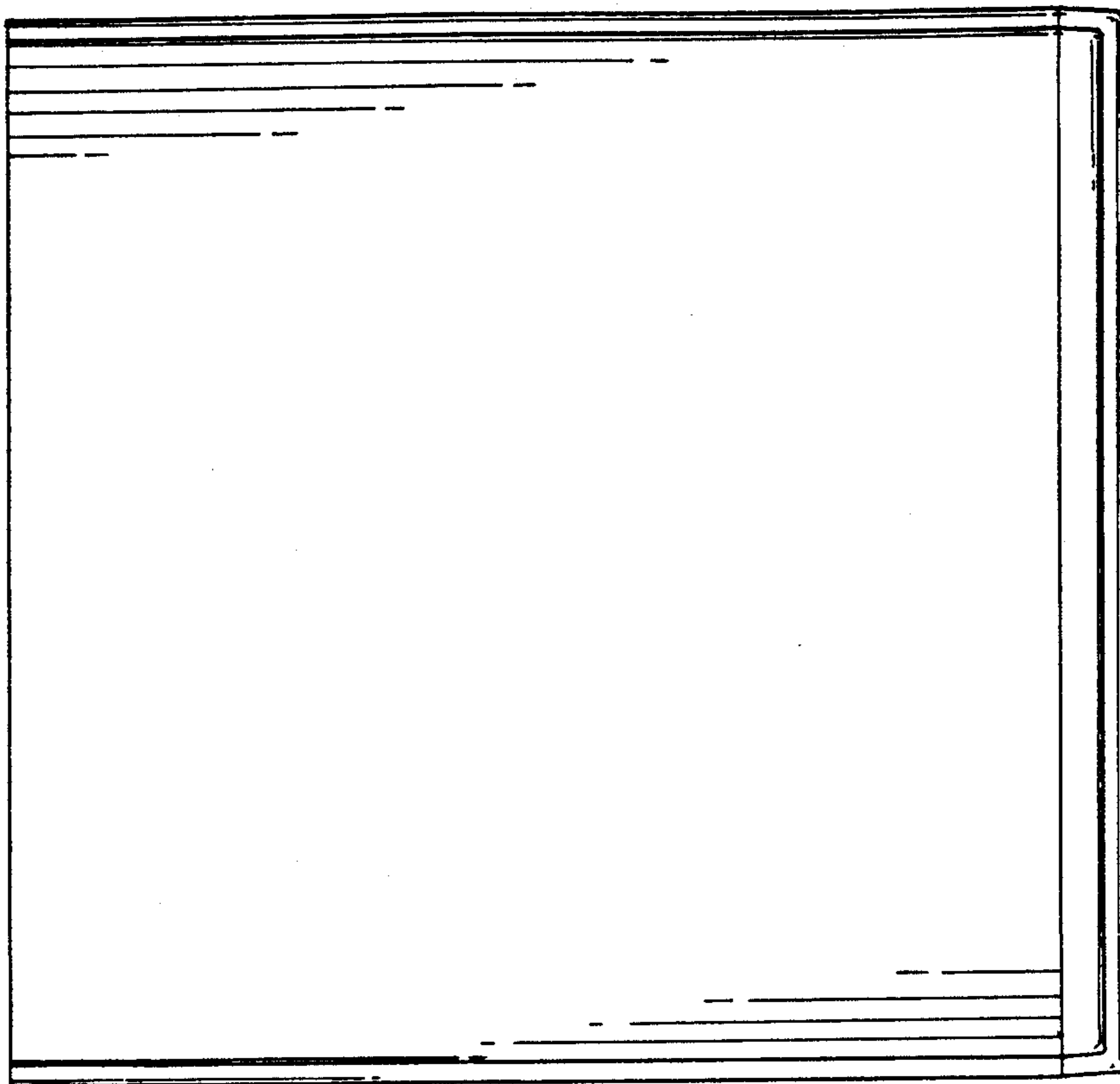


FIG. 4

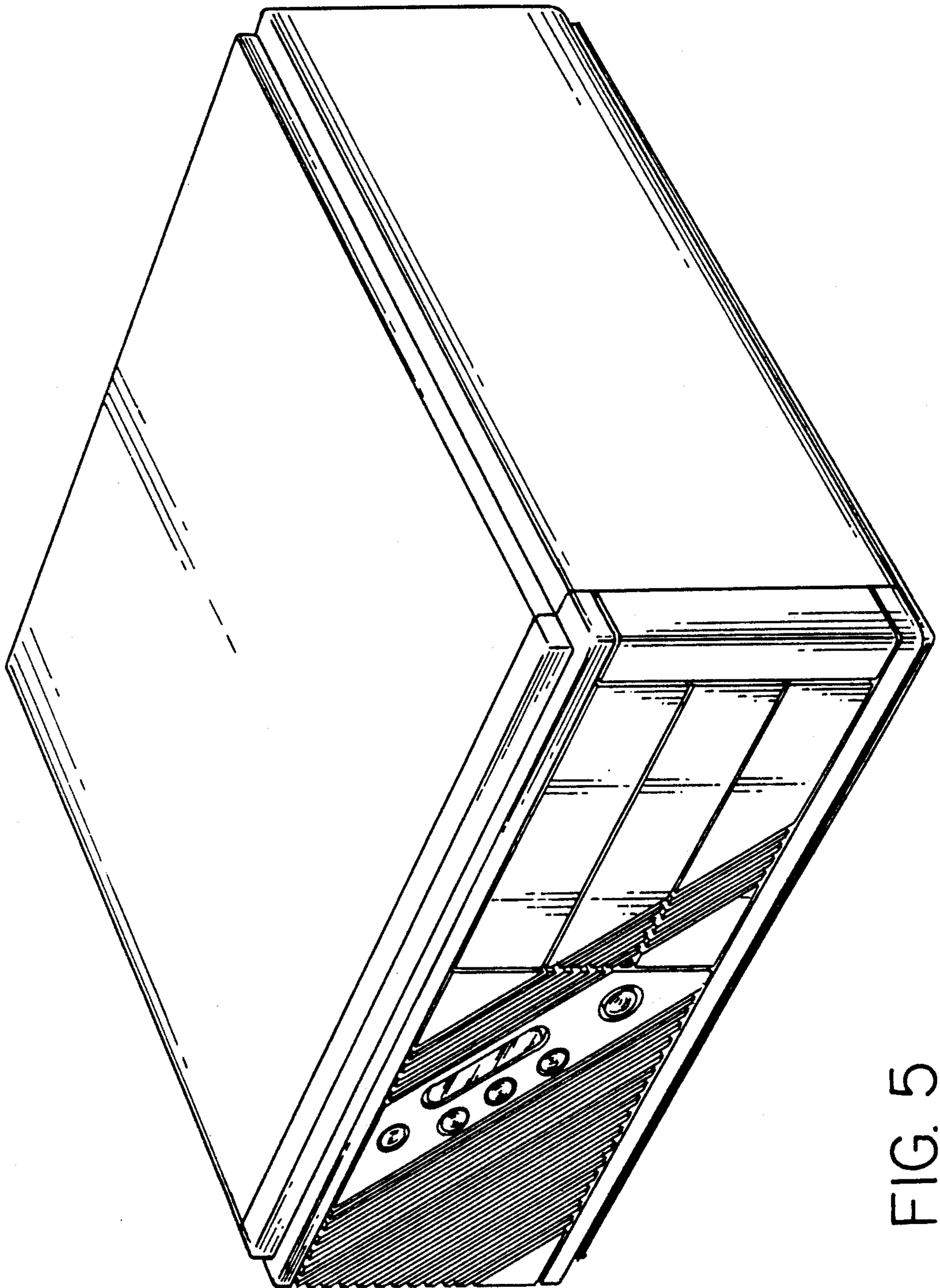


FIG. 5