

[54] ARITHMETIC PROCESSOR FOR USE IN ELECTRONIC COMPUTERS

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[**] Term: 14 Years

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[30] Foreign Application Priority Data
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[52] U.S. Cl. D14/102
[58] Field of Search D14/100, 102, 109; 361/390-395

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[57] CLAIM

The ornamental design for an arithmetic processor for use in electronic computers, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of an arithmetic processor for use in electronic computers showing our new design;
FIG. 2 is a top plan view;
FIG. 3 is a rear elevational view;
FIG. 4 is a bottom view;
FIG. 5 is a right side elevational view;
FIG. 6 is a left side elevational view;
FIG. 7 is a front, top and right side perspective view;
FIG. 8 is a perspective view similar to FIG. 7 with the sliding door in open position for illustrative purposes;
FIG. 9 is a sectional view taken along line 9—9 of FIG. 1; and
FIG. 10 is a sectional view taken along line 10—10 of FIG. 1.
The internal mechanism removed in FIGS. 9 and 10 for convenience of illustration.

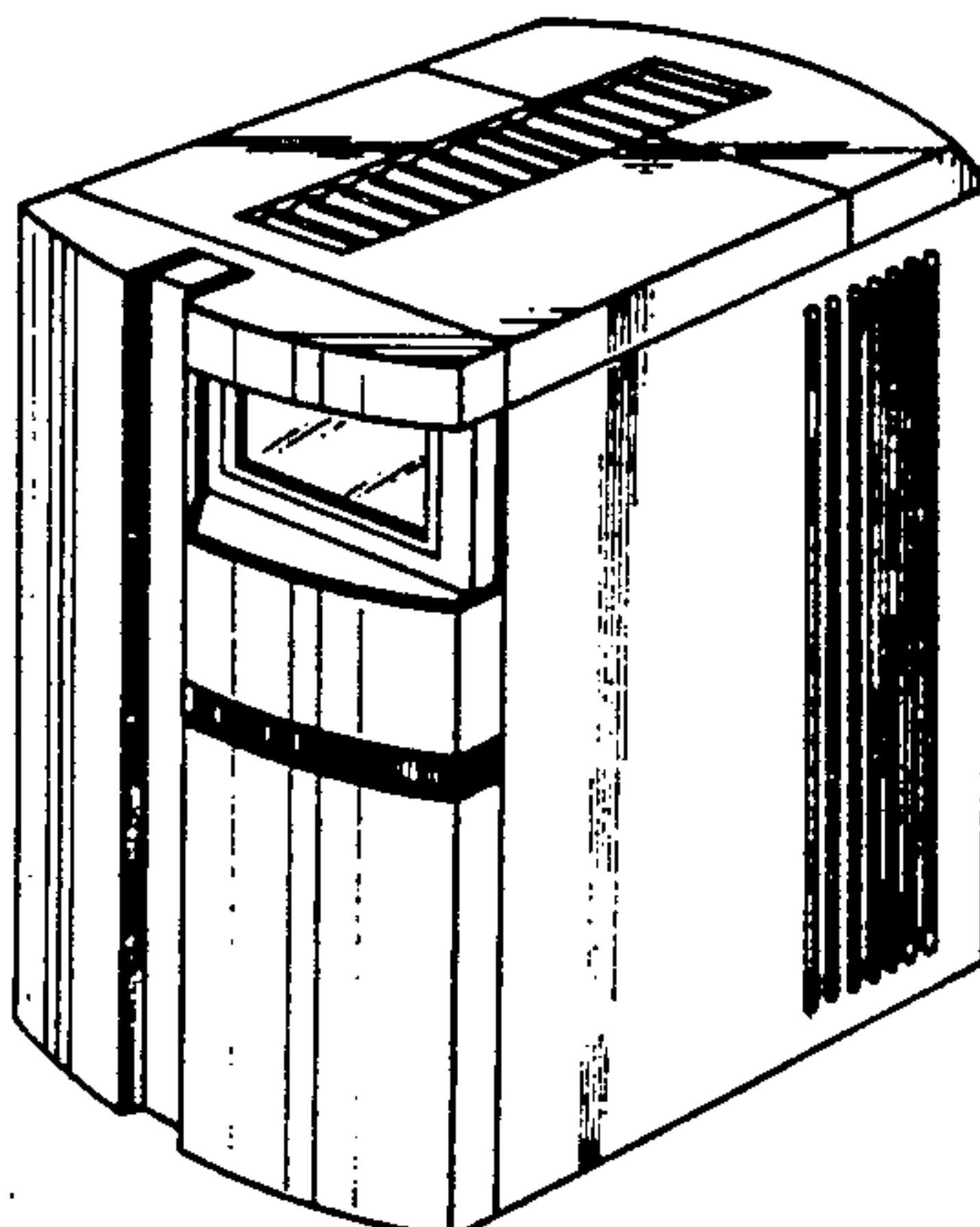
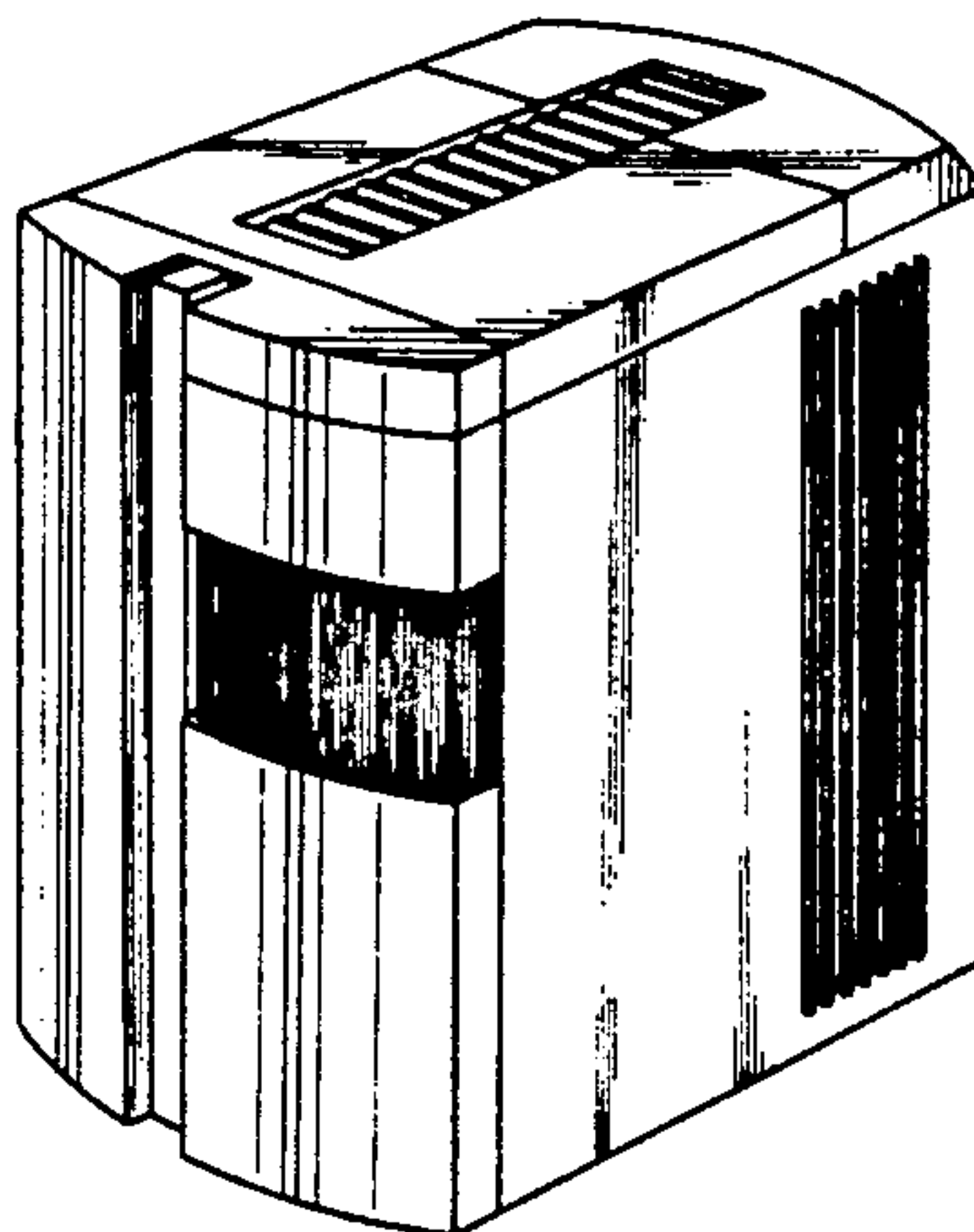


FIG. 1

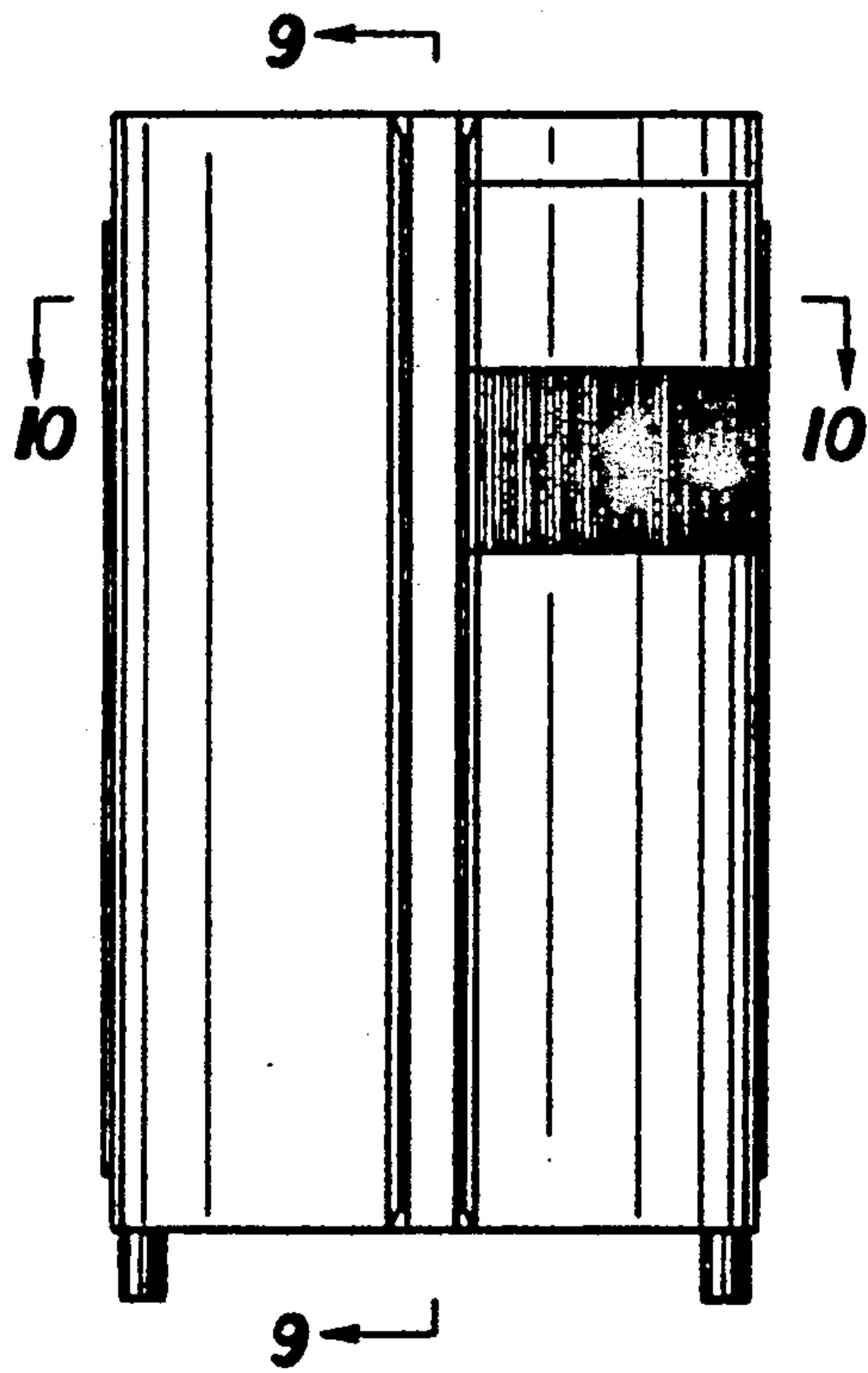


FIG. 2

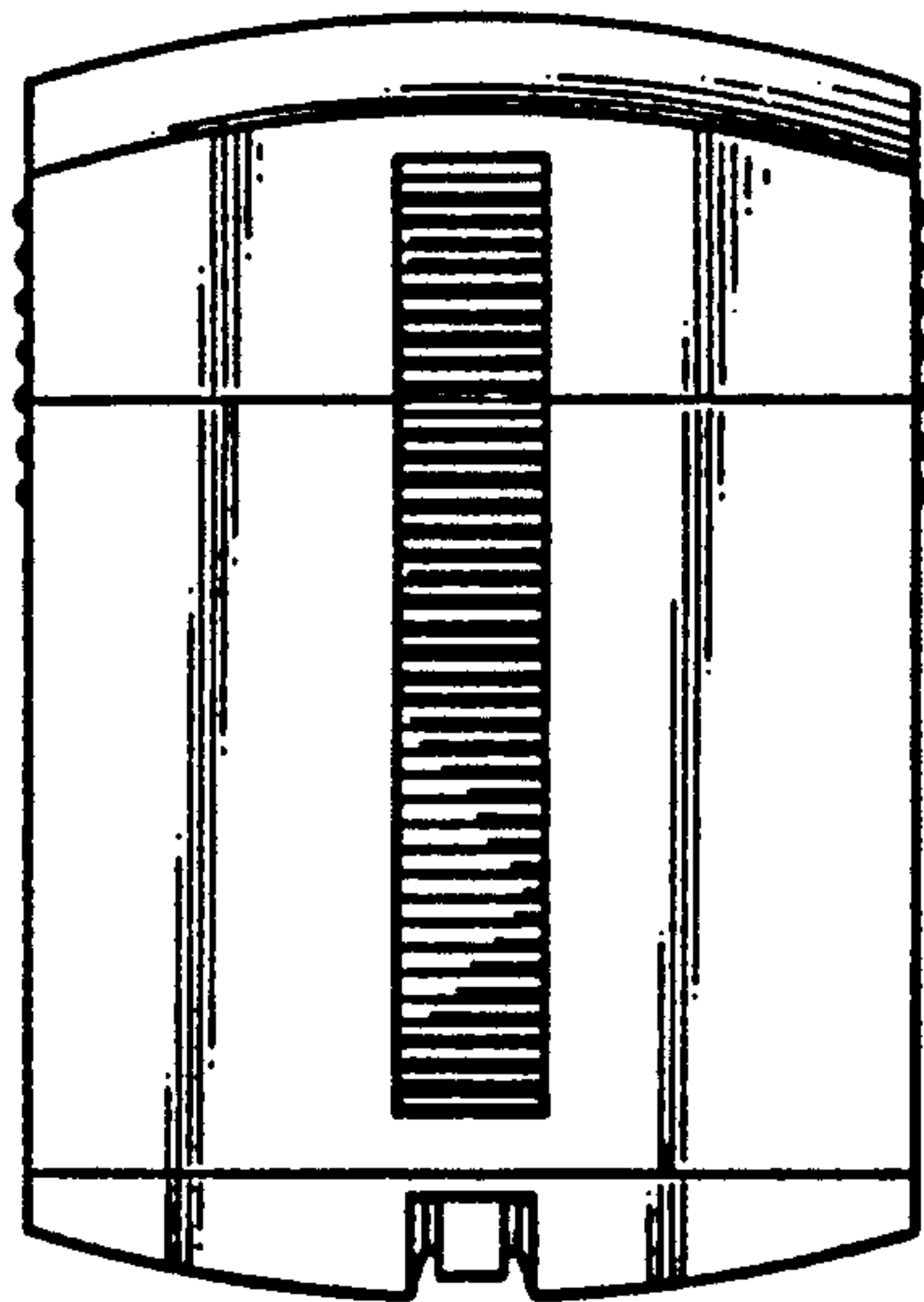


FIG. 3

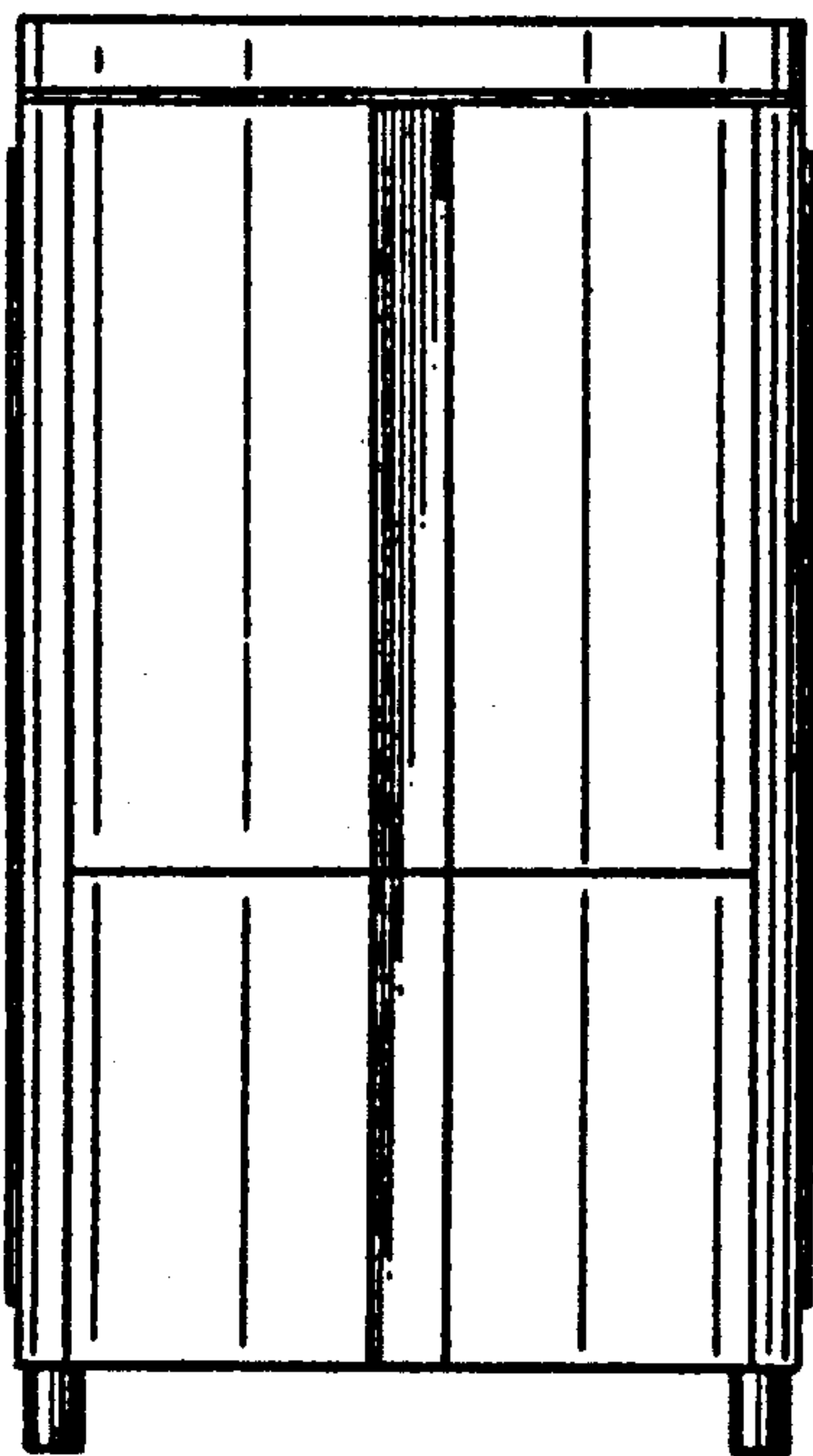


FIG. 4

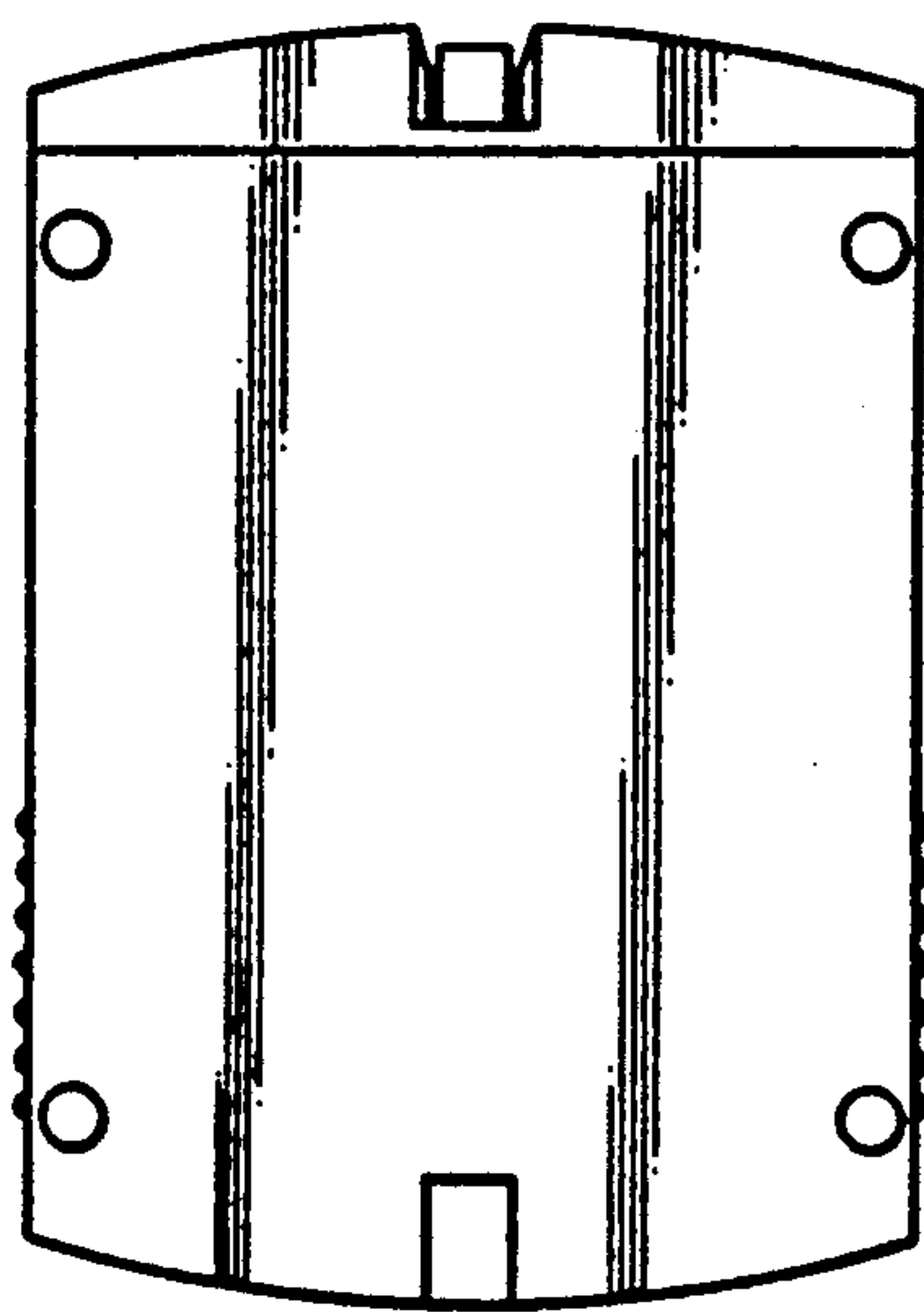


FIG. 5

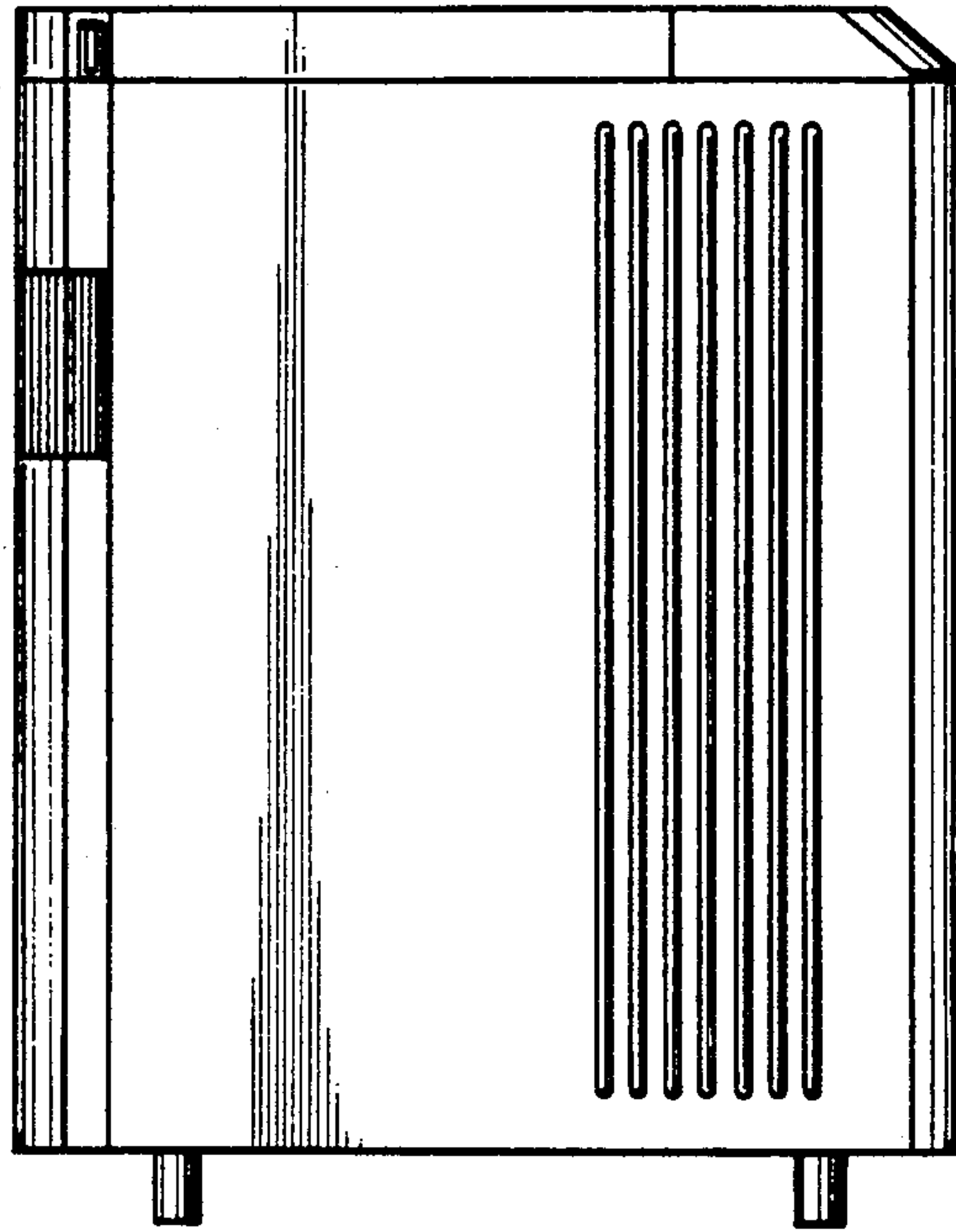


FIG. 6

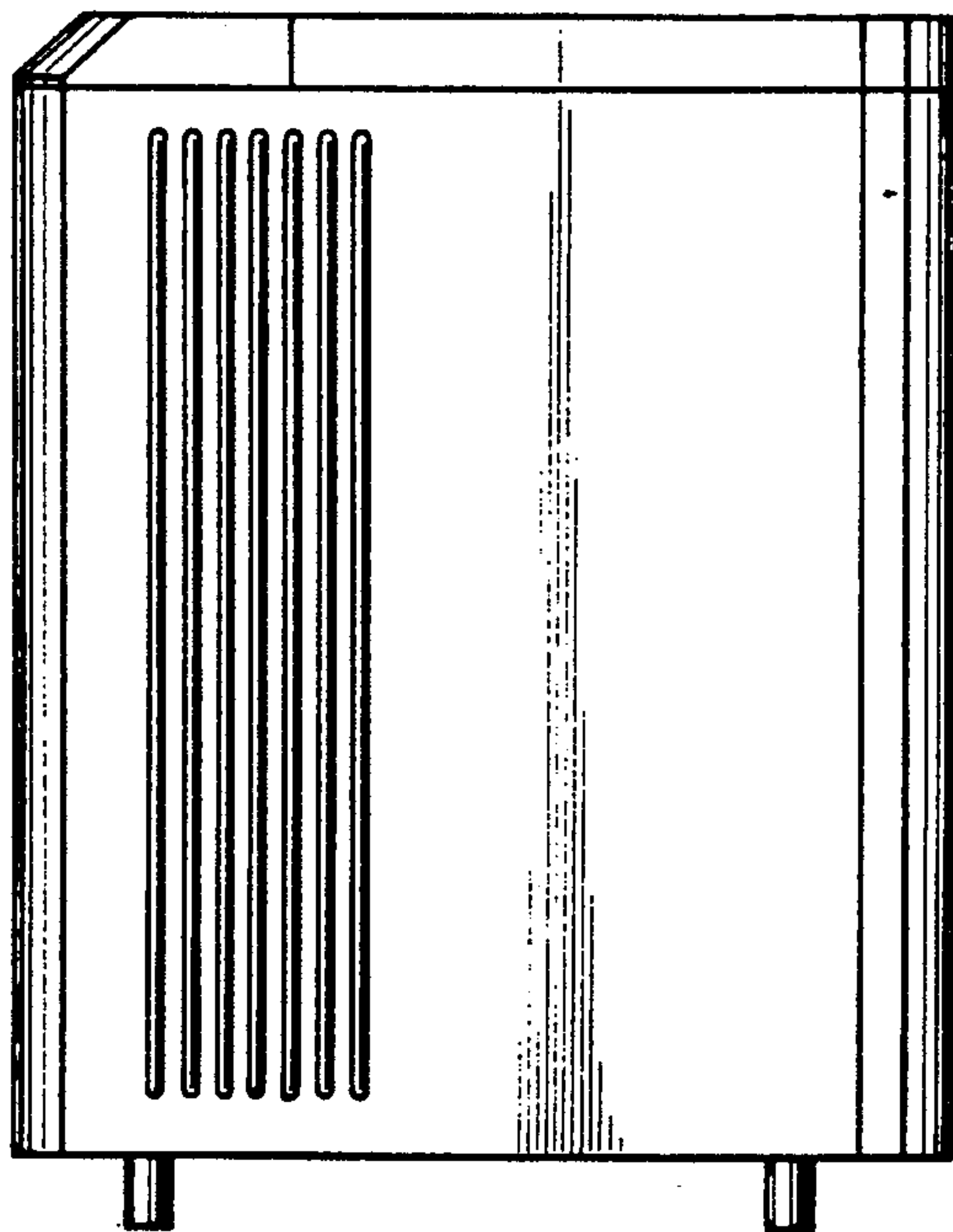


FIG. 7

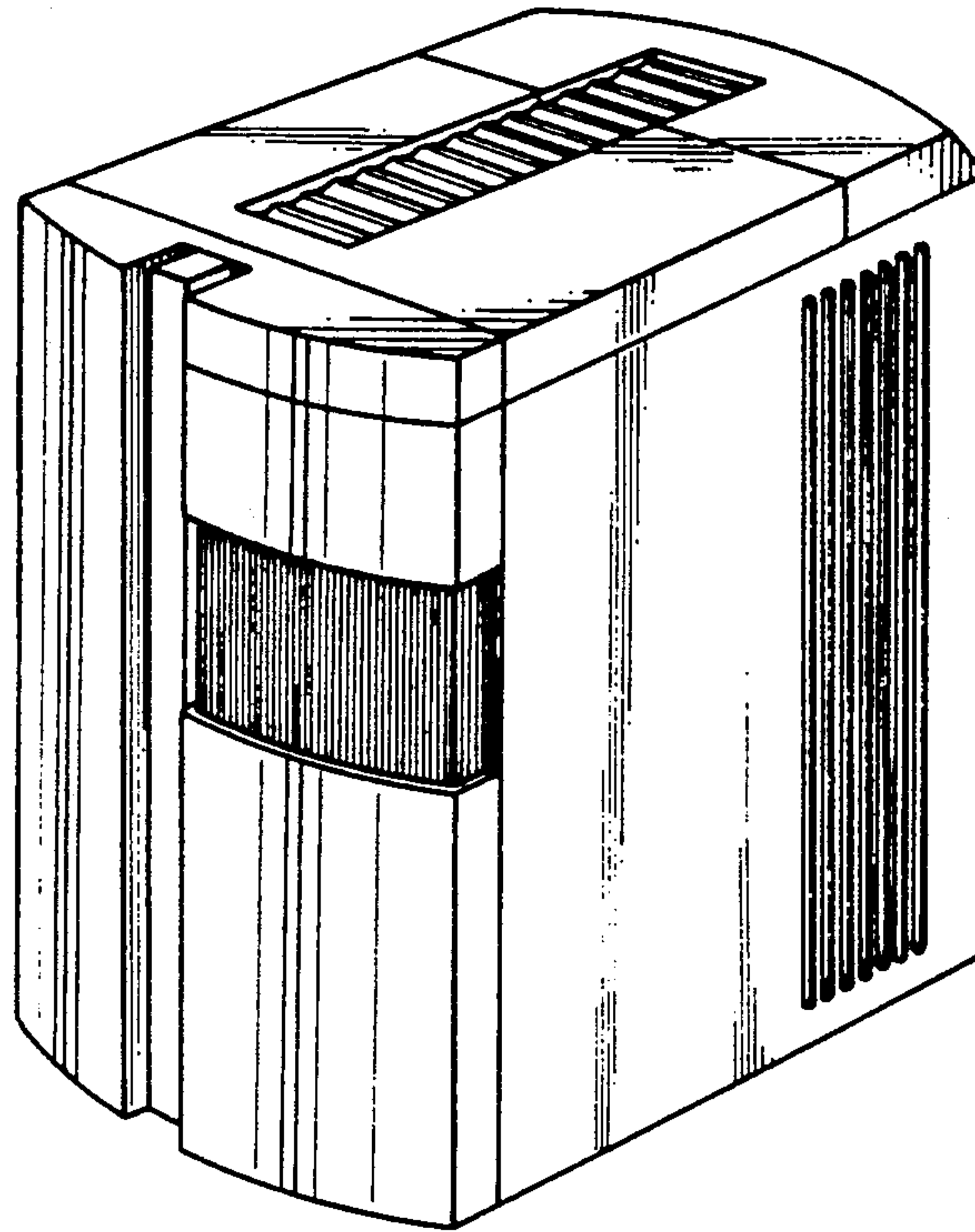


FIG. 8

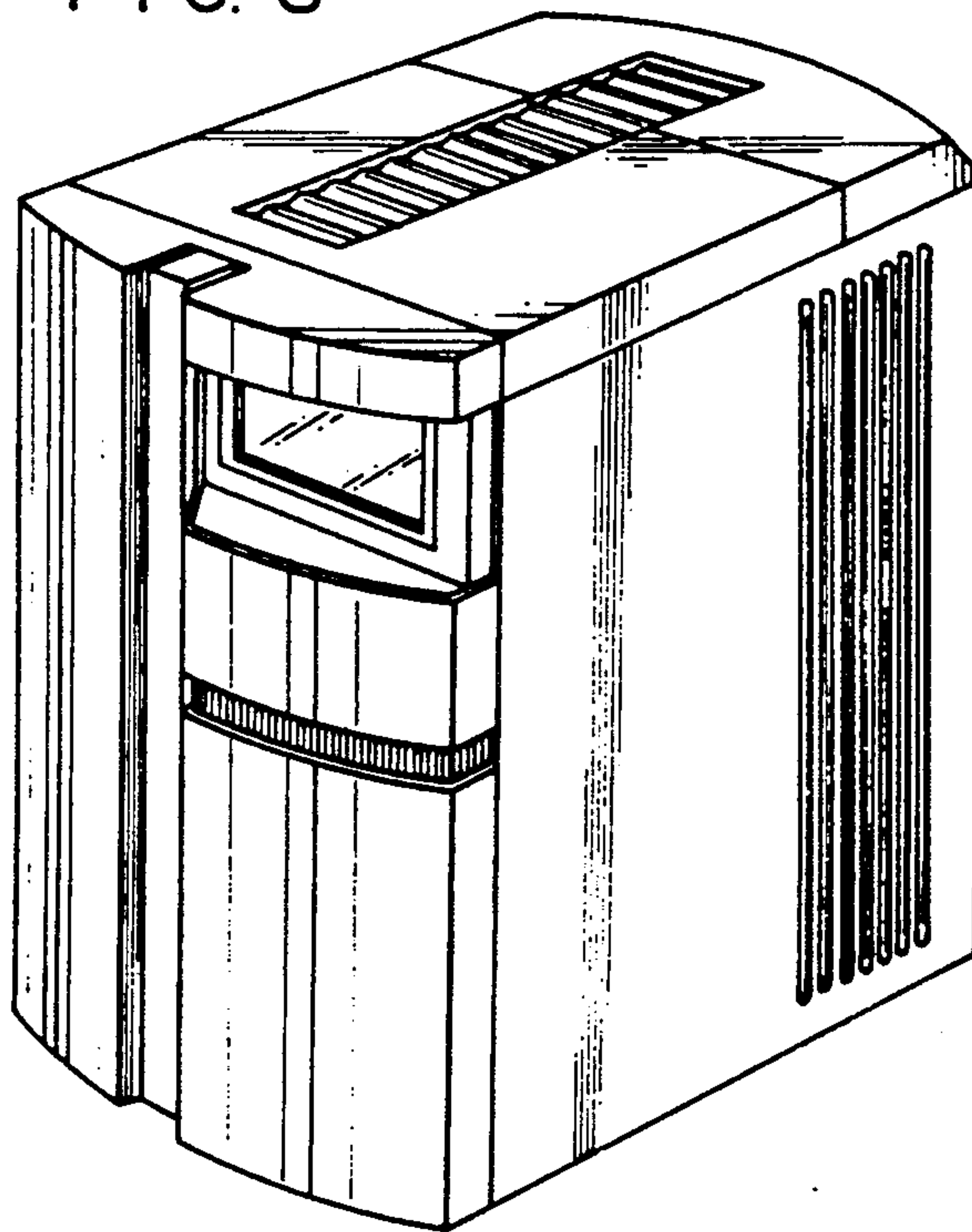


FIG. 9

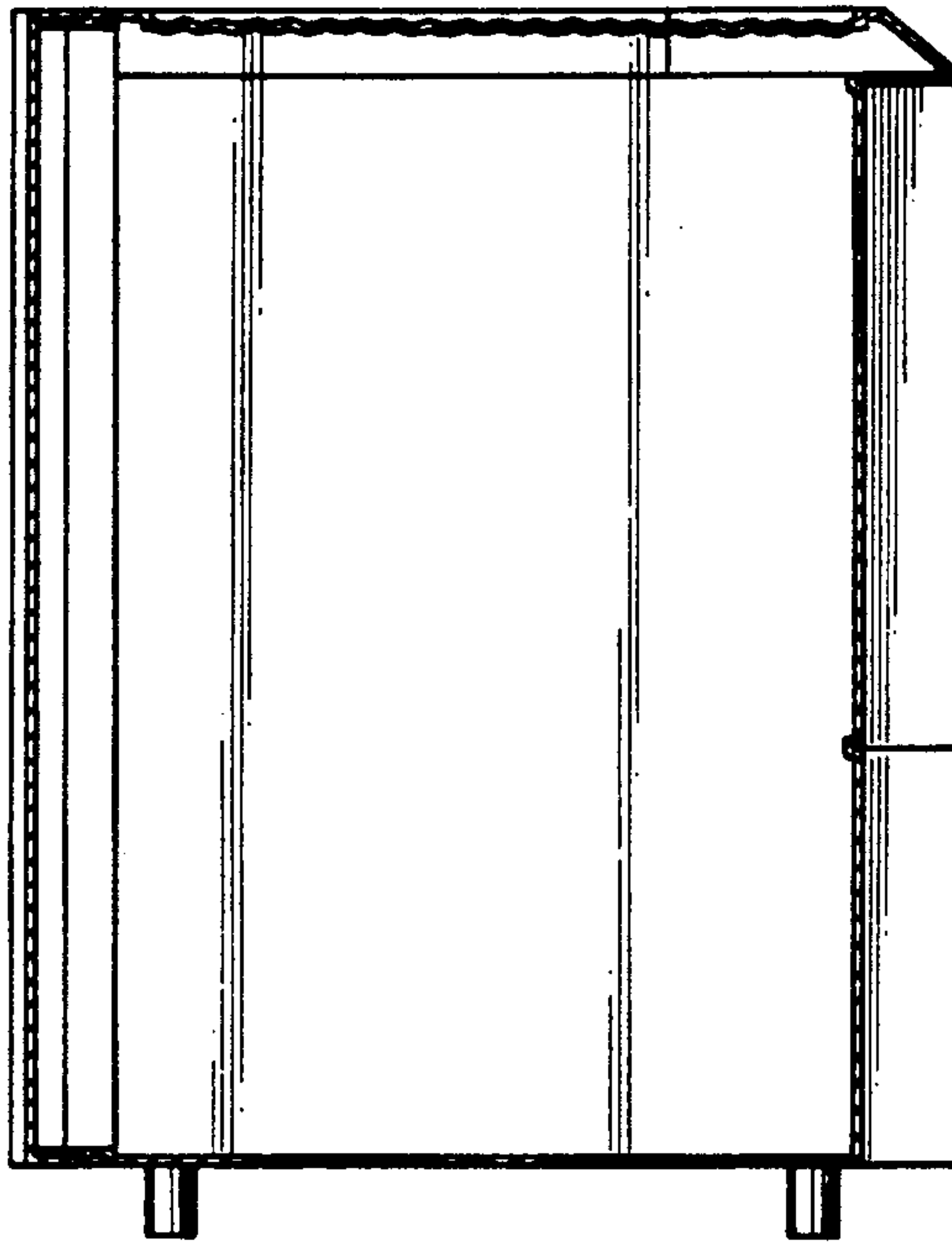


FIG. 10

