

United States Patent [19]

Murata

[11] Patent Number: **Des. 311,141**

[45] Date of Patent: **** Oct. 9, 1990**

[54] **CLOCK**

[75] Inventor: **Ryo Murata, Tachikawa, Japan**

[73] Assignee: **Casio Computer Co., Ltd., Tokyo, Japan**

[**] Term: **14 Years**

[21] Appl. No.: **70,516**

[22] Filed: **Jul. 7, 1987**

[52] U.S. Cl. **D10/15; D10/21**

[58] Field of Search **D10/1-39, D10/122-132; 368/82-84, 10-11, 63, 239-242, 276-277, 285**

[56] **References Cited**

U.S. PATENT DOCUMENTS

- D. 234,578 3/1975 Powers D10/15
- D. 245,765 9/1977 Yoshida D10/15
- D. 246,351 11/1977 Yoshida D10/15

- D. 263,124 2/1982 Dennis D10/15
- D. 264,944 6/1982 Palmer D10/15
- D. 267,319 12/1982 Van DeVen D10/15
- D. 304,018 10/1989 Nomizu D10/26

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[57] **CLAIM**

The ornamental design for a clock, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of a clock showing my new design;
 FIG. 2 is a top plan view thereof;
 FIG. 3 is a right side elevational view thereof;
 FIG. 4 is a left side elevational view thereof;
 FIG. 5 is a bottom plan view thereof;
 FIG. 6 is a rear elevational view thereof; and
 FIG. 7 is a perspective view thereof.

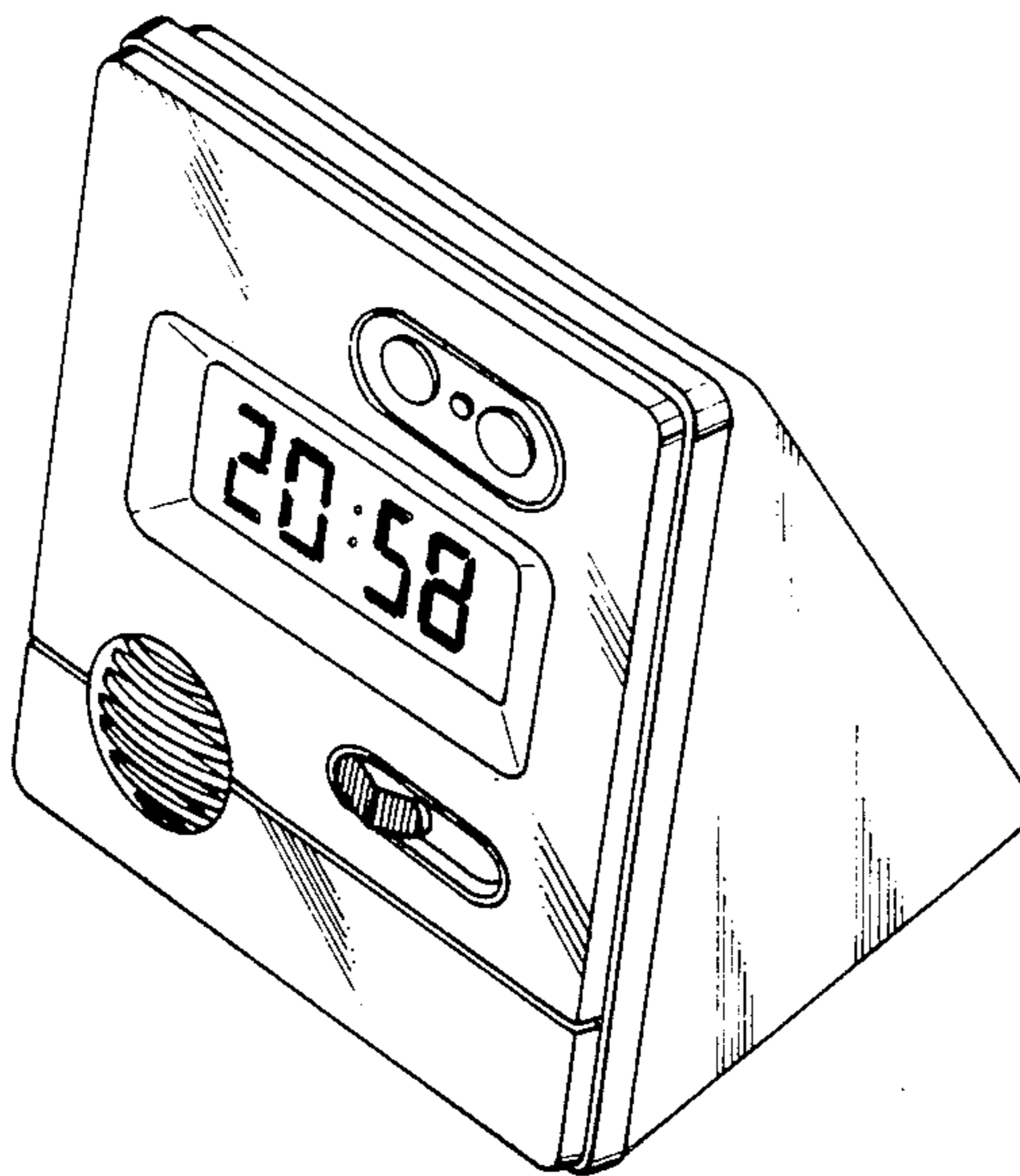


FIG. 1

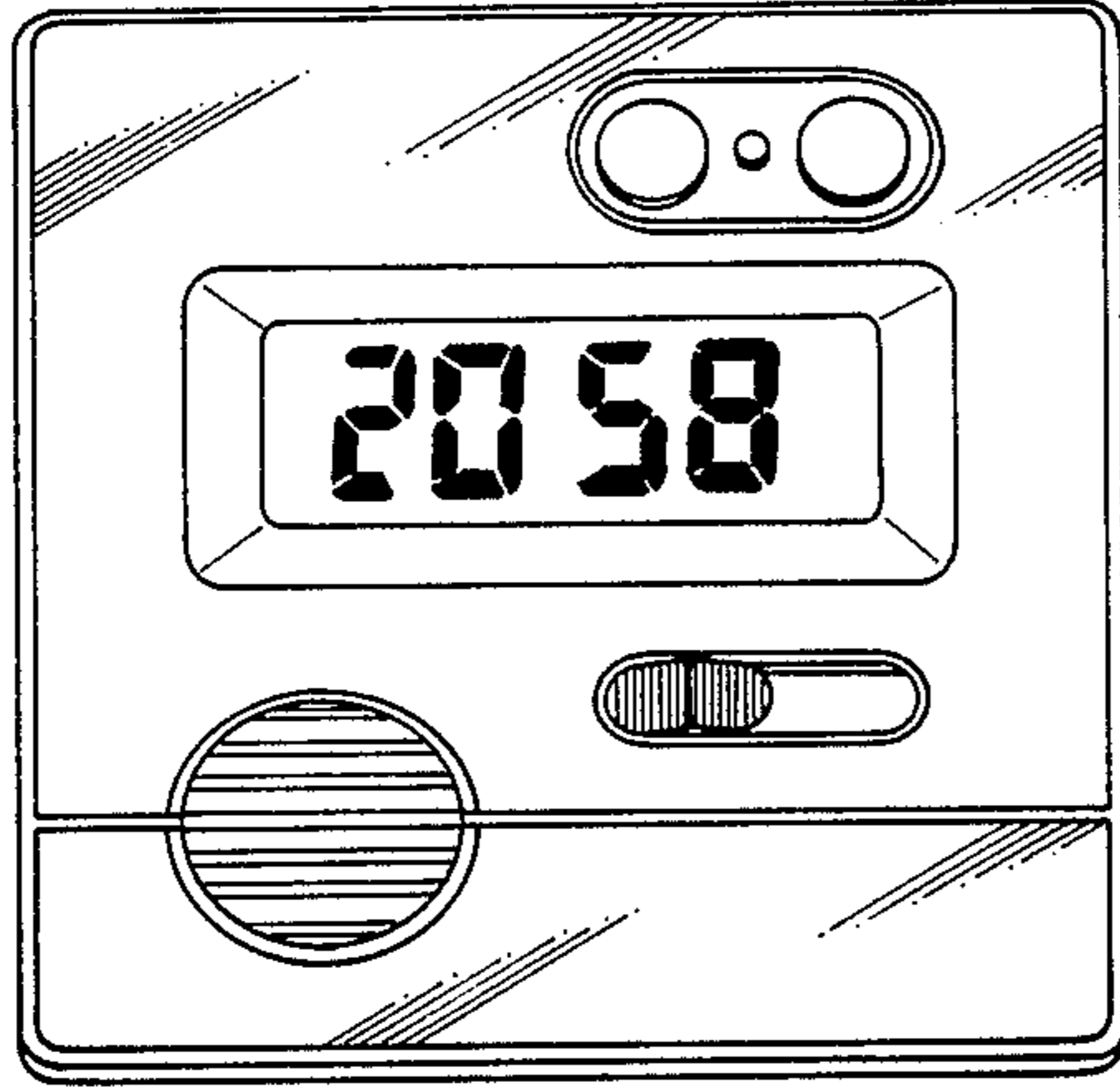


FIG. 3

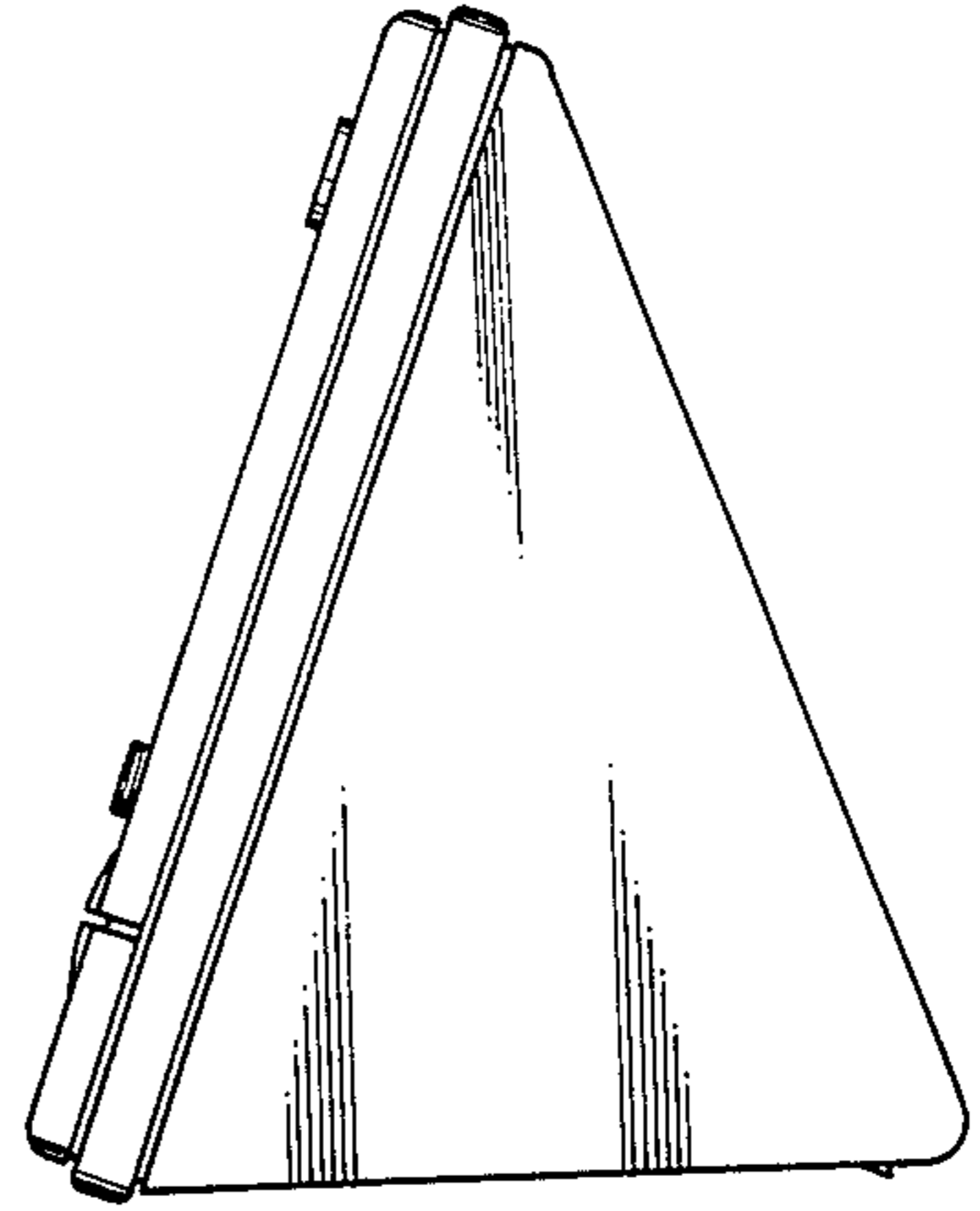


FIG. 2

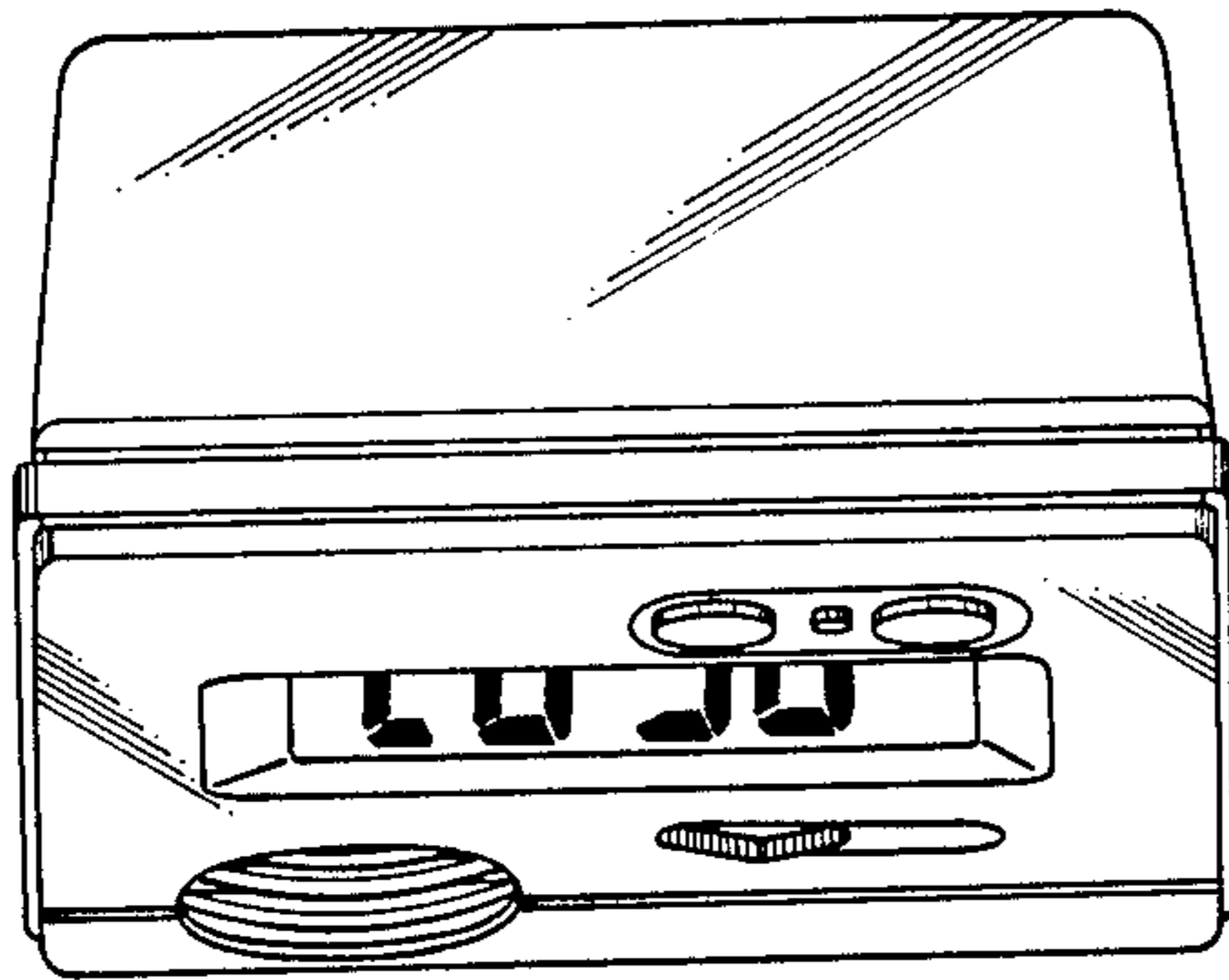


FIG. 4

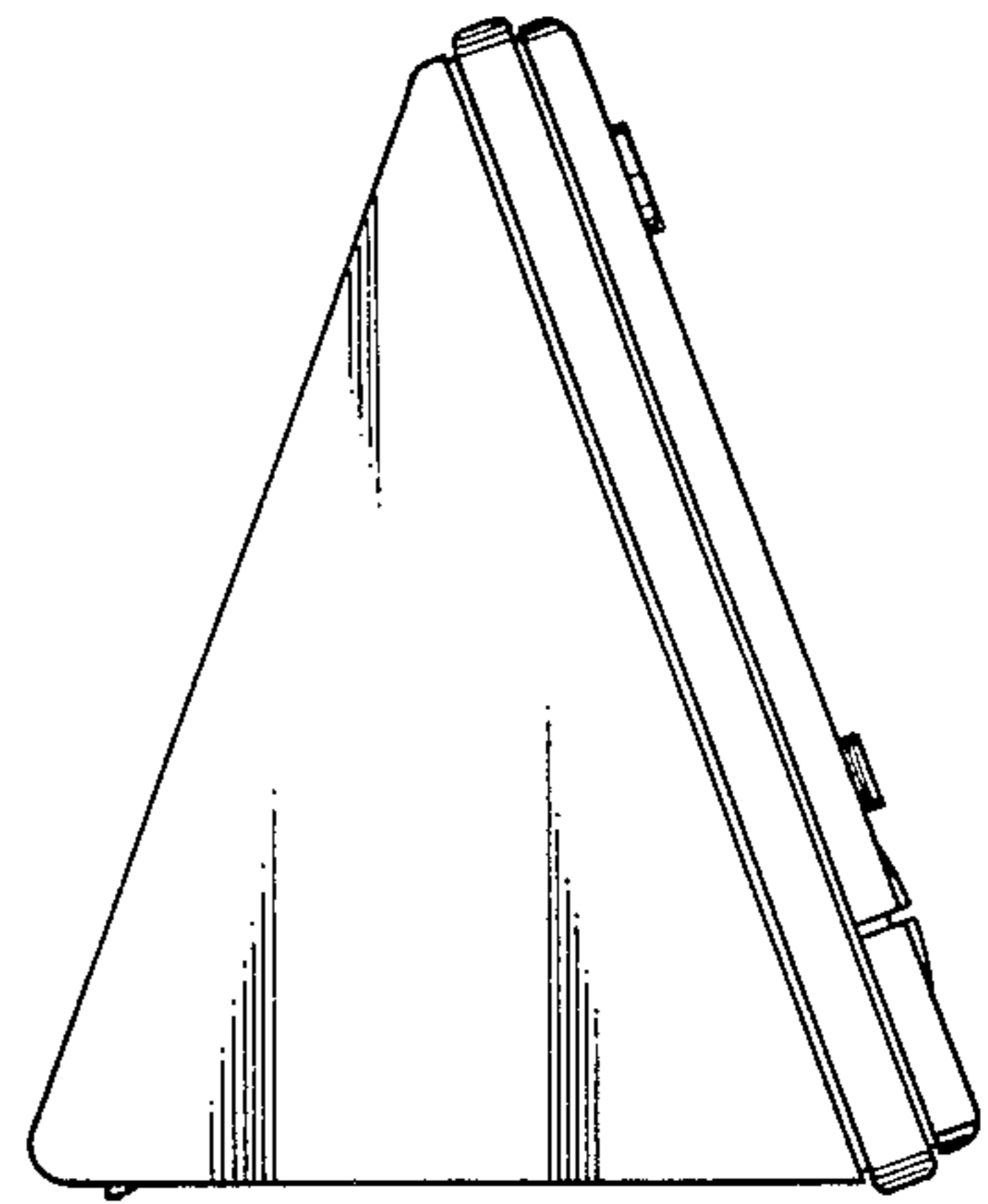


FIG. 5

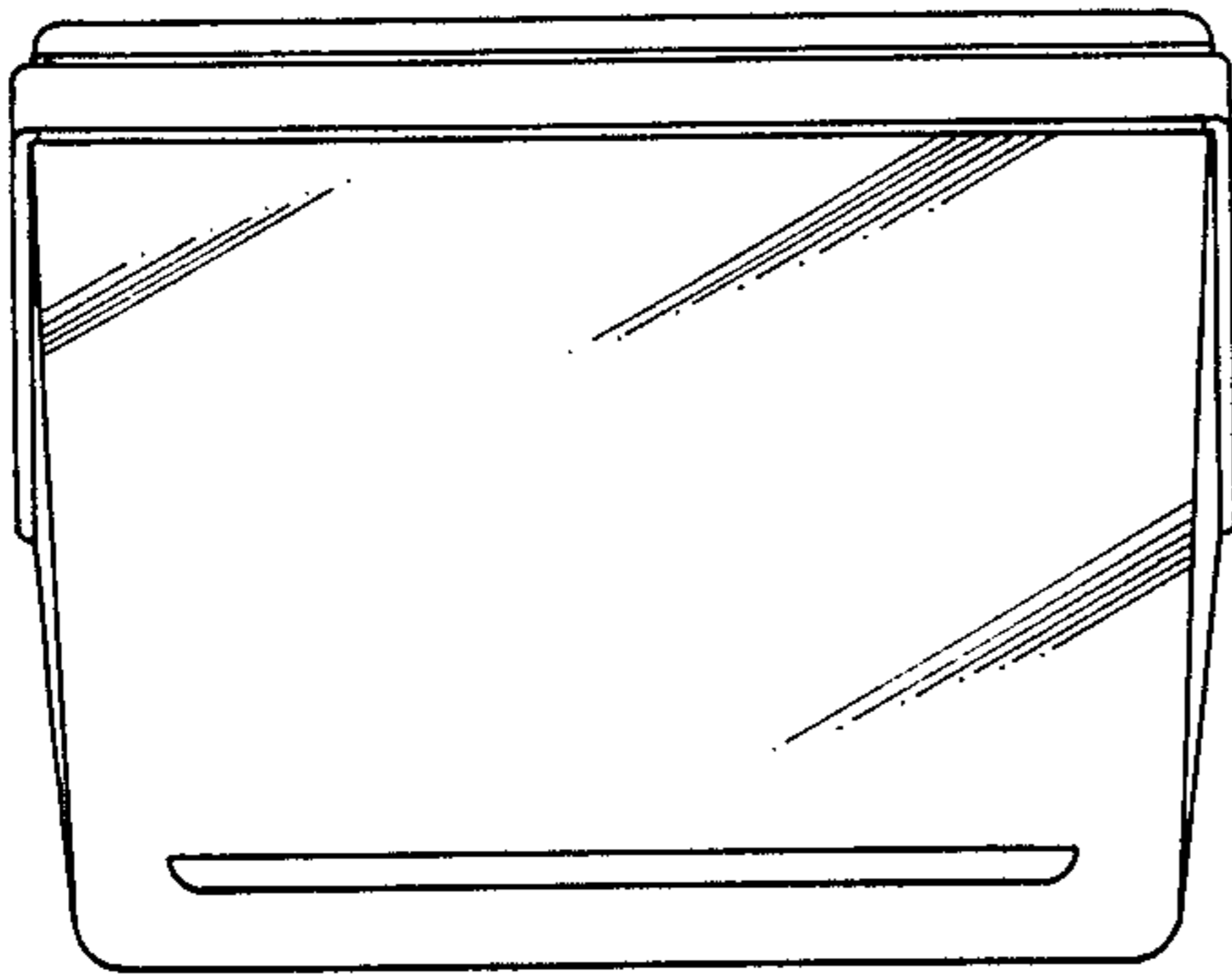


FIG. 6

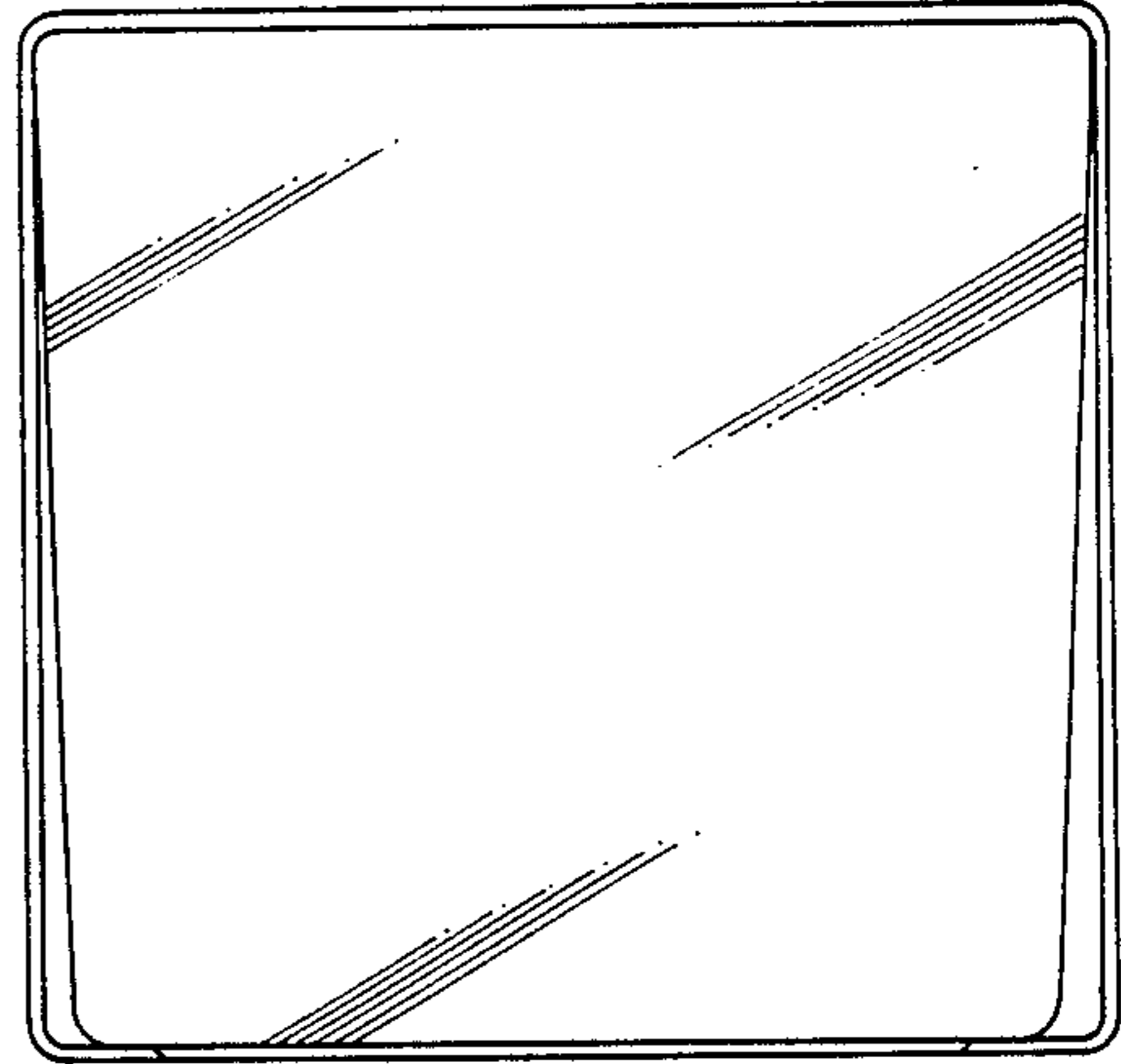


FIG. 7

