

[54] ELECTRONIC CIRCUIT BREAKER PROGRAMMER TESTER

[75] Inventor: Thomas D. Burton, Unionville, Conn.

[73] Assignee: General Electric Company, New York, N.Y.

[**] Term: 14 Years

[21] Appl. No.: 79,601

[22] Filed: Jul. 30, 1987

[52] U.S. Cl. D10/78

[58] Field of Search D10/75, 78, 80; D13/34; 324/72, 72.5, 156, 158 R, 158 P, 158 F, 500, 512, 519-542, 555, 556; 335/6; 340/638; 361/96, 97, 93, 94

[56] References Cited

U.S. PATENT DOCUMENTS

- D. 134,520 12/1942 Cushing D10/75
- D. 202,556 10/1965 Seabury, Jr. D10/75
- D. 205,906 10/1966 Stevenson D10/78 X
- D. 264,192 4/1982 Greenlee et al. D10/78 X
- D. 264,193 4/1982 Greenlee et al. D10/78 X

- 2,509,415 5/1950 Bernreuter 324/156
- 2,792,549 5/1957 Bernreuter 324/156
- 2,819,448 1/1958 Neeper 324/156
- 2,901,704 8/1959 Lamb 324/156
- 4,105,965 8/1978 Russell 324/424
- 4,128,804 12/1978 Russell 324/424
- 4,476,721 10/1984 Hochreuther et al. D10/78 X
- 4,814,712 3/1989 Burton et al. 324/424

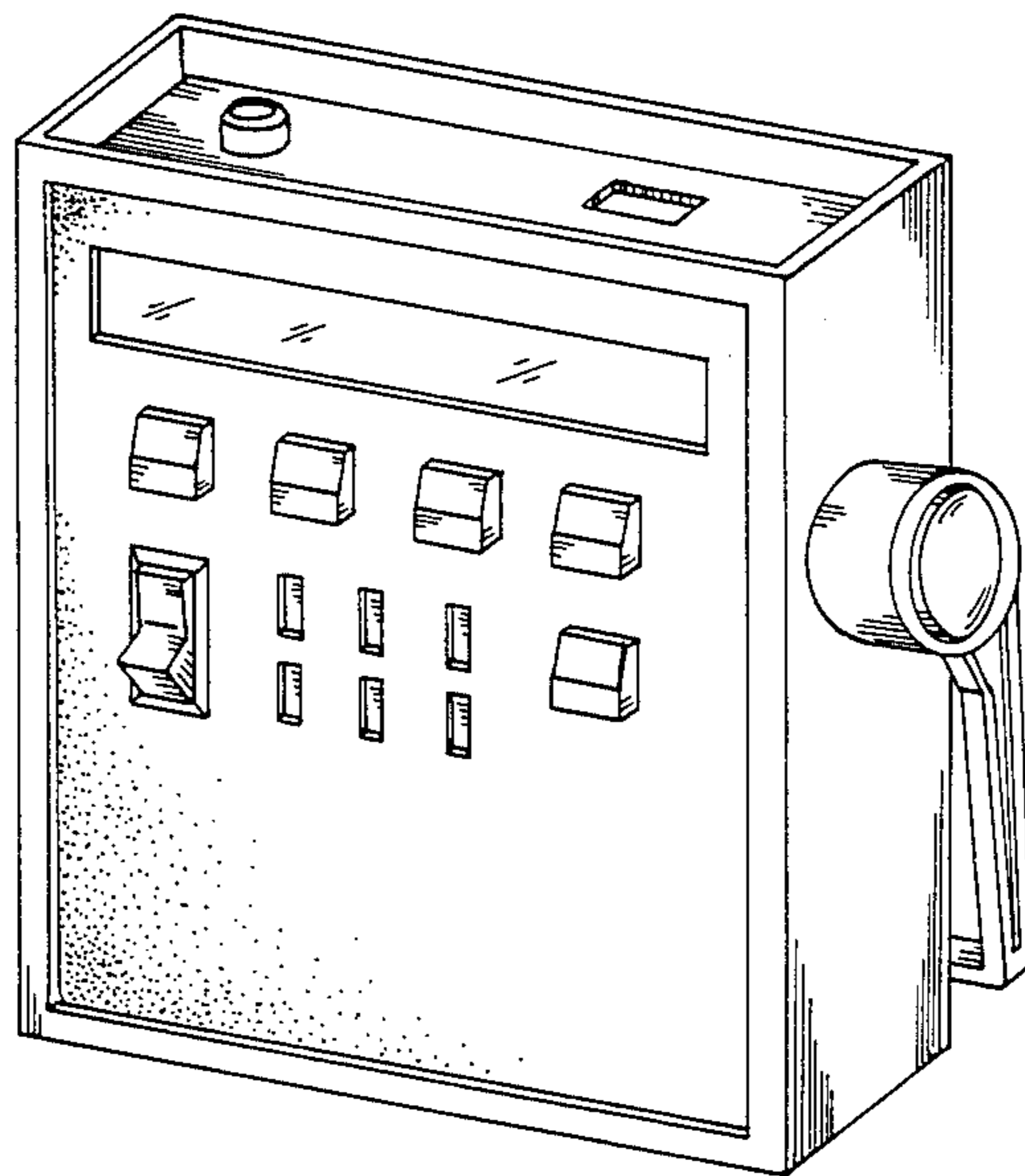
Primary Examiner—Nelson C. Holtje
Assistant Examiner—Antoine D. Davis
Attorney, Agent, or Firm—Richard A. Menelly; Walter C. Bernkopf; Fred Jacob

[57] CLAIM

The ornamental design for an electronic circuit breaker programmer tester, as shown and described.

DESCRIPTION

FIG. 1 is a top, front, and right side perspective view of an electronic circuit breaker programmer tester showing my new design;
FIG. 2 is a top plan view;
FIG. 3 is a bottom plan view;
FIG. 4 is a rear elevational view; and
FIG. 5 is a left side elevational view thereof.



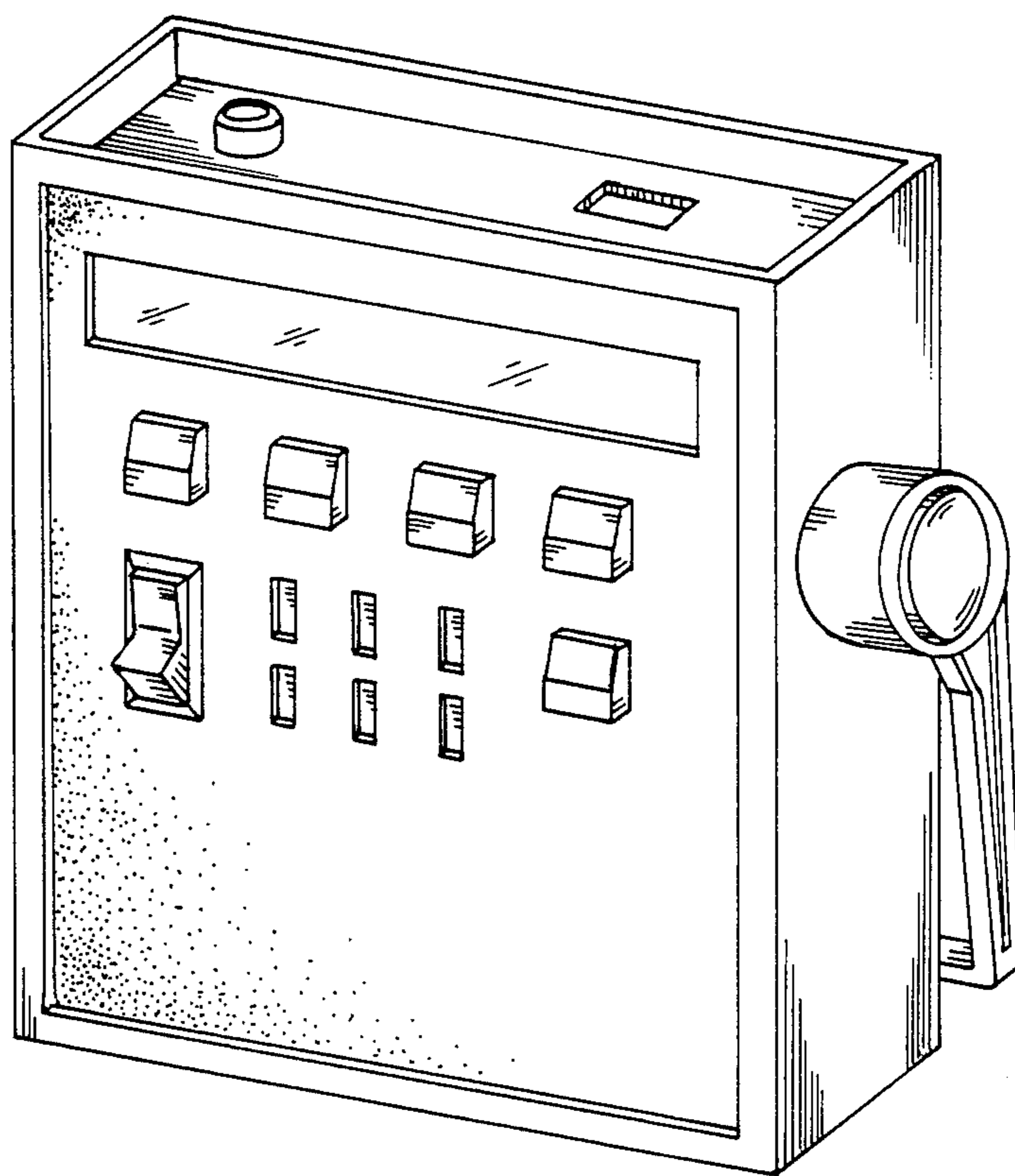


FIG. 1

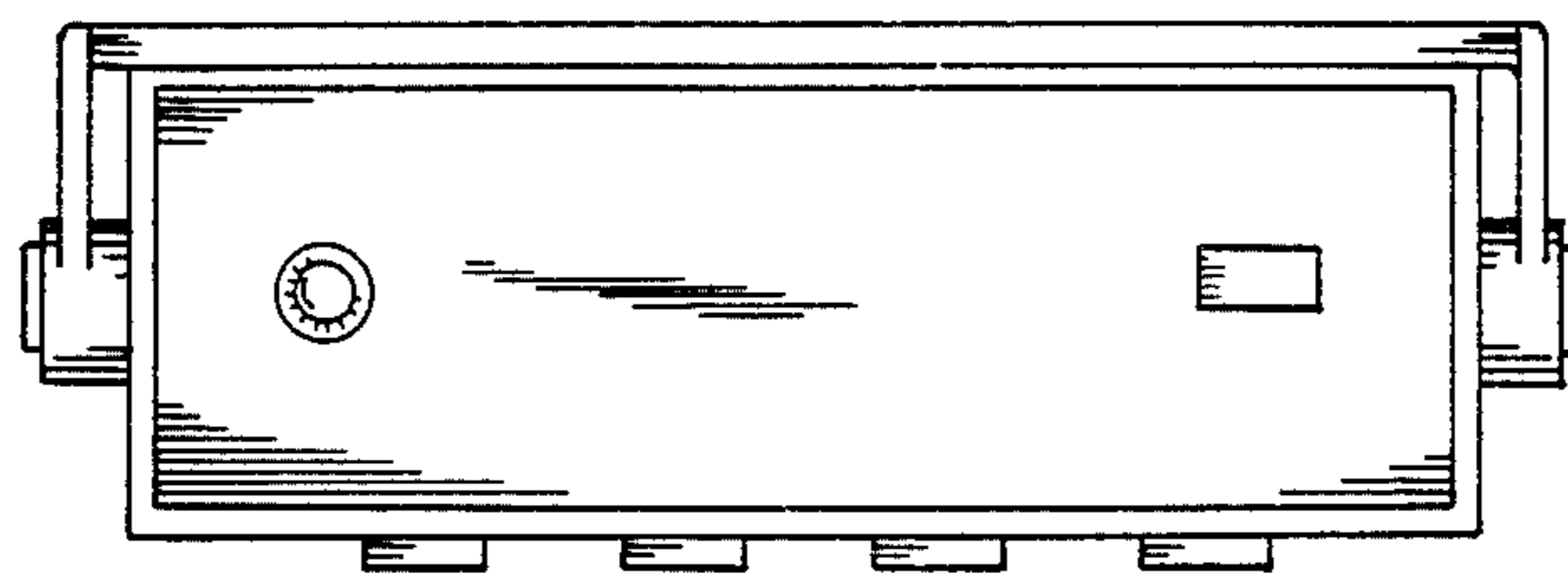


FIG. 2

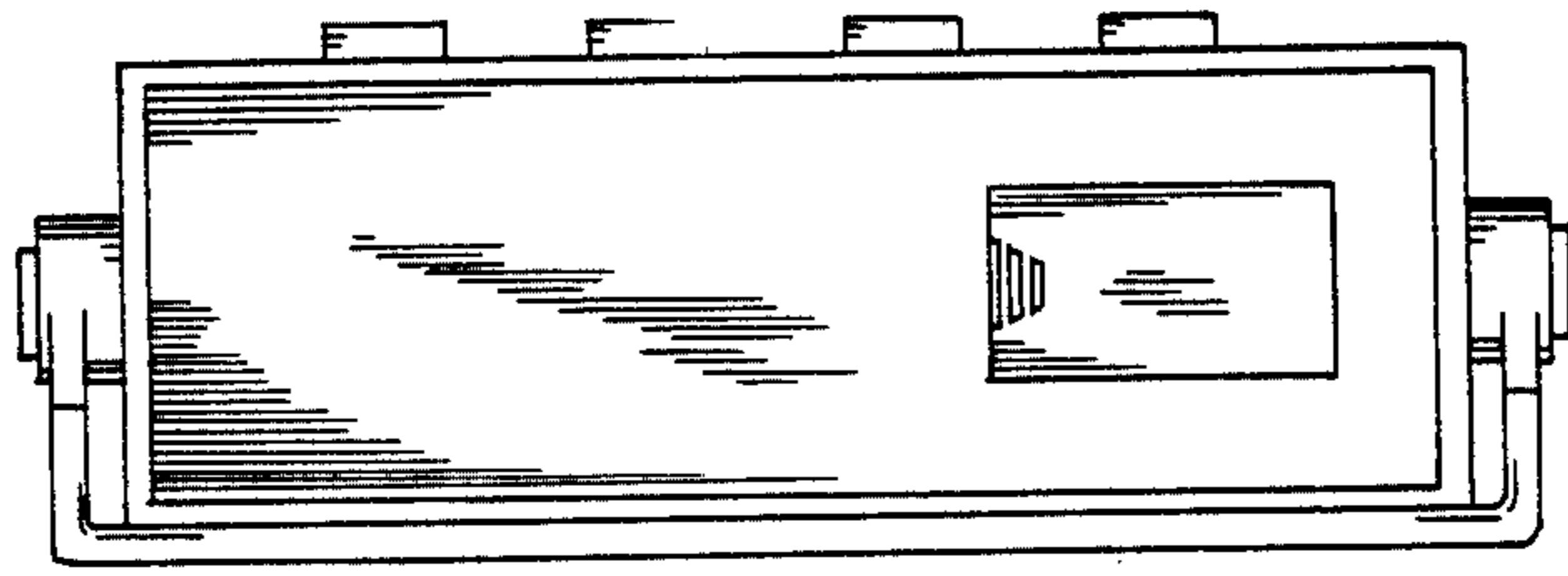


FIG. 3

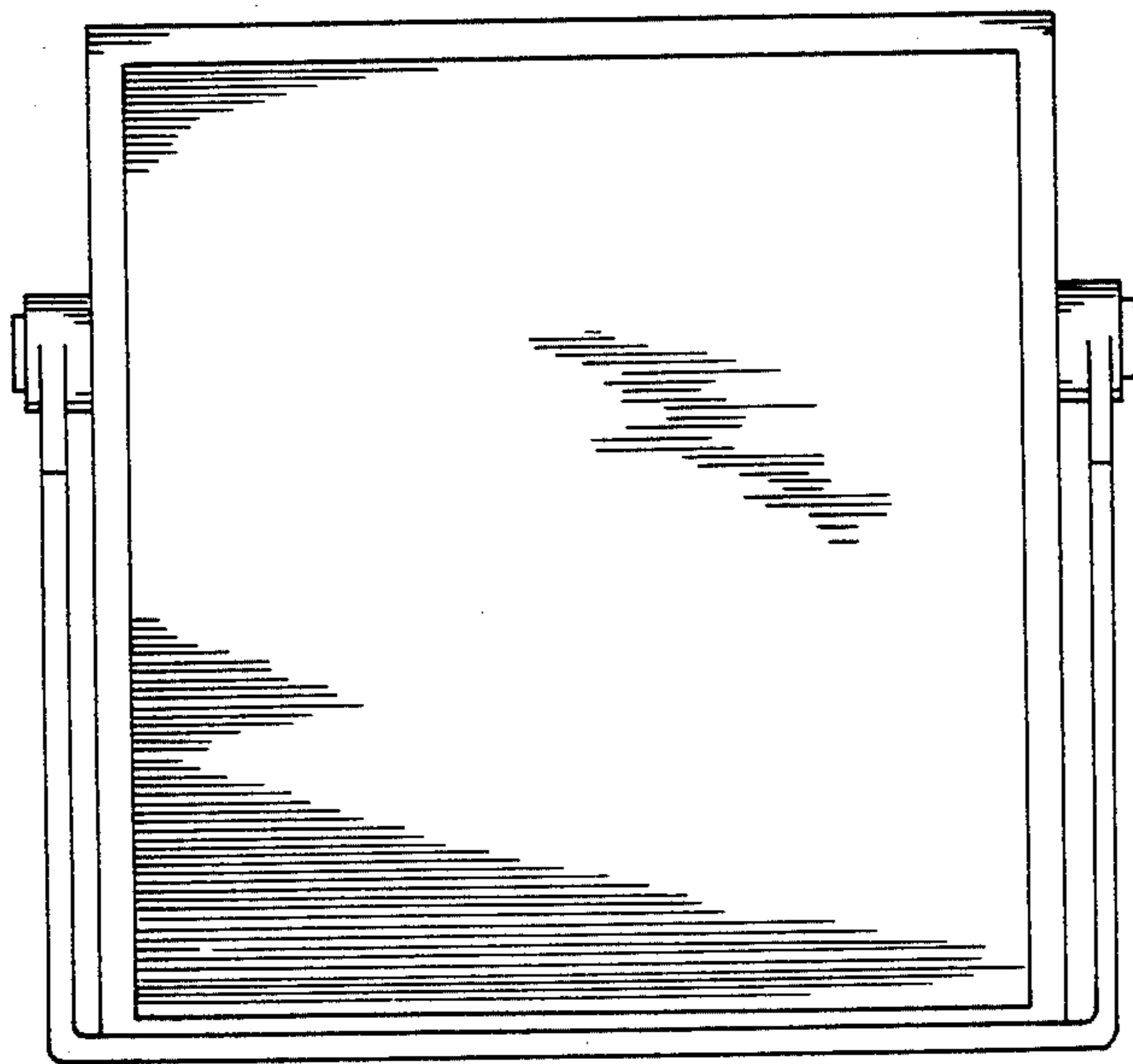


FIG. 4

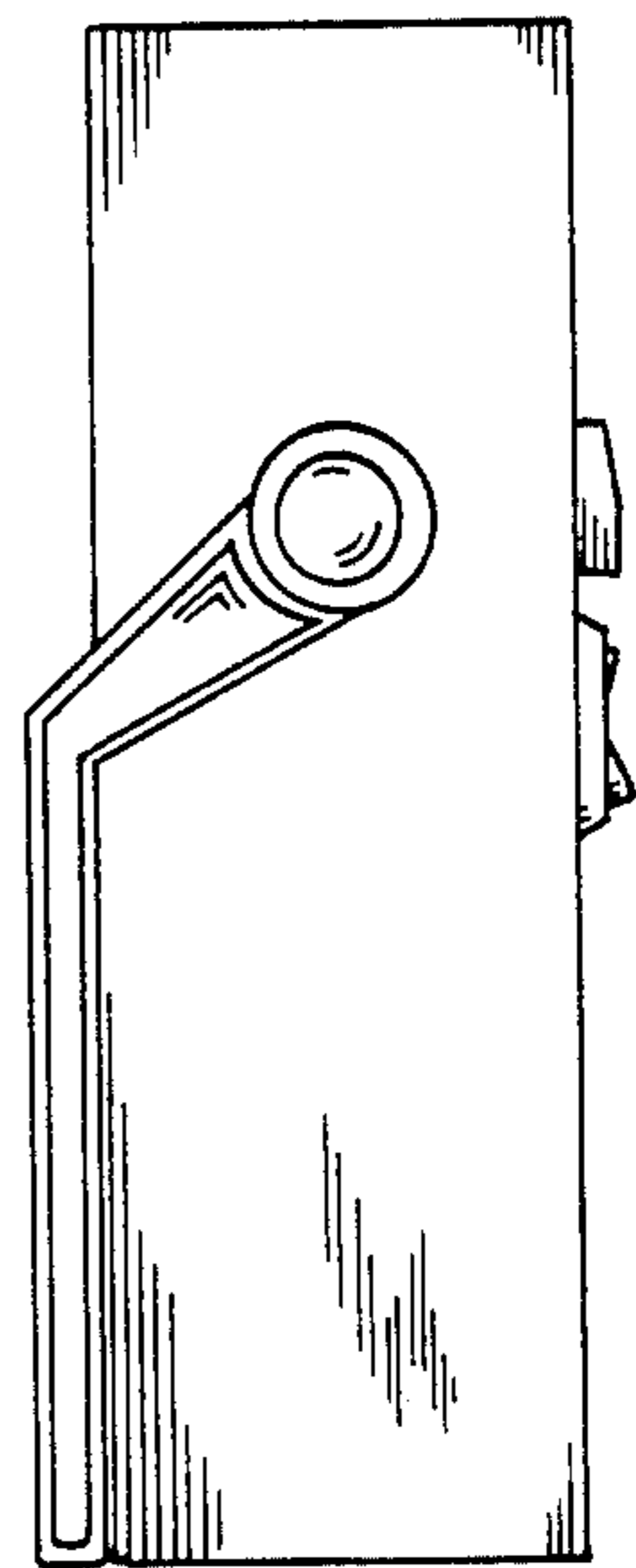


FIG. 5