United States Patent [19]

Yoshida et al.

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[54]	CENTRAL PROCESSOR FOR AN ELECTRONIC COMPUTER			
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[**]	Term:	14 Years		
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[30]	Foreign Application Priority Data			
Sep. 4, 1987 [JP] Japan				
				
[58]		arch		
	•	, 41; 174/50, 52 R; 360/97–99, 132, 133;		
		/330, 331, 334, 335, 390–395, 399, 415;		
		200/5 A, 5 R, 6 A, 6 R; 312/198, 208		

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[57] CLAIM

The ornamental design for a central processor for an electronic computer, as shown.

DESCRIPTION

FIG. 1 is a front, top and left side perspective view of a central processor for an electronic computer showing our new design;

FIG. 2 is a top plan view thereof;

FIG. 3 is a front elevational view thereof;

FIG. 4 is a bottom plan view thereof;

FIG. 5 is a right side elevational view thereof; and

FIG. 6 is a rear elevational view thereof.

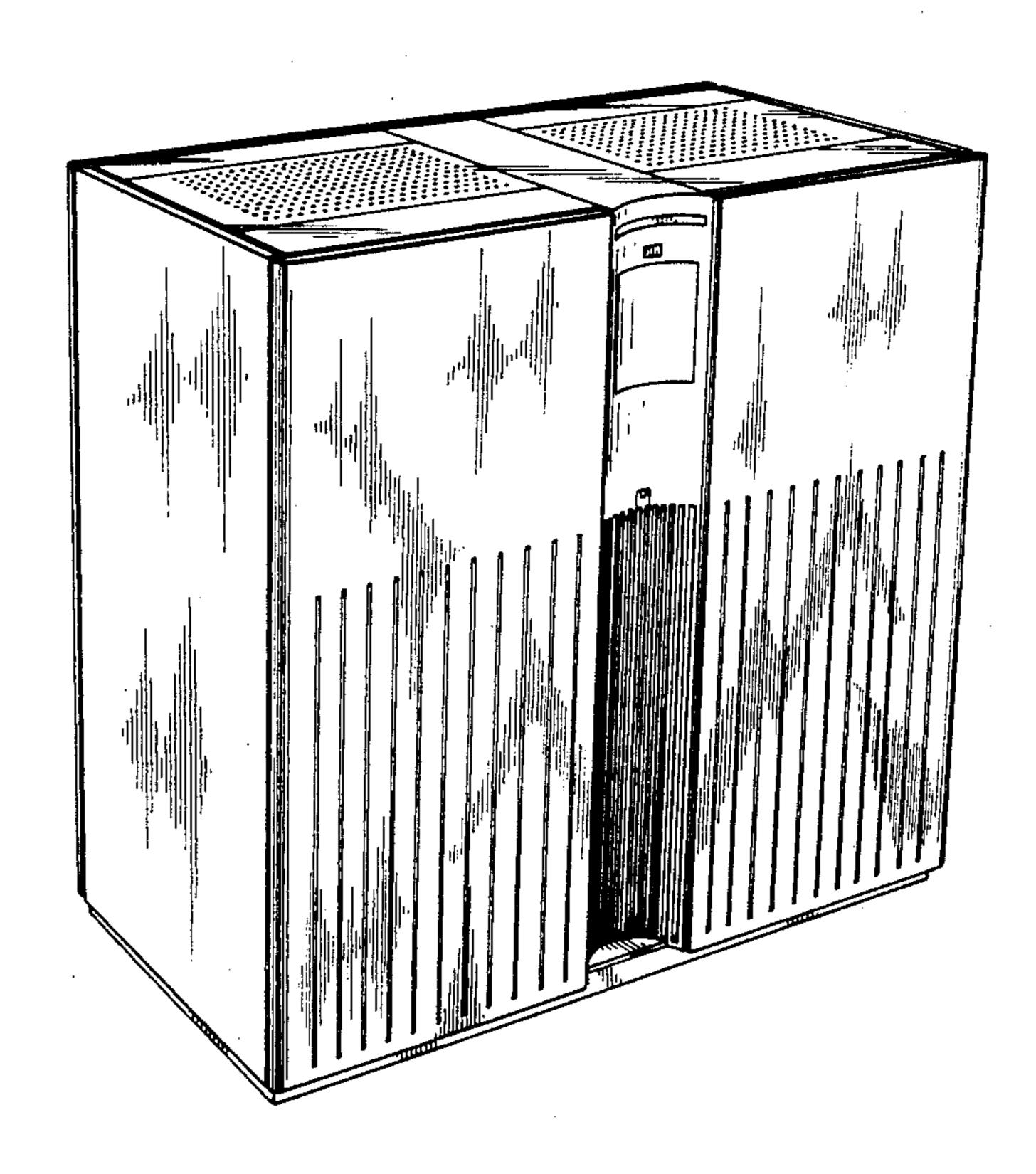


FIG. 1

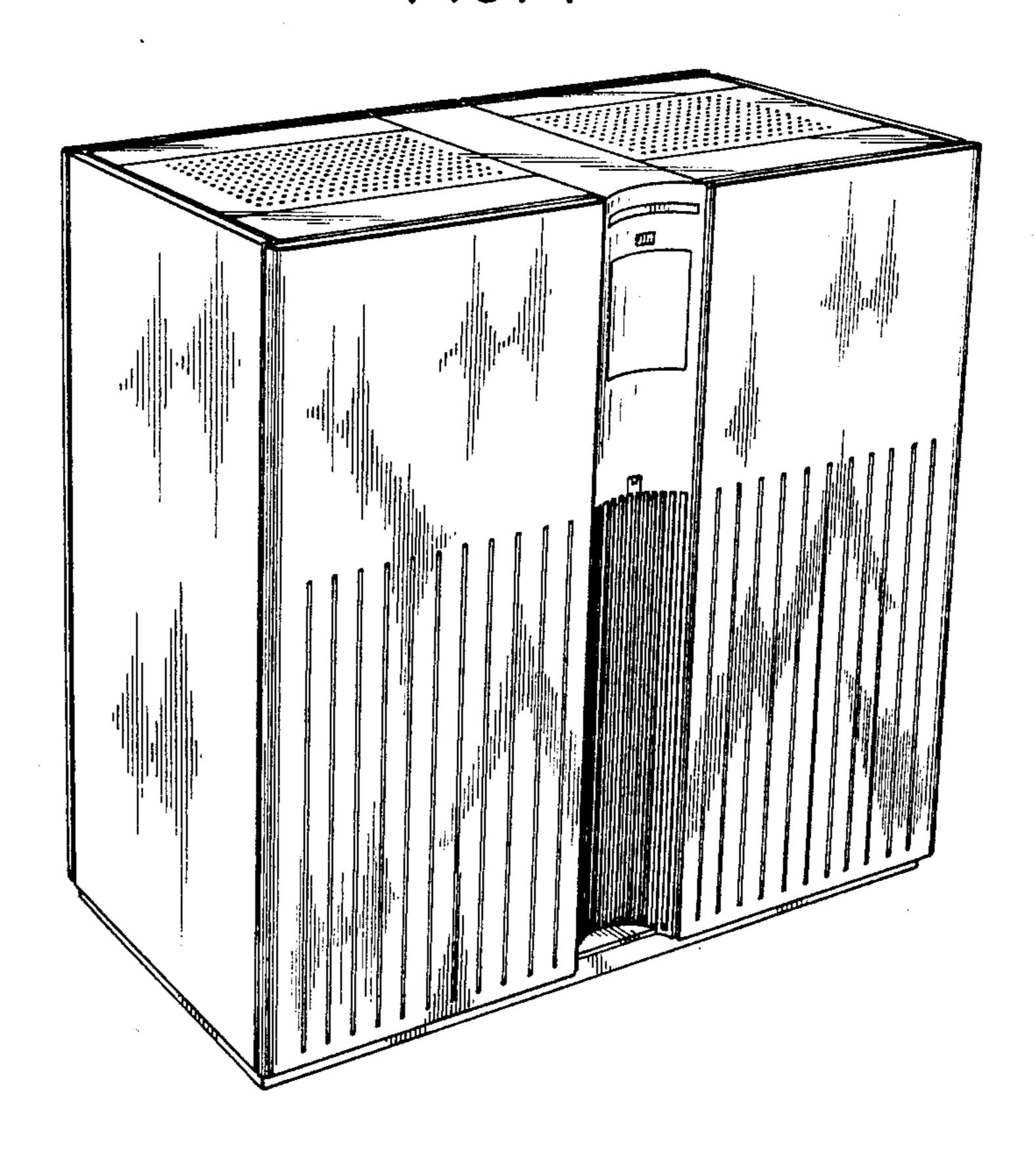


FIG. 2

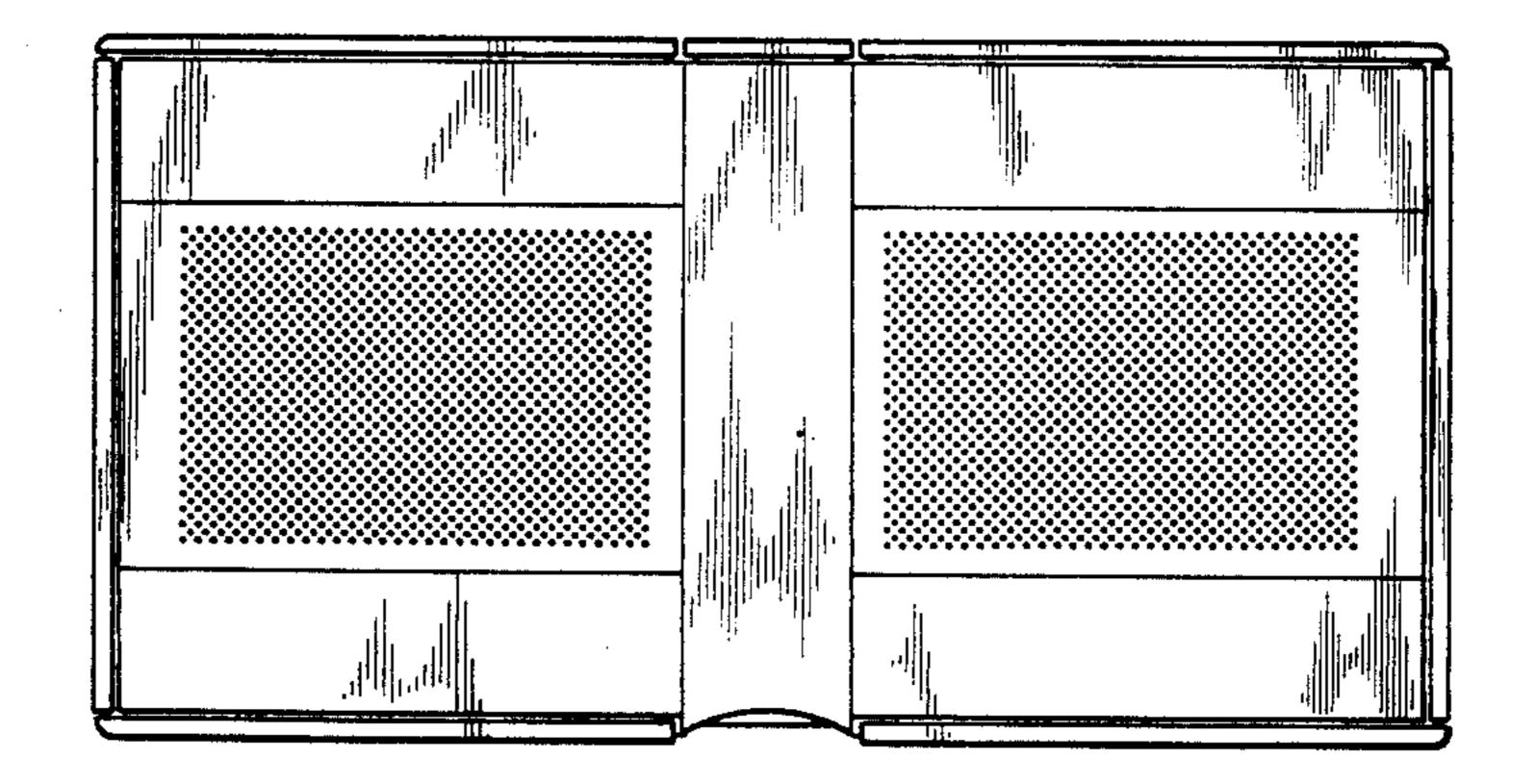


FIG. 3

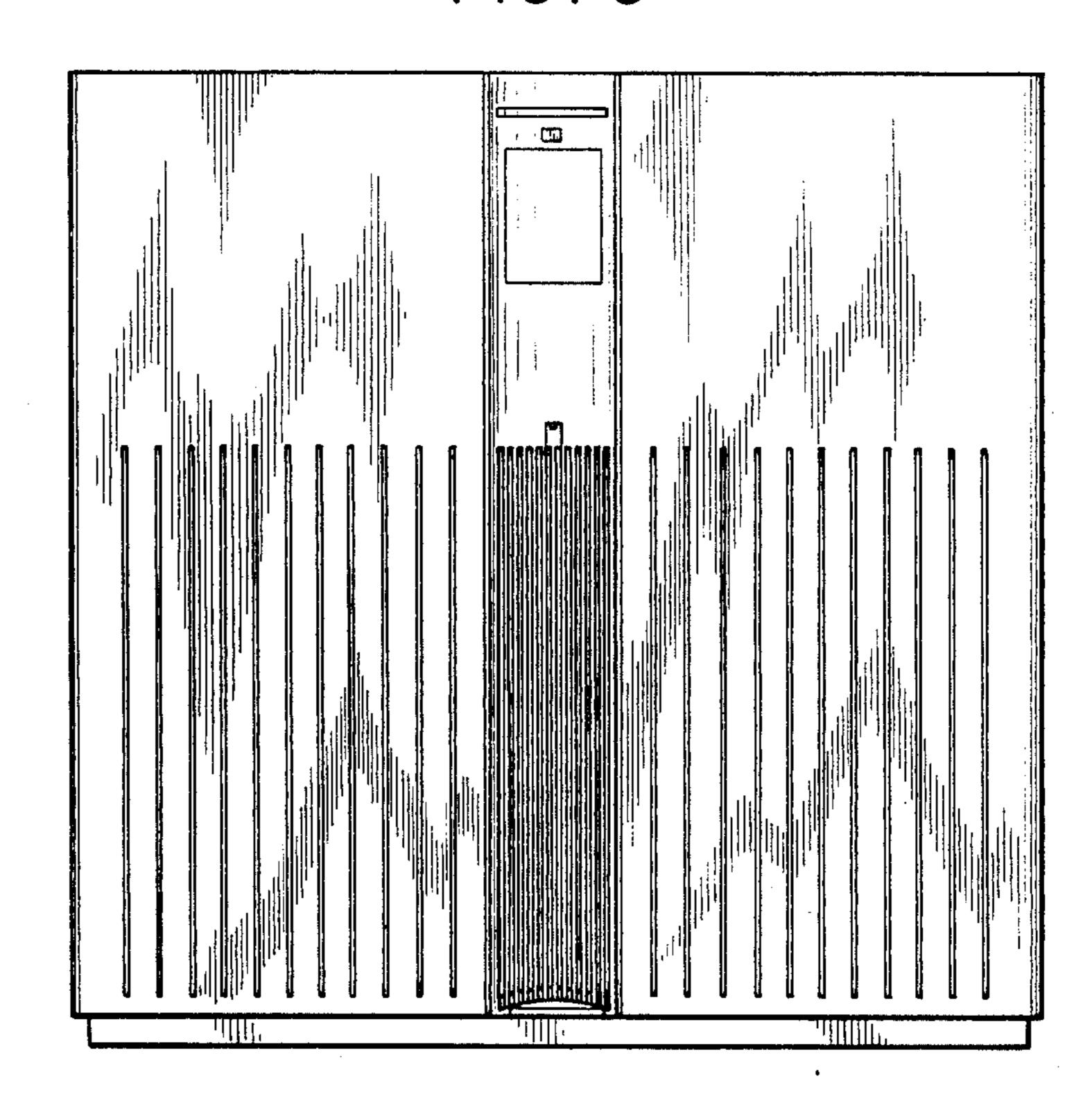
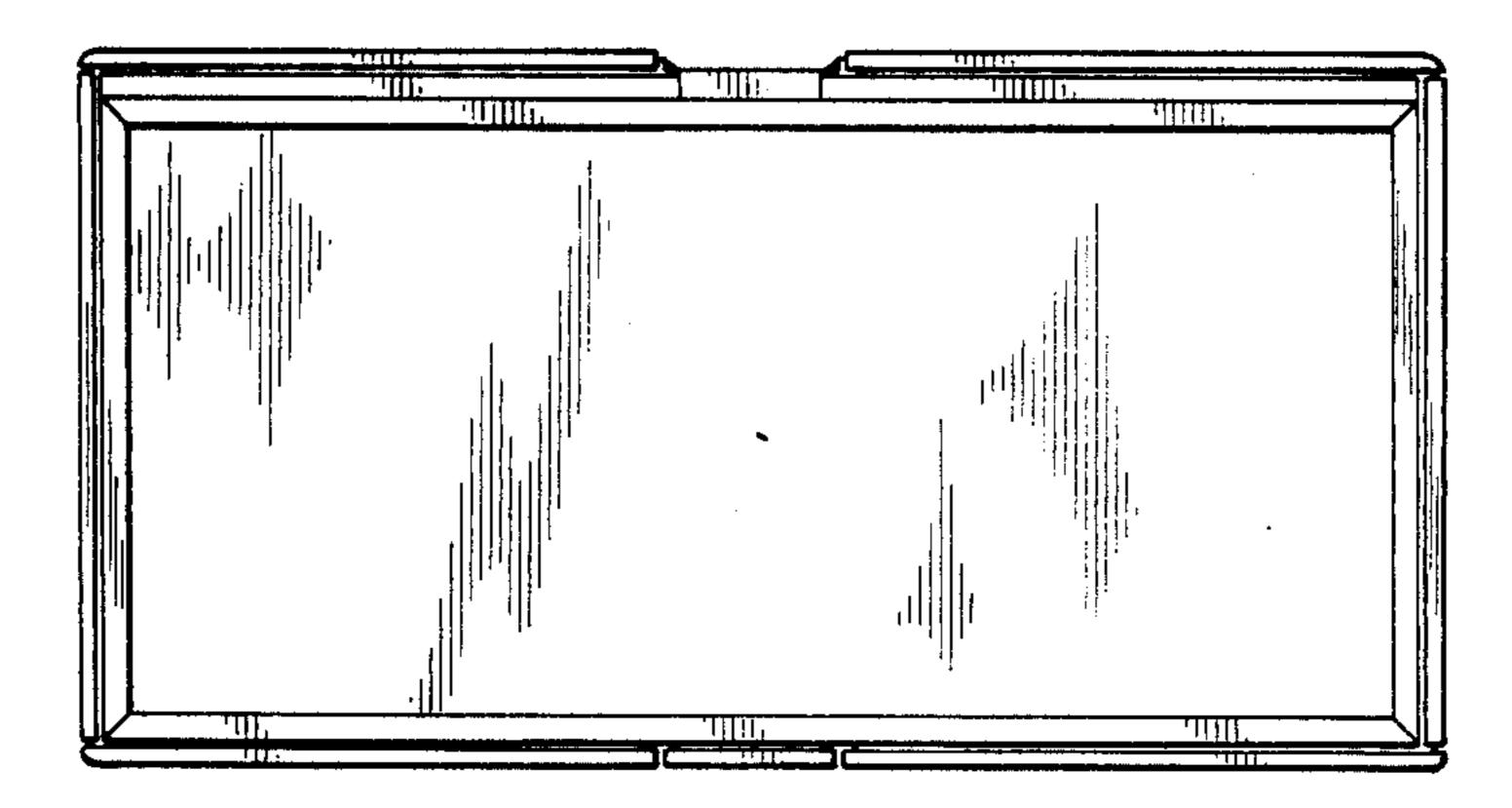
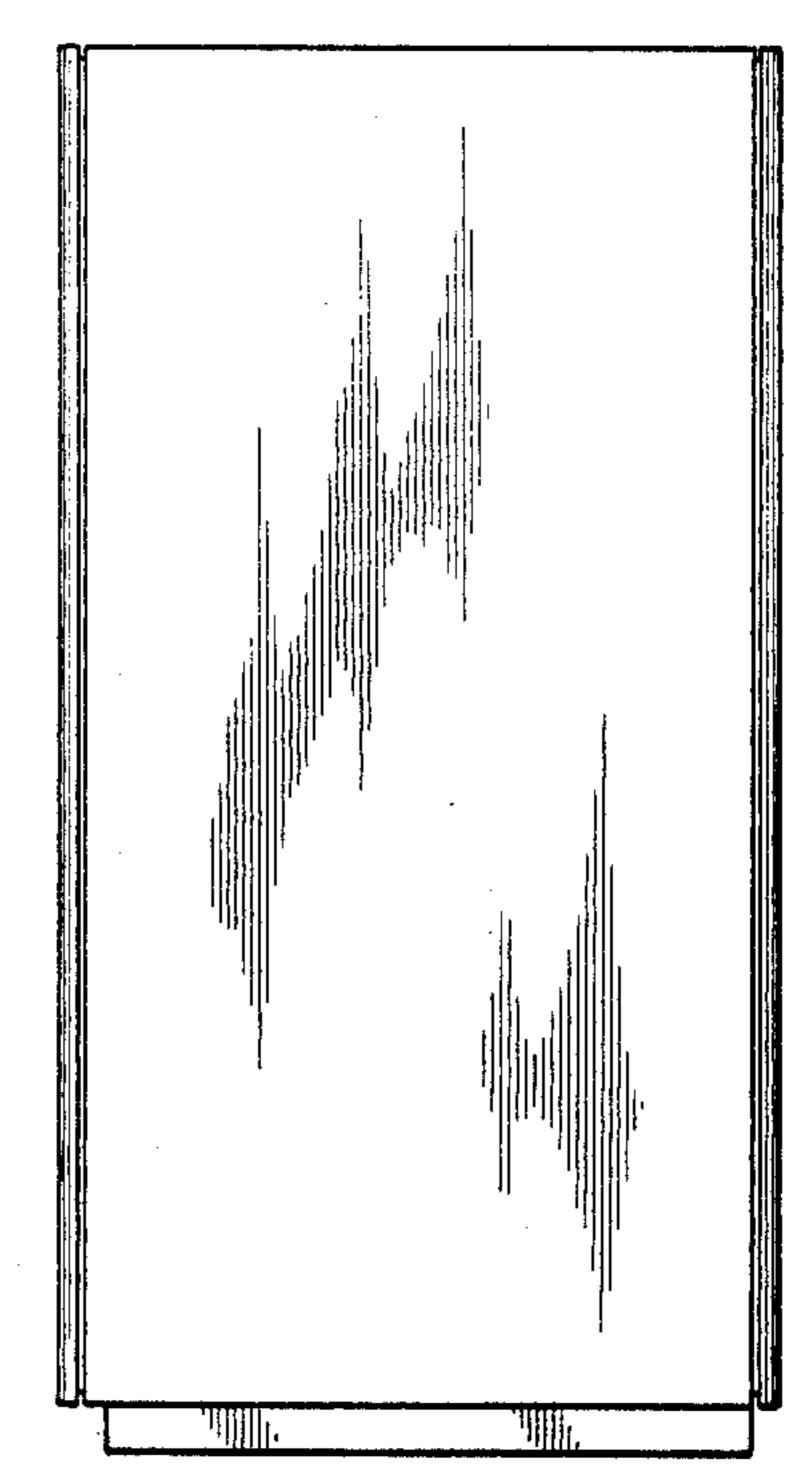


FIG. 4



F/G. 5



F1G. 6

