

[54] **ELECTRONIC CIRCUIT SETUP STATION**

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[*] **Notice:** The portion of the term of this patent subsequent to Dec. 5, 2003 has been disclaimed.

[**] **Term: 14 Years**

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[52] **U.S. Cl. D13/164**

[58] **Field of Search D13/11, 12, 19, 32, D13/35, 40, 41, 99; D14/113; D15/100; D18/22; D24/8; D6/443, 447; 315/295; 364/513; 310/89; 361/42; 118/671; 232/24, 31**

[56]

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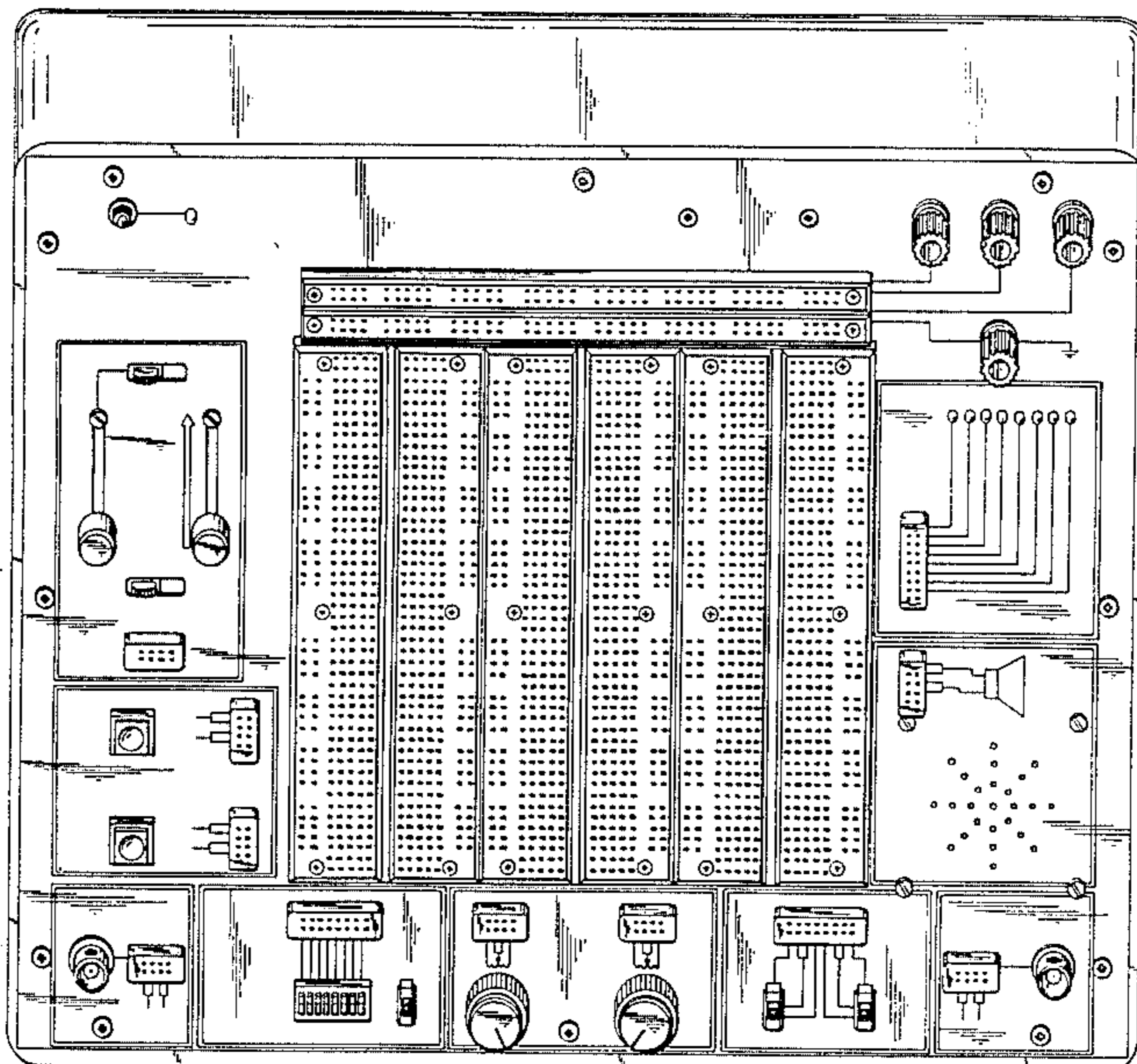
[57]

CLAIM

The ornamental design for electronic circuit setup station, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of an electronic circuit setup station showing my new design.
 FIG. 2 is a top plan view thereof.
 FIG. 3 is a rear elevational view thereof.
 FIG. 4 is a bottom plan view thereof.
 FIG. 5 is a left side elevational view thereof.
 FIG. 6 is a right side elevational view thereof.



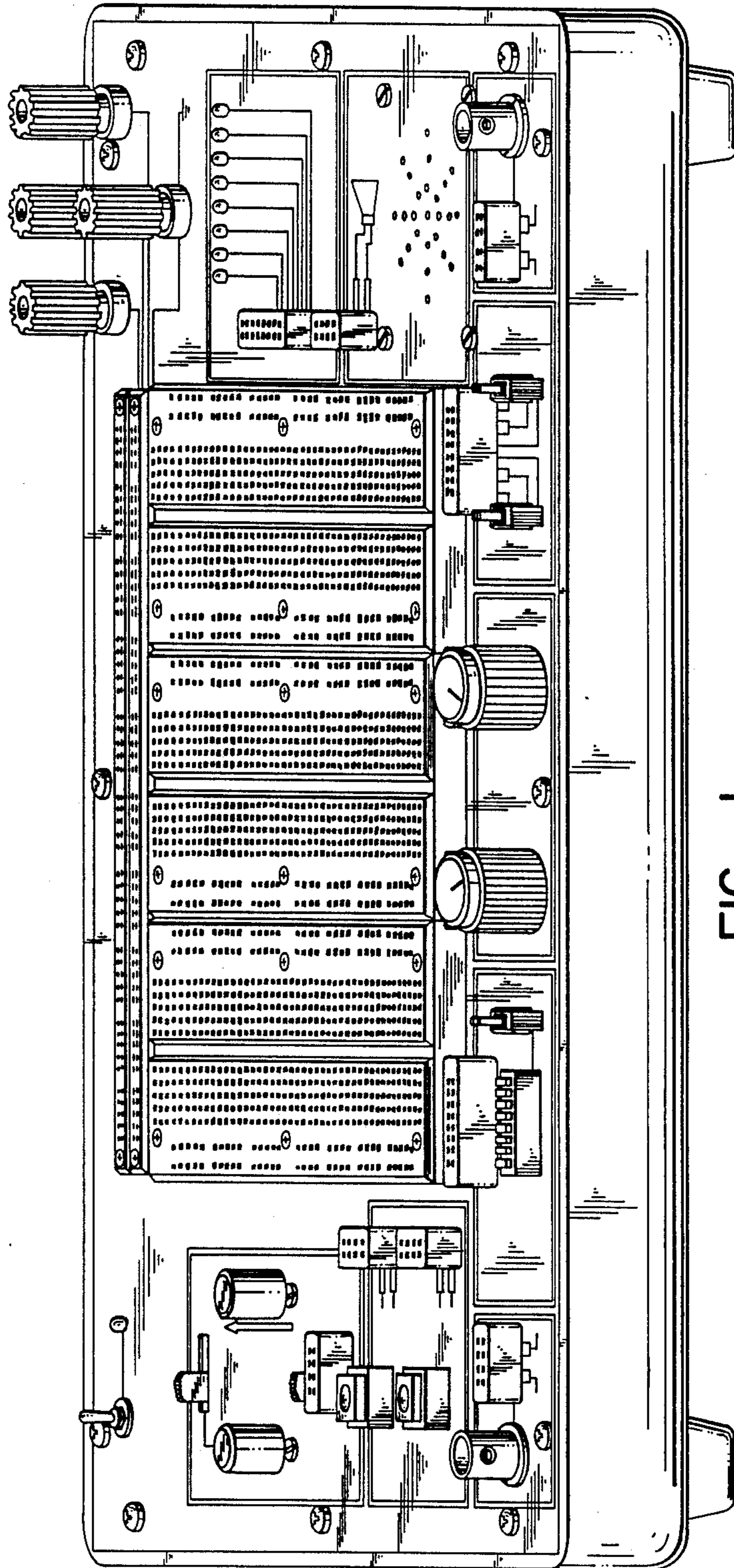
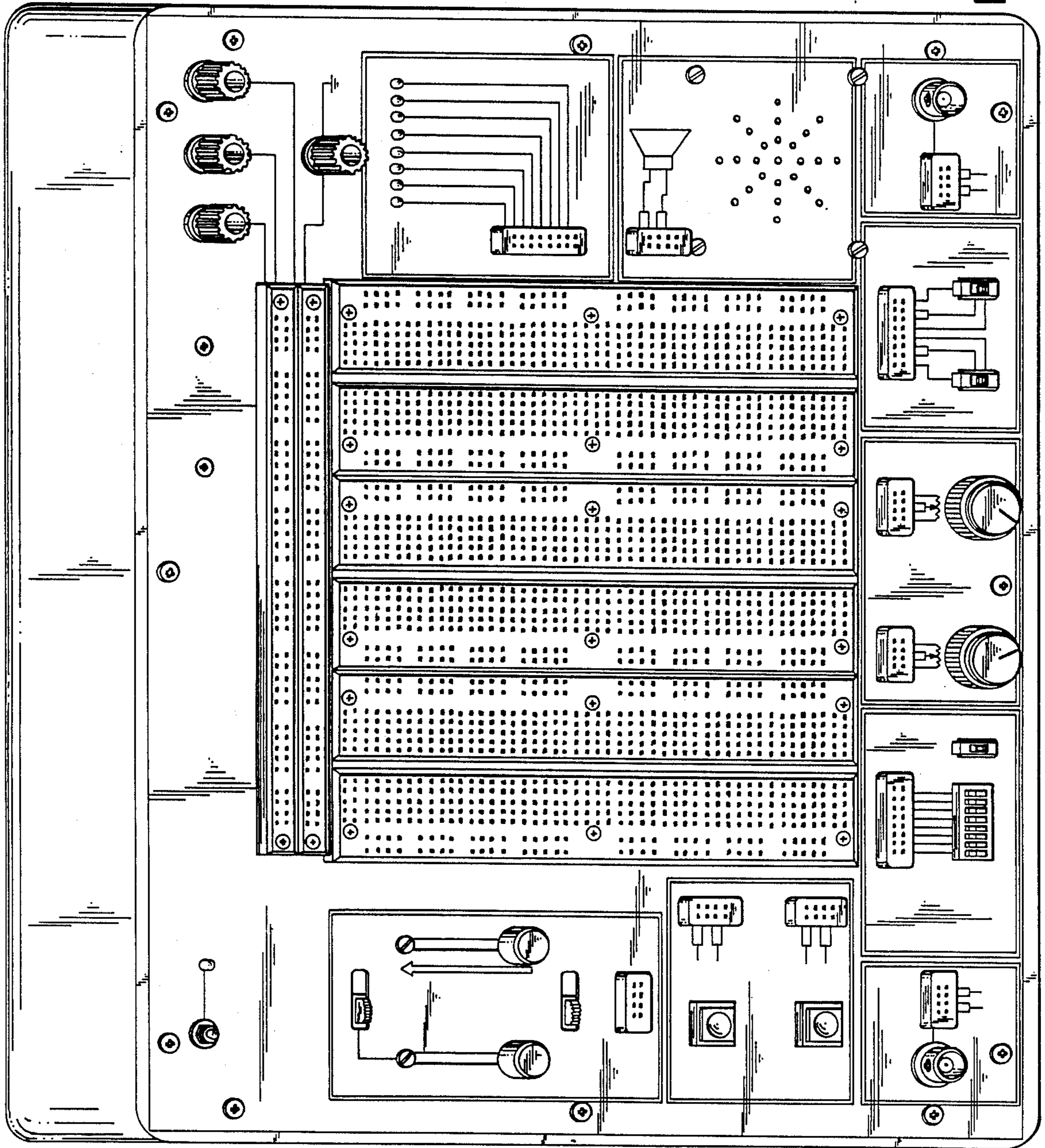


FIG. 1

FIG. 2



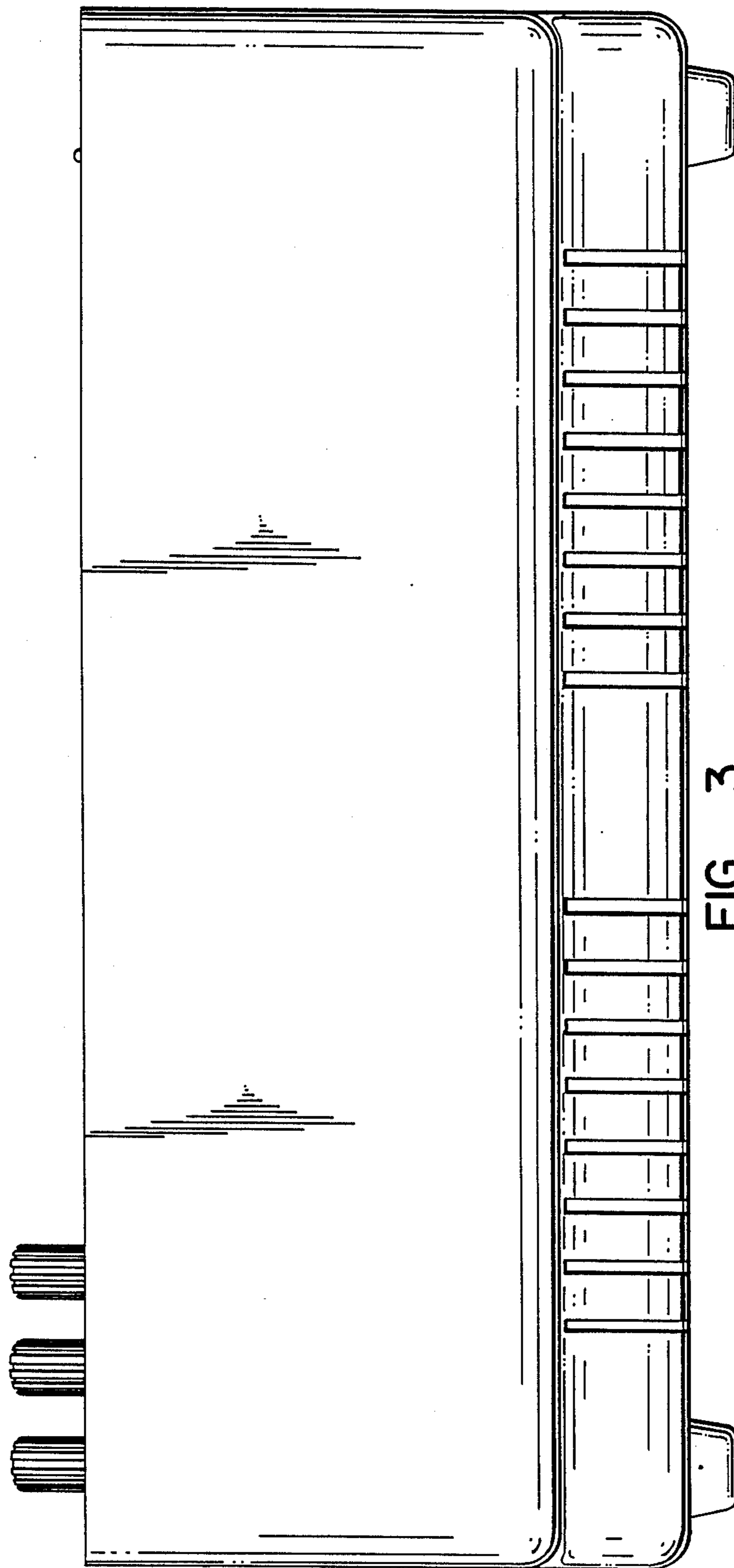
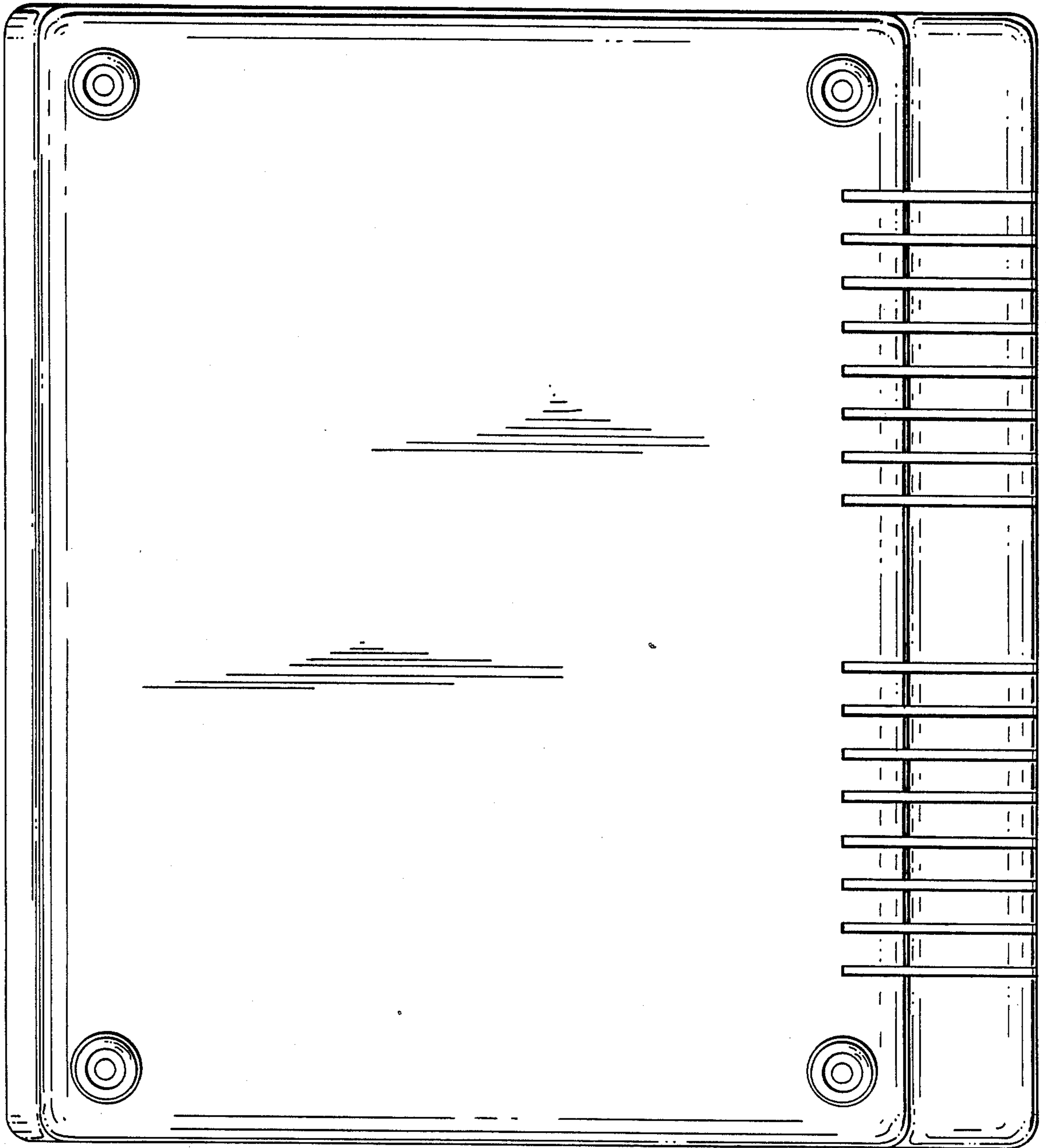


FIG. 3

FIG. 4



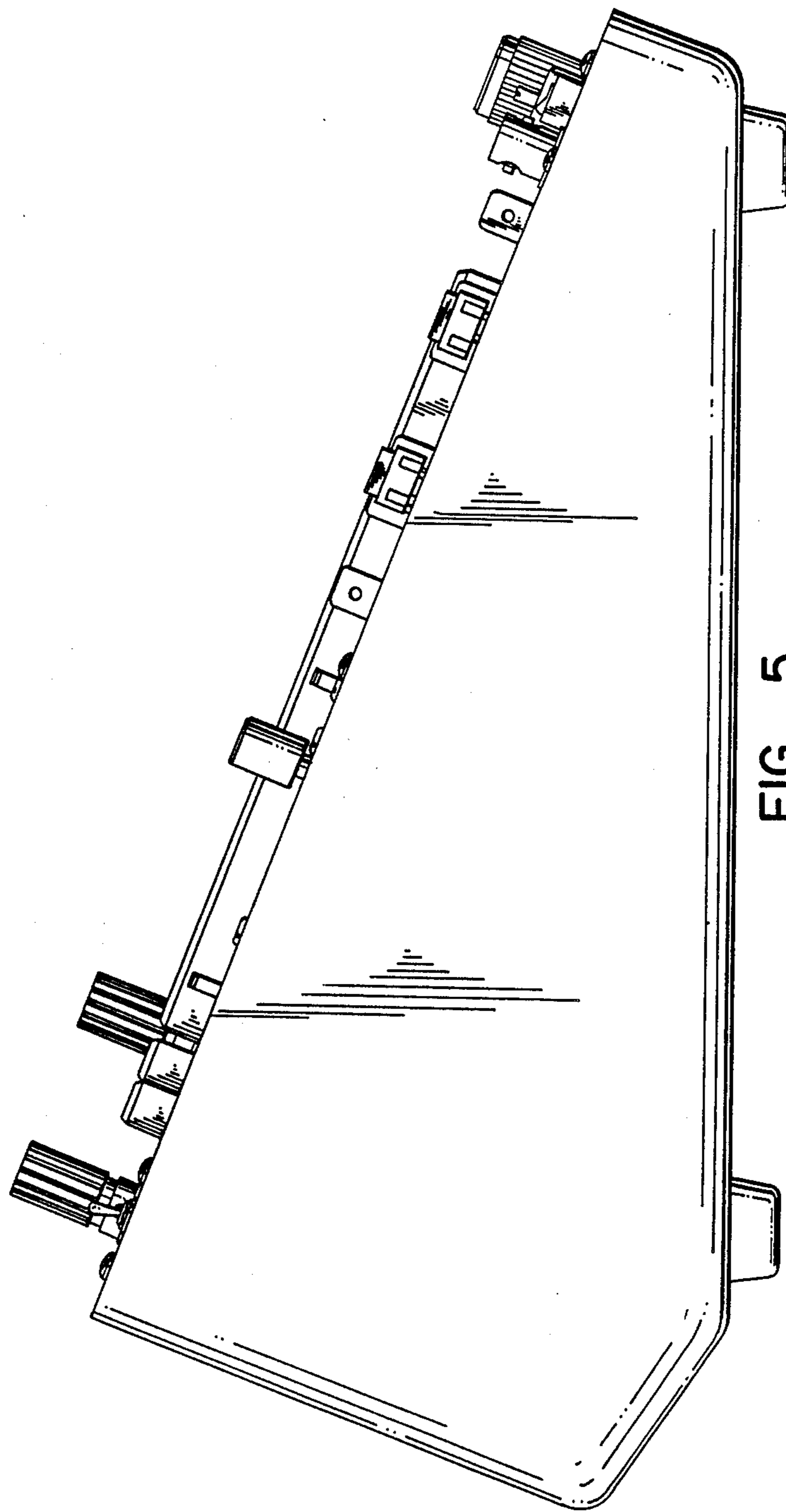


FIG. 5

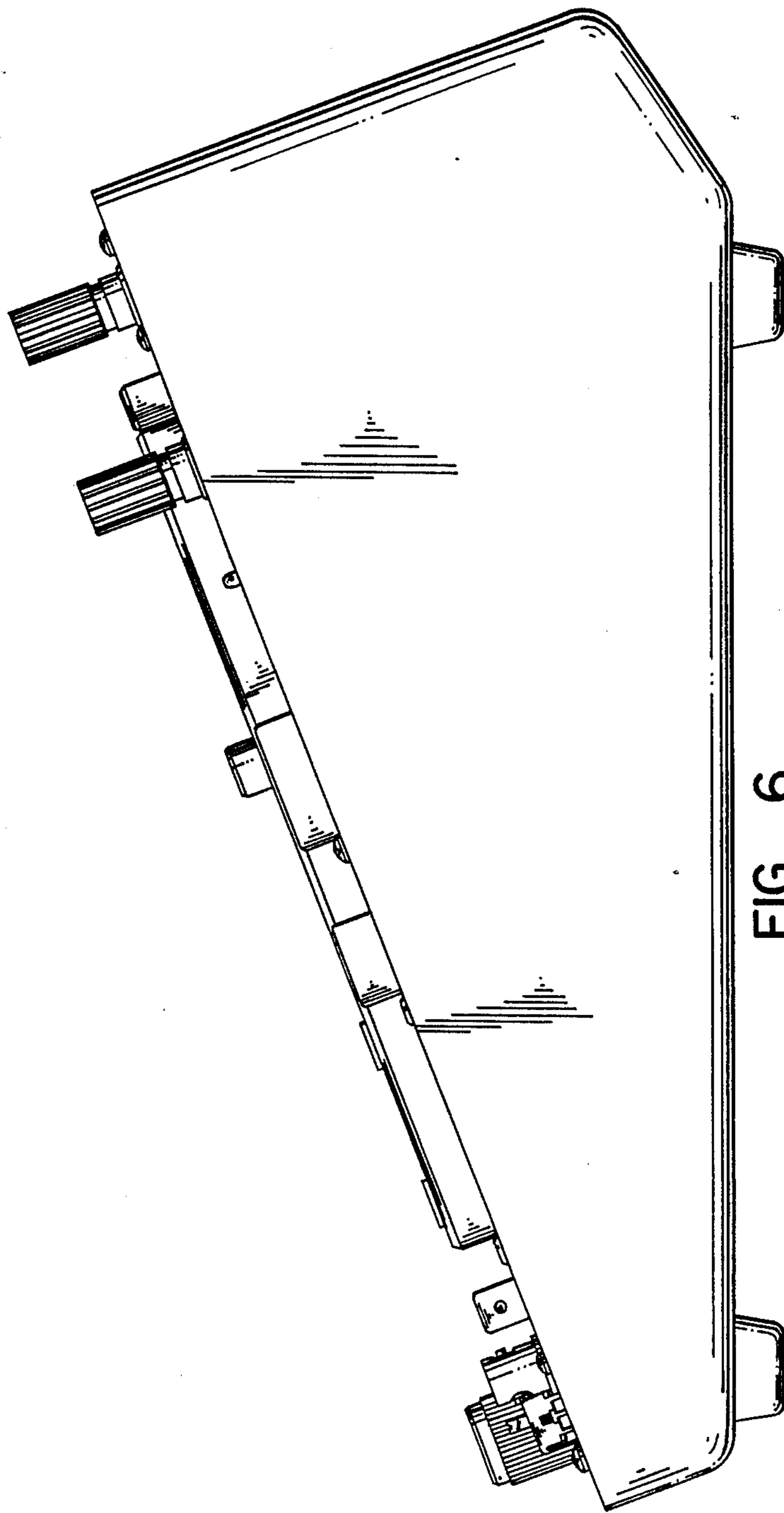


FIG. 6