

[54] MULTI-PIN SOCKET

[75] Inventor: Yasuhiro Komatsu, Osaka, Japan

[73] Assignee: Hosiden Electronics Co. Ltd., Osaka, Japan

[\*\*] Term: 14 Years

[21] Appl. No.: 107,449

[22] Filed: Oct. 13, 1987

[30] Foreign Application Priority Data

Apr. 17, 1987 [JP] Japan ..... 62-15246

[52] U.S. Cl. .... D13/24

[58] Field of Search ..... D13/24, 27-31; 439/578, 586-592, 595, 598, 603, 606, 607, 629, 630, 631, 680, 681, 692, 347, 350

[56] References Cited

U.S. PATENT DOCUMENTS

D. 272,144 1/1984 Mayumi ..... D13/24

D. 286,281 10/1986 Sekiguchi ..... D13/24  
D. 298,312 11/1988 Sekiguchi ..... D13/24  
D. 300,424 3/1989 Matsuzaki et al. .... D13/24  
3,137,535 6/1964 Collier et al. .... 439/680 X  
4,557,542 12/1985 Collier et al. .... 439/595

Primary Examiner—Susan J. Lucas  
Attorney, Agent, or Firm—Pollock, Vande Sande and Priddy

[57] CLAIM

The ornamental design for a multi-pin socket, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of a multi-pin socket showing my new design;  
FIG. 2 is a front end elevational view thereof;  
FIG. 3 is a rear end elevational view thereof;  
FIG. 4 is a right side elevational view thereof;  
FIG. 5 is a left side elevational view thereof;  
FIG. 6 is a top plan view thereof;  
FIG. 7 is a bottom plan view thereof.

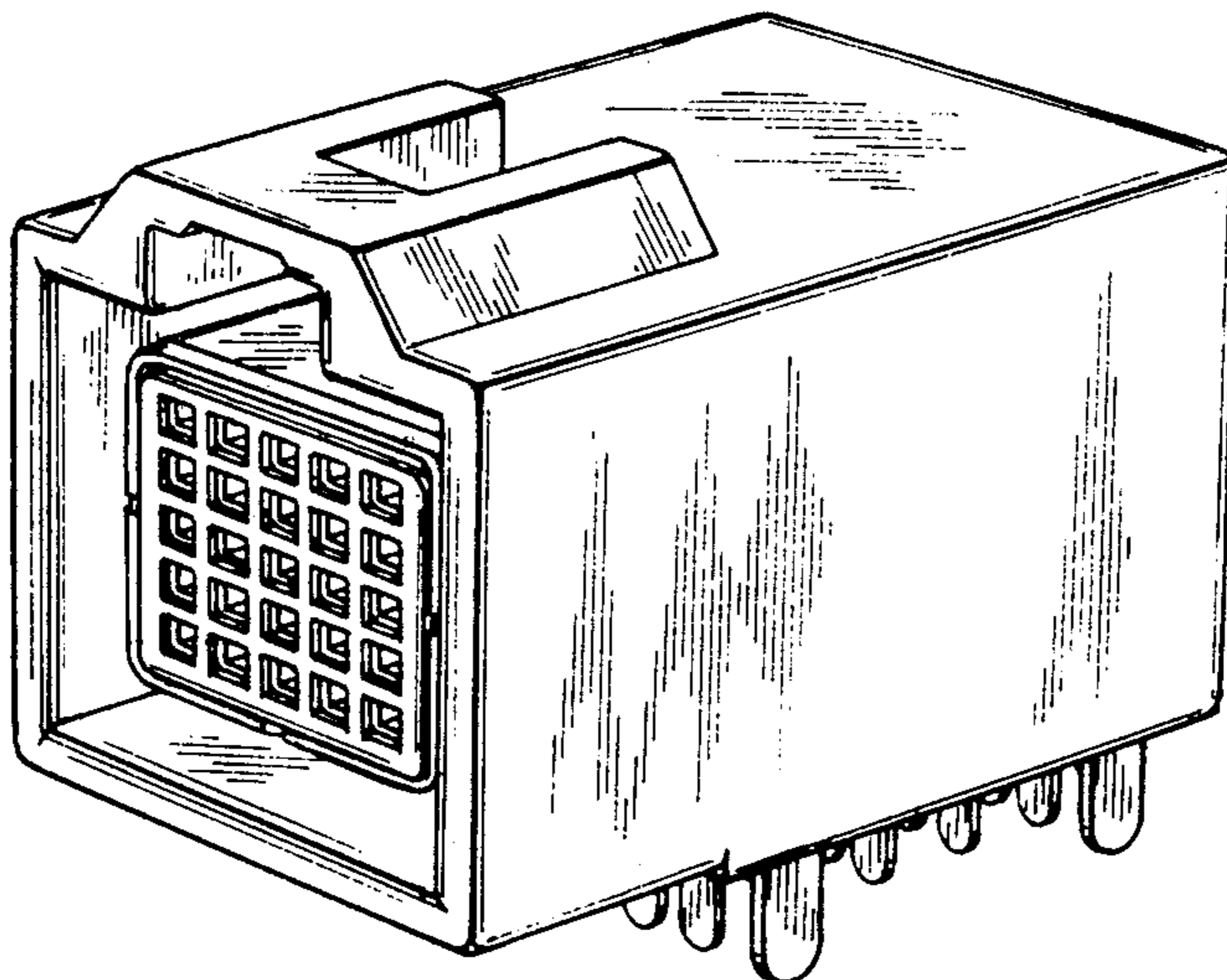


FIG. 1

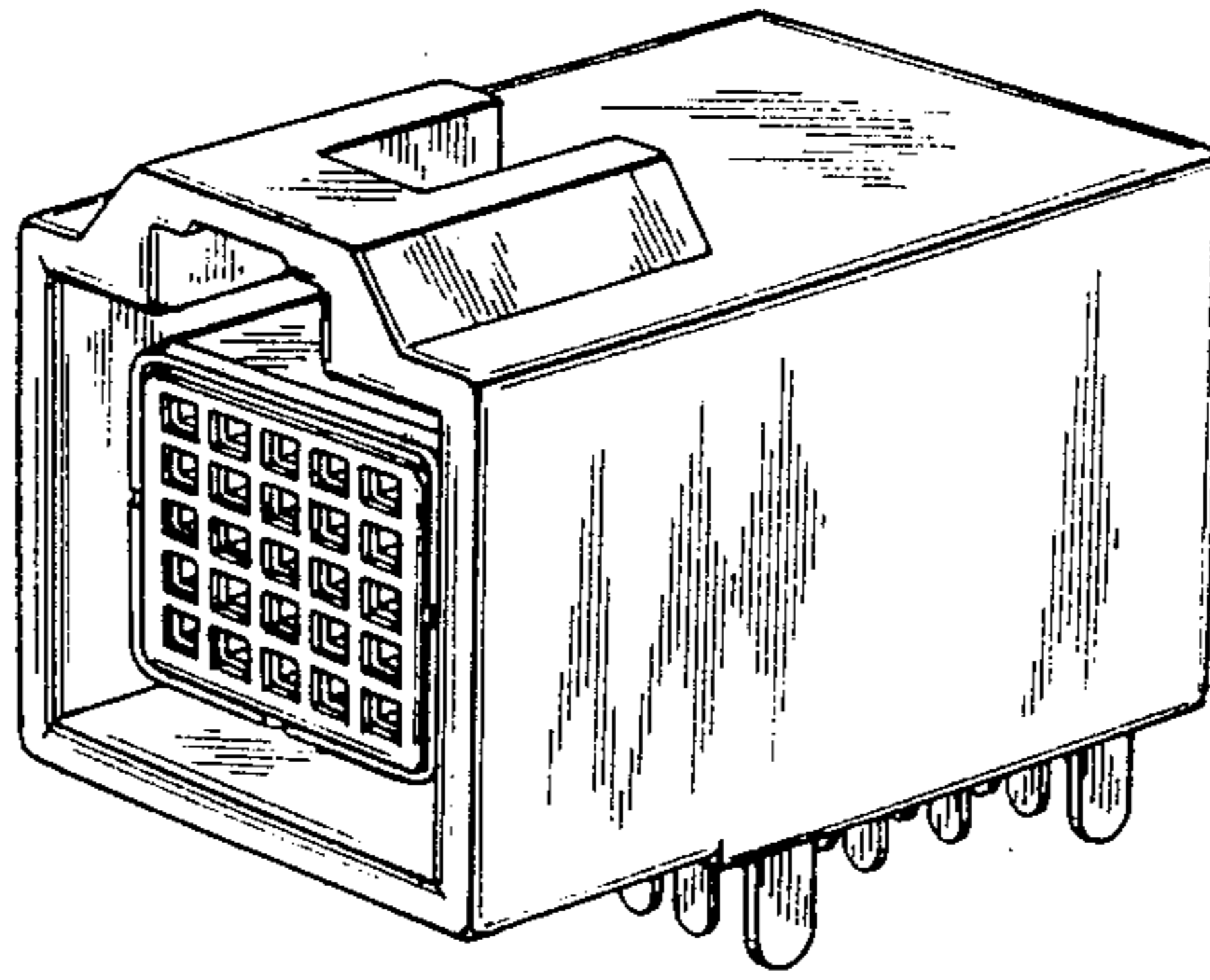


FIG. 2

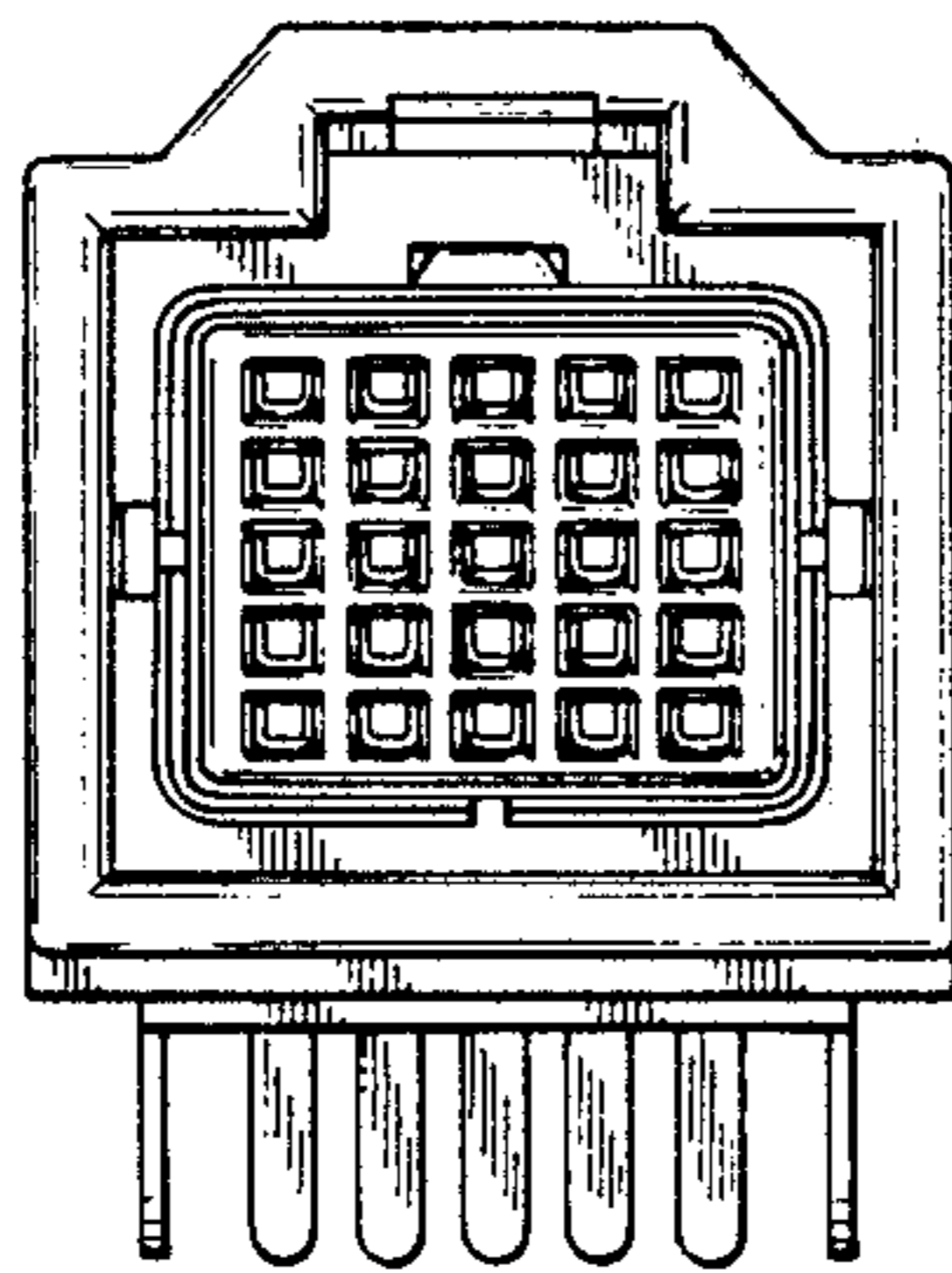


FIG. 3

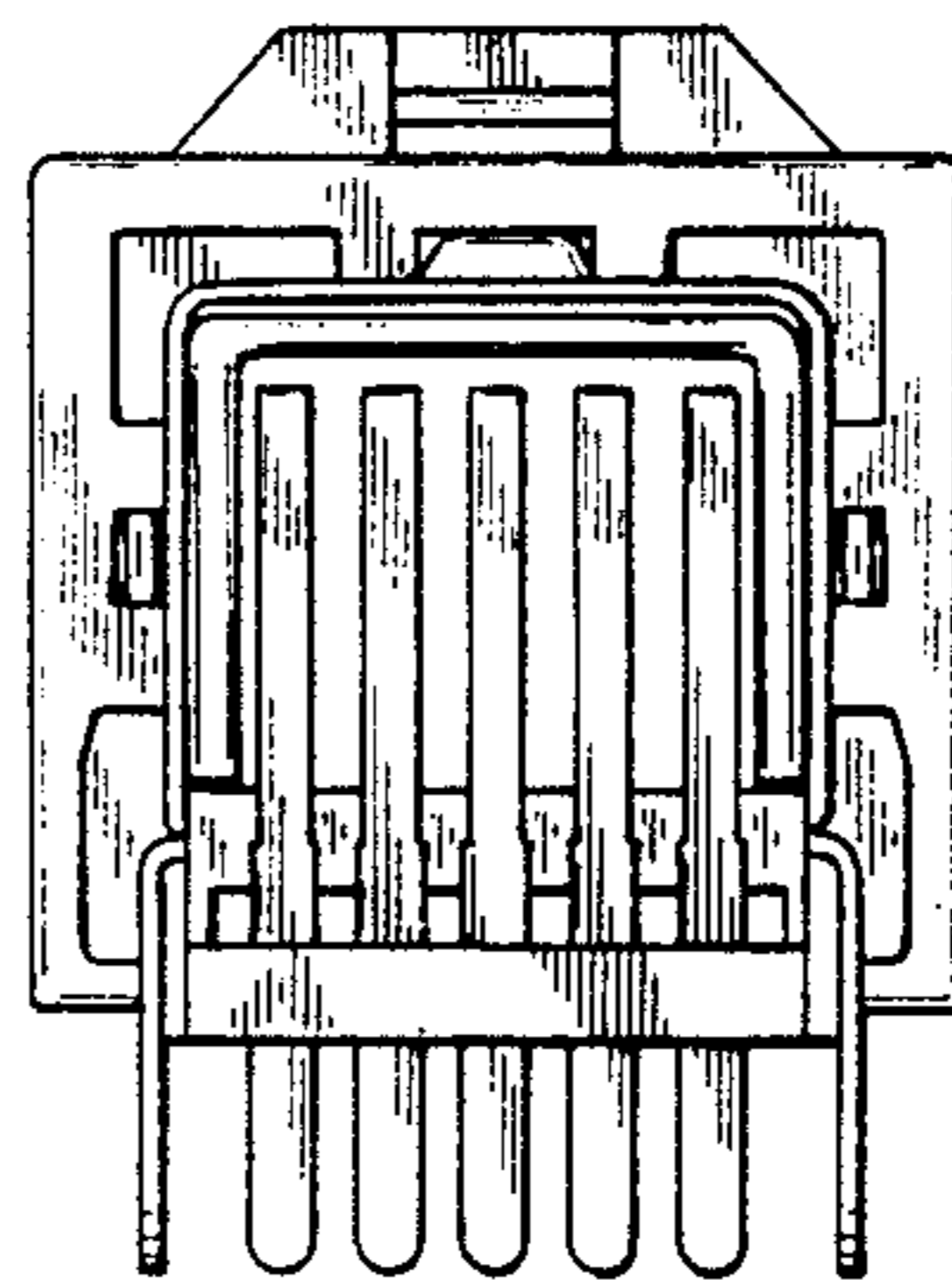


FIG. 4

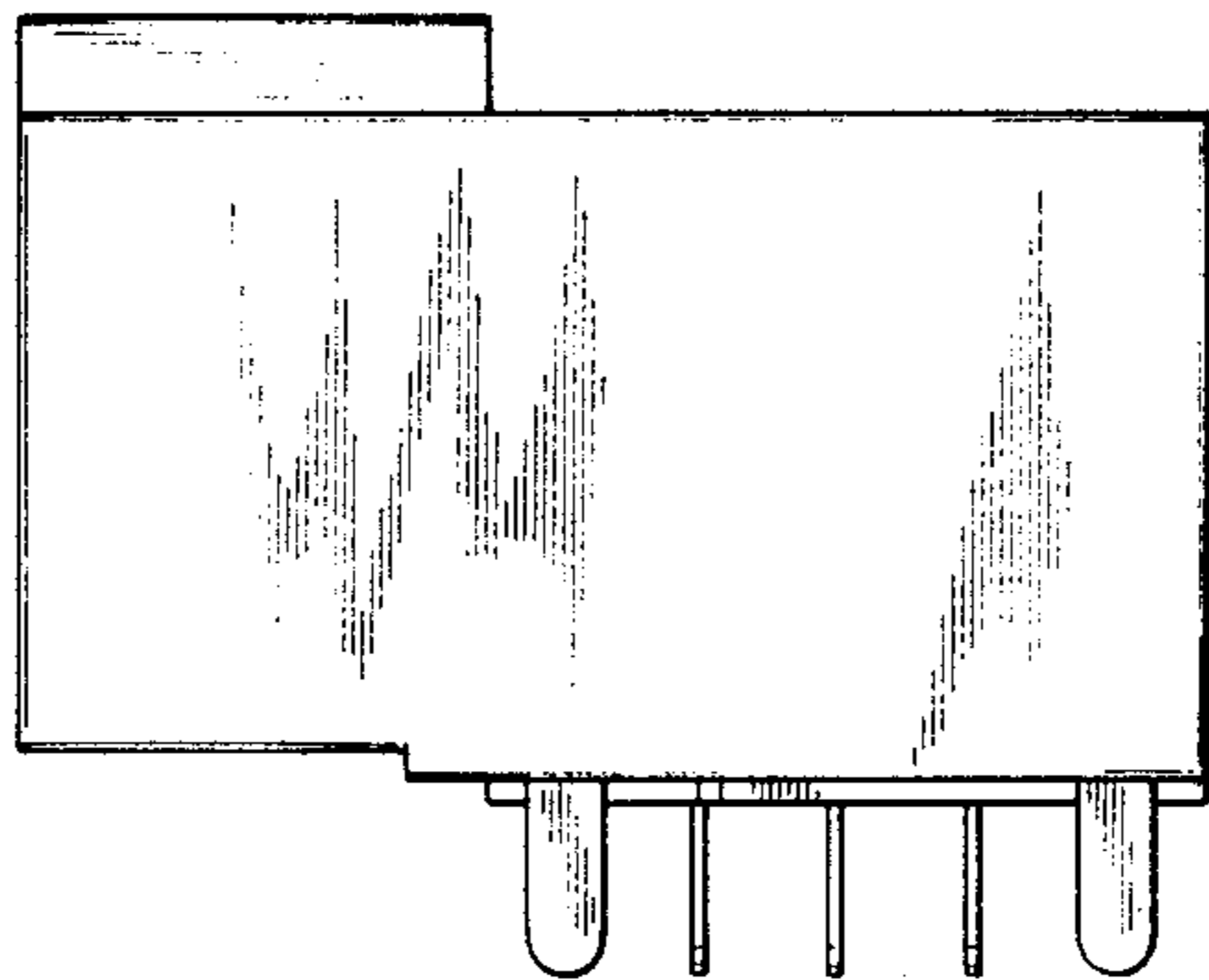


FIG. 5

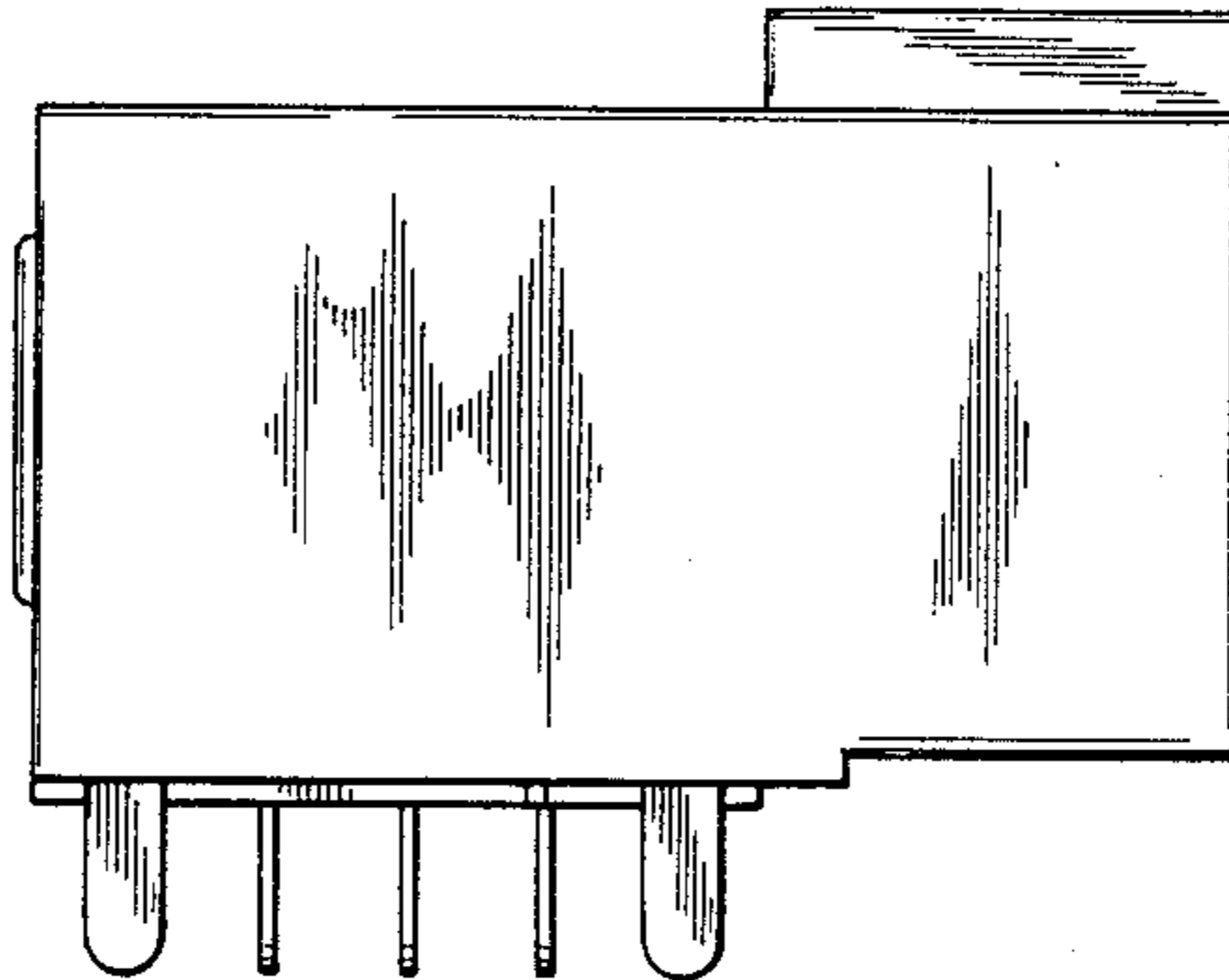


FIG. 6

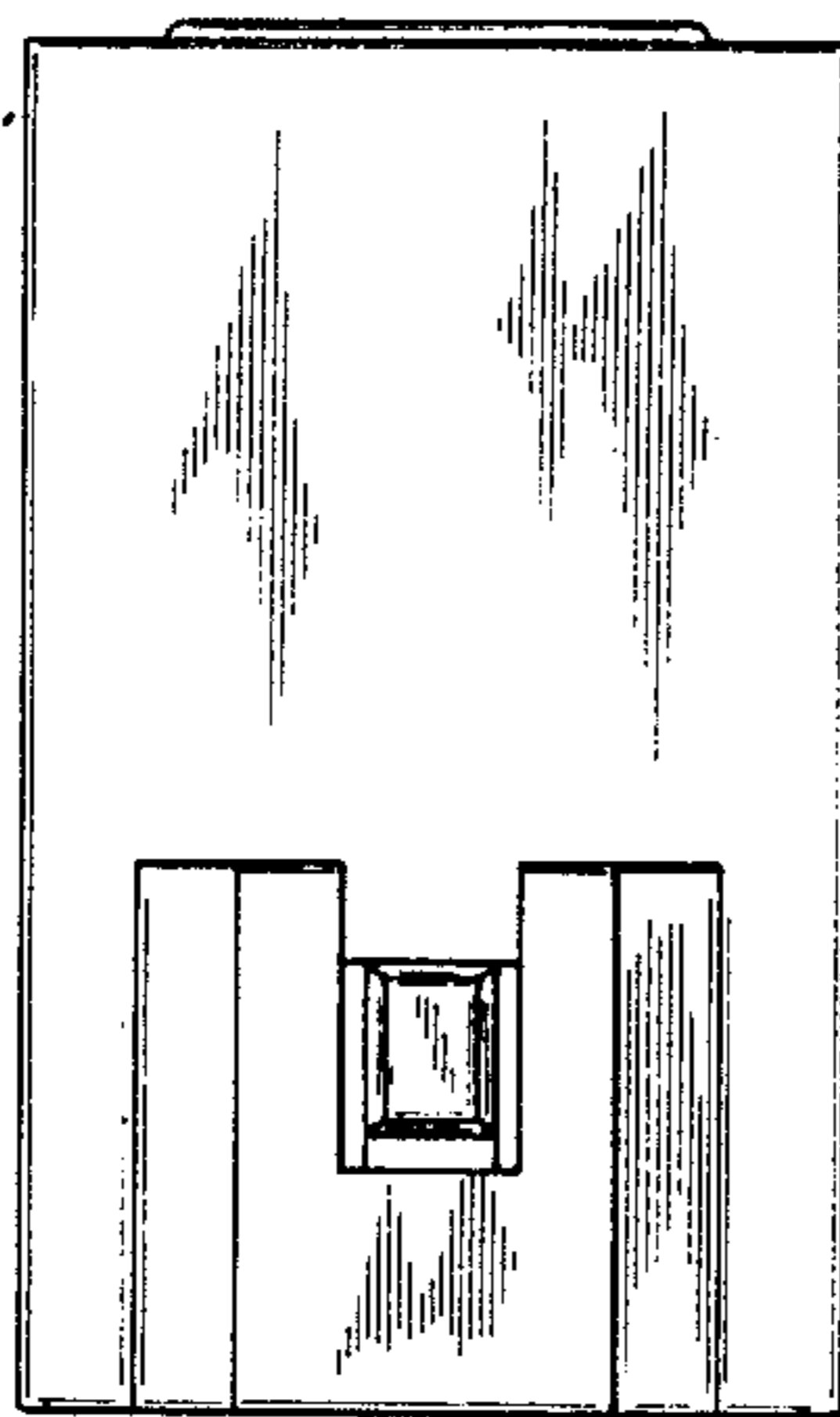


FIG. 7

