

[54] ENCLOSURE FOR CIRCUIT PROTECTION RELATED DEVICES

[75] Inventors: Larry J. Newmark, Avon; David A. Hibbert, Tolland, both of Conn.

[73] Assignee: General Electric Company, New York, N.Y.

[**] Term: 14 Years

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[52] U.S. Cl. D13/35

[58] Field of Search D13/4, 11, 34, 35, 40, D13/99; 361/331-334, 346-348, 350, 352, 355-358, 361, 364, 380, 381, 383, 390-392, 394; 174/48, 50, 66; 336/90, 92; 220/3.2, 3.3, 3.8

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OTHER PUBLICATIONS

General Electric brochure (DE-41 0586 MAO), submitted by applicant, located in application file.
General Electric Technical Information guide (GEZ 7409) submitted by applicant. Located in application file.

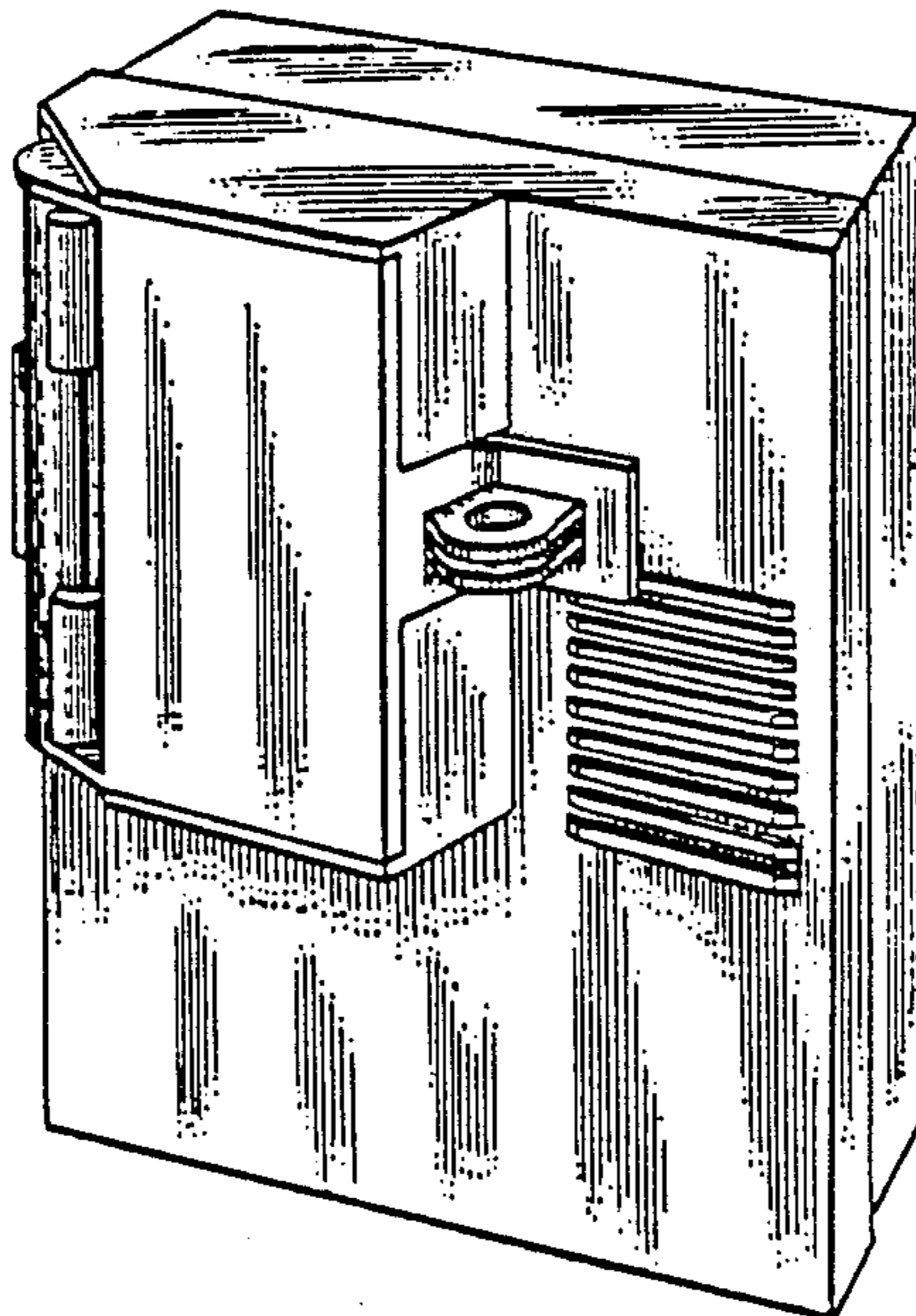
Primary Examiner—James M. Gandy
Assistant Examiner—Joel Sincavage
Attorney, Agent, or Firm—Richard A. Menelly; Walter C. Bernkopf; Fred Jacob

[57] CLAIM

The ornamental design for an enclosure for circuit protection related devices, as shown and described.

DESCRIPTION

FIG. 1 is an upper, front, right perspective view of an enclosure for circuit protection related devices showing our new design;
FIG. 2 is an upper front, left perspective view thereof;
FIG. 3 is a left side elevational view thereof;
FIG. 4 is a top plan view thereof;
FIG. 5 is a front elevational view thereof;
FIG. 6 is a right side elevational view thereof;
FIG. 7 is a bottom plan view thereof.



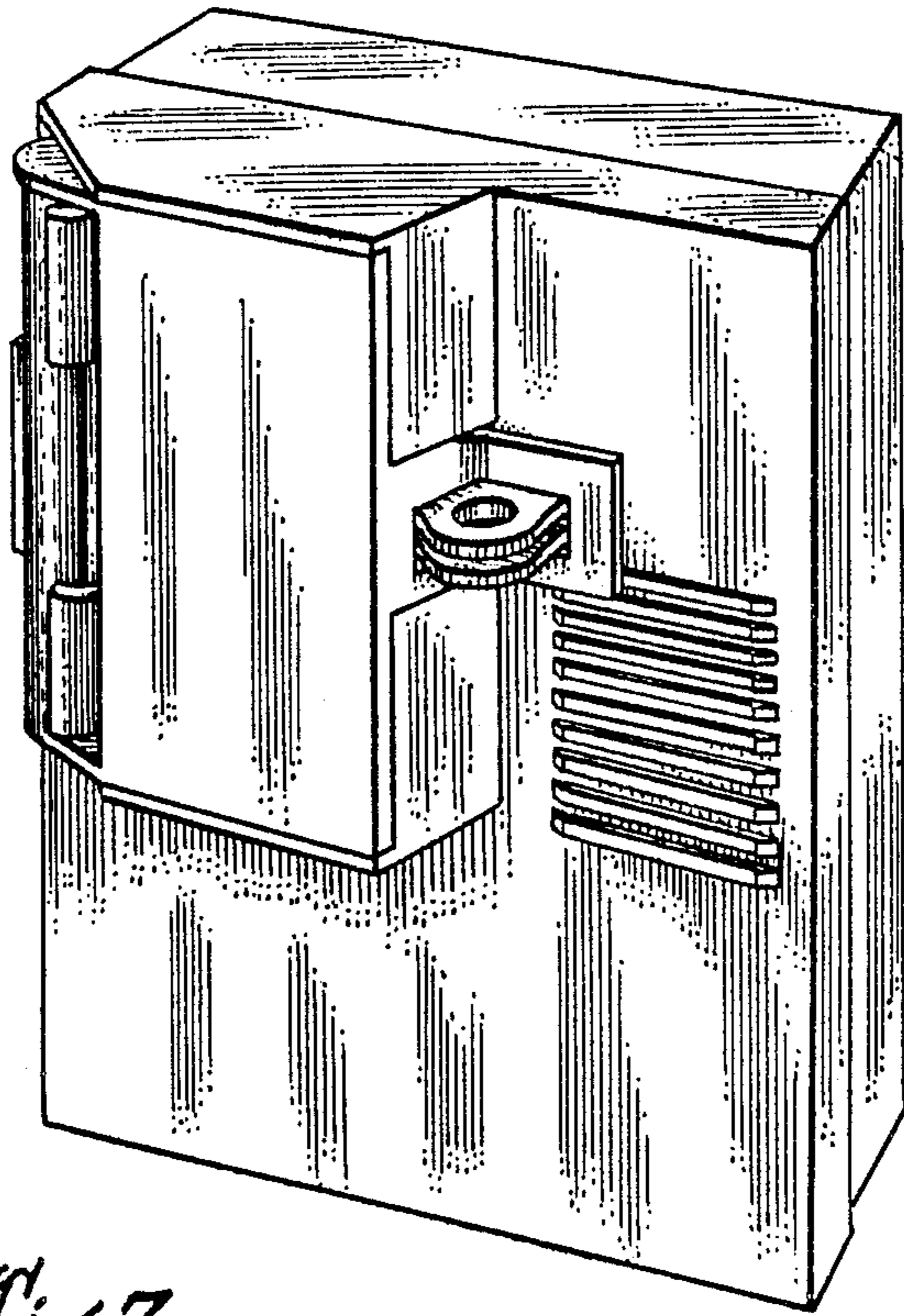
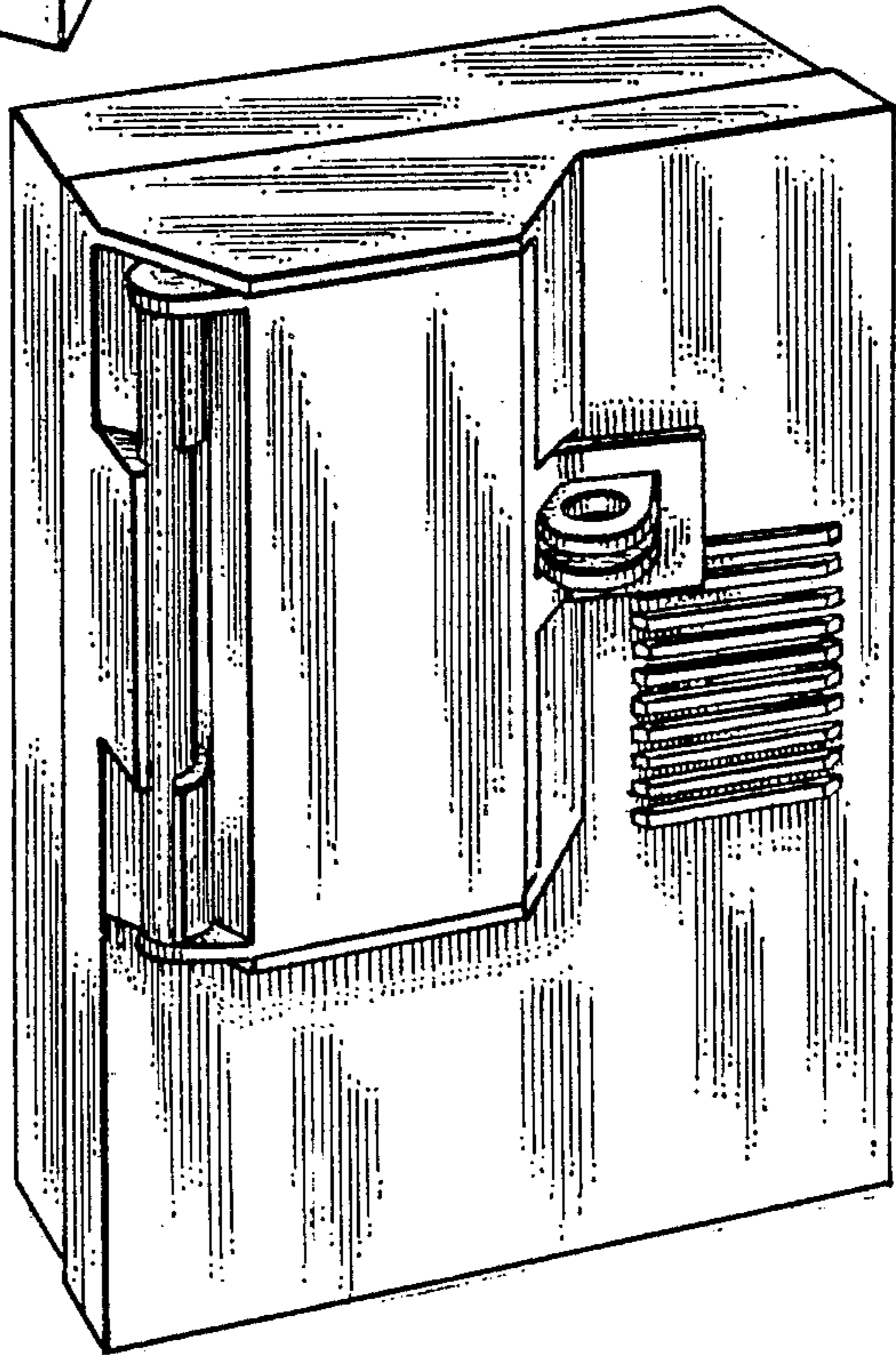
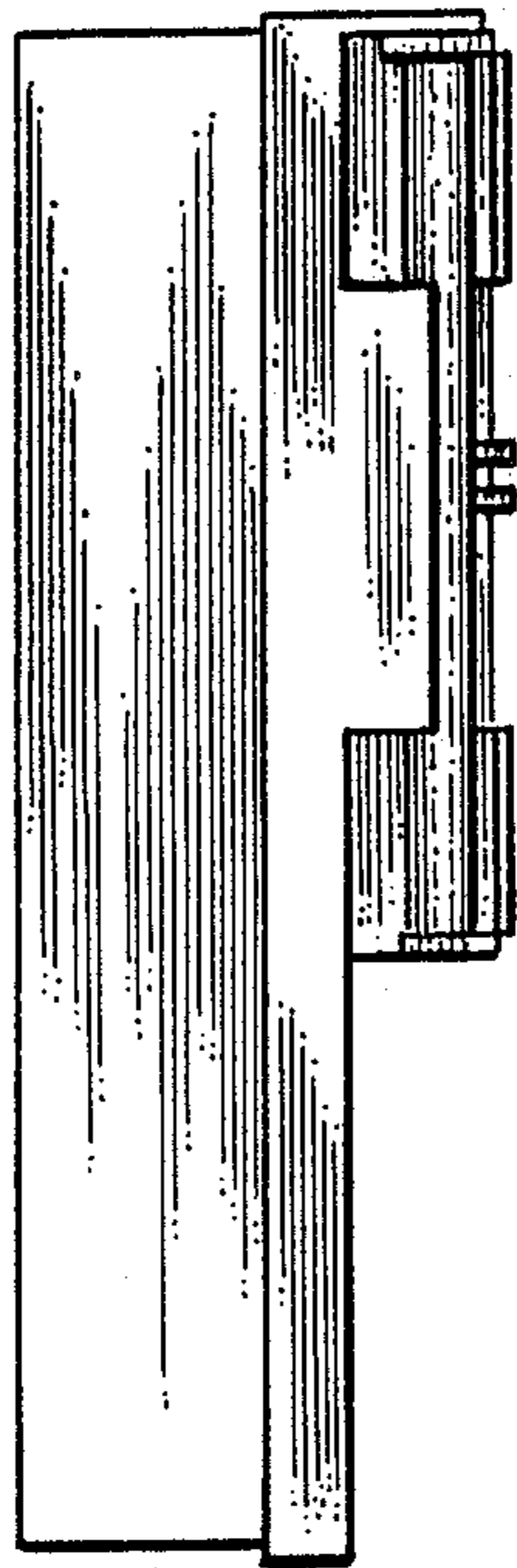


Fig. 1.

Fig. 2.

Fig. 3.



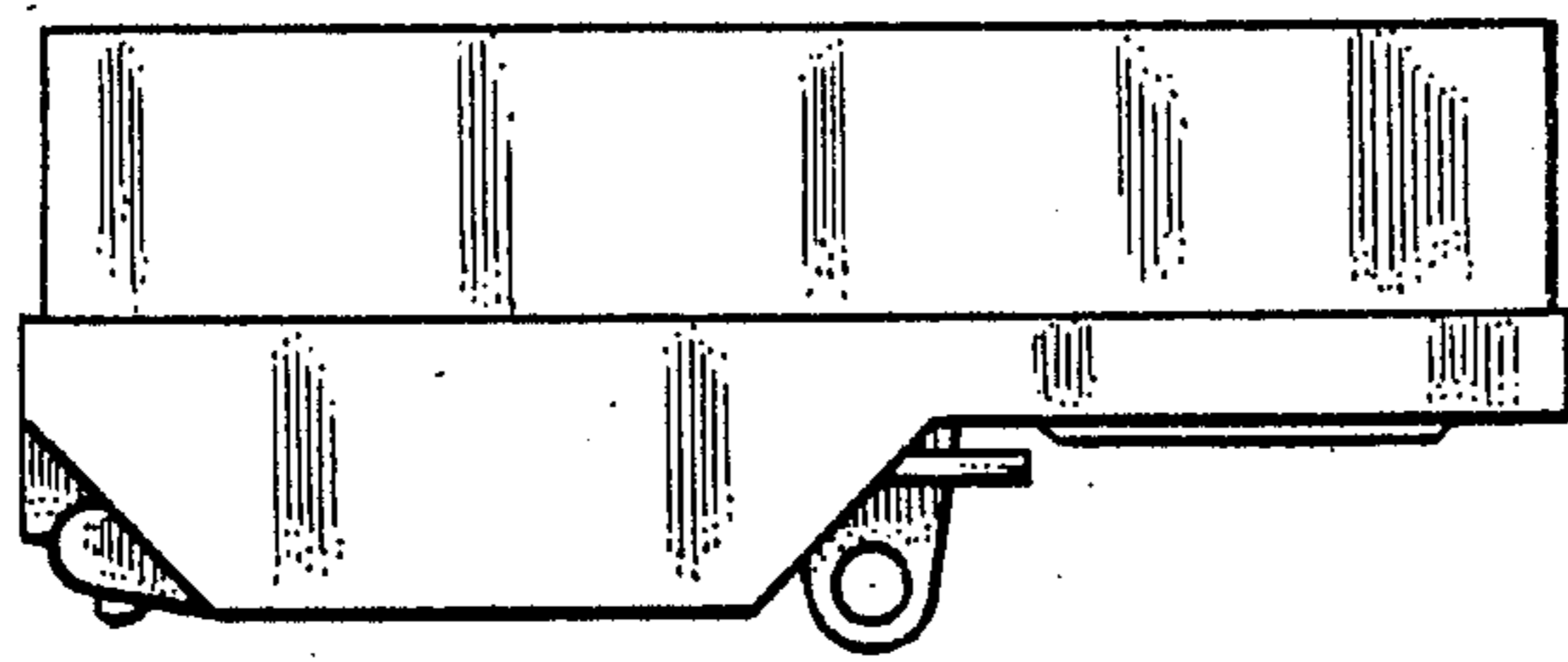


Fig. 4.

Fig. 5.

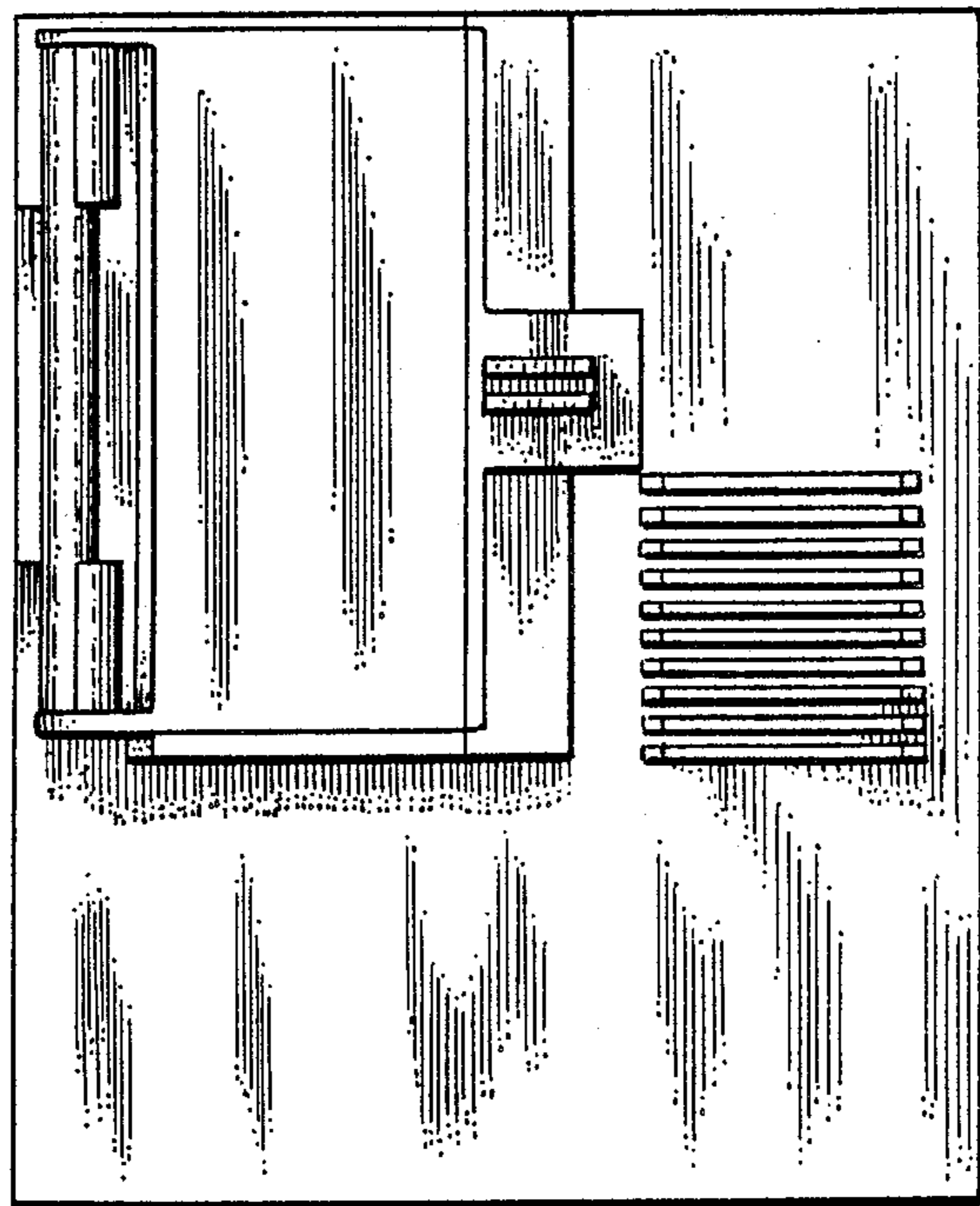


Fig. 6.

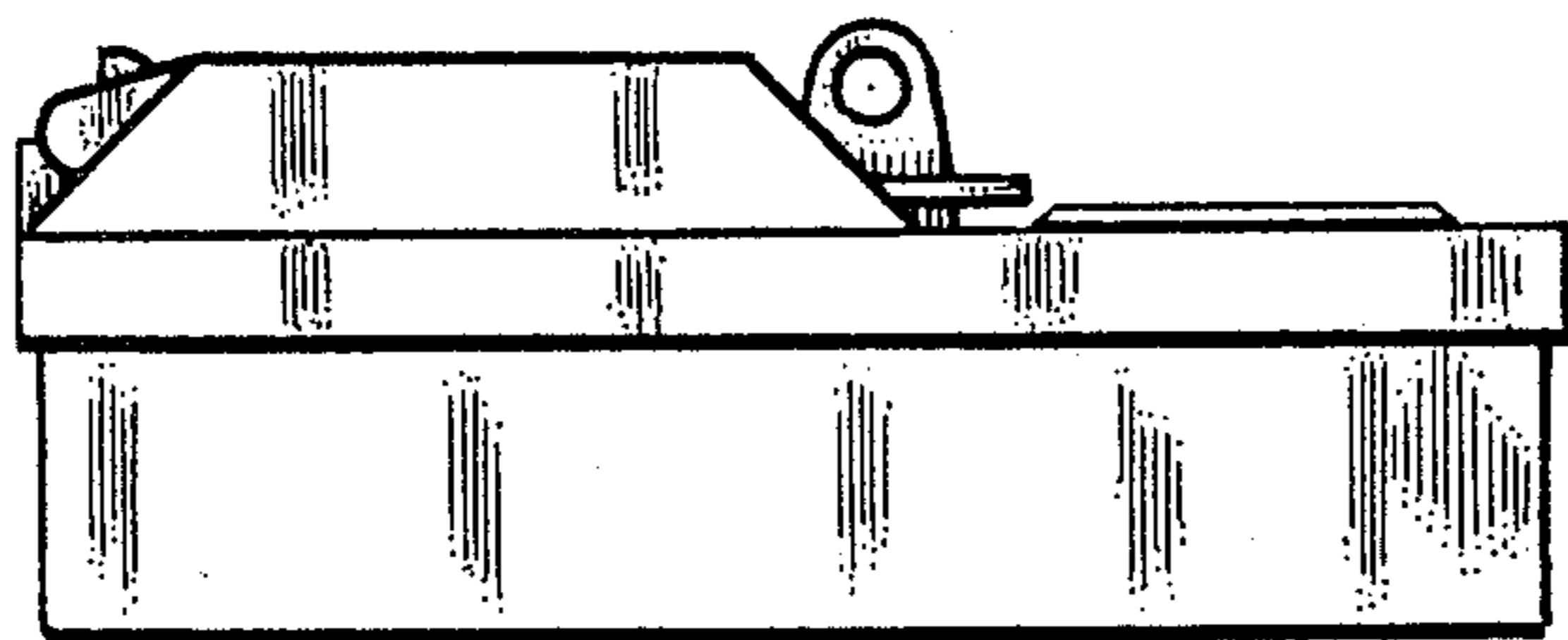
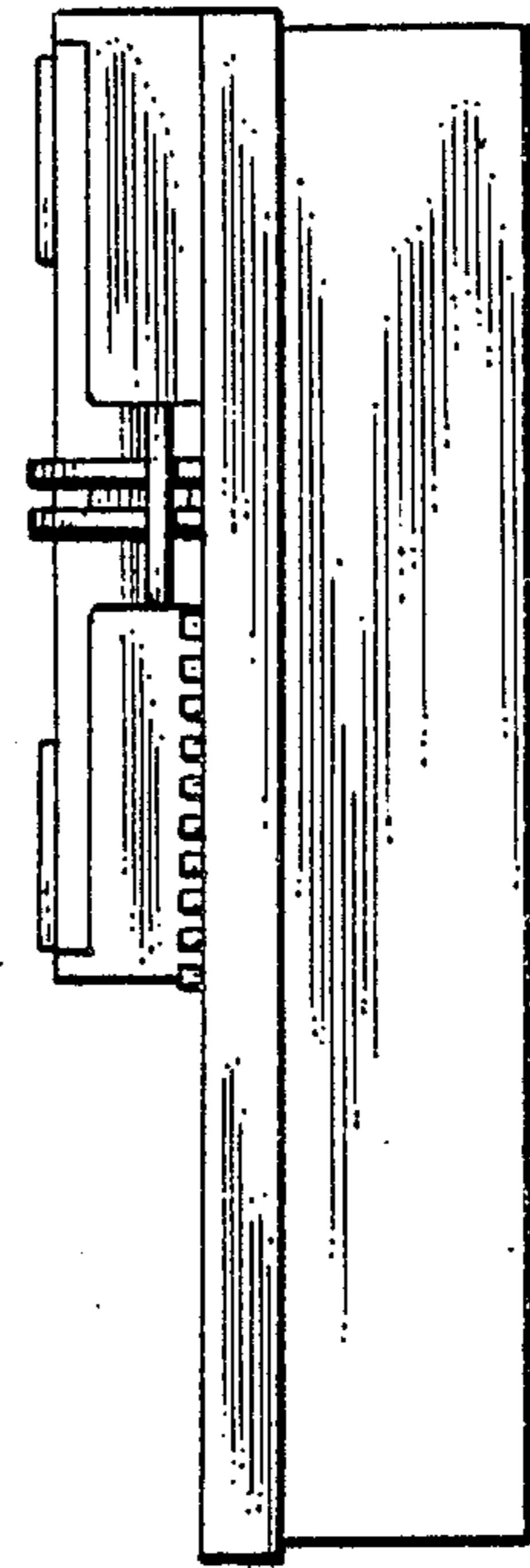


Fig. 7.