United States Patent [19] Seymour

[54] **ELECTRONIC HOUSING FOR BREADBOARDING BLOCKS**

Arthur Seymour, 1245 Rosewood [76] Inventor: Ave., Deerfield, Ill. 60015

[**] Term: 14 Years

Appl. No.: 819,350 [21]

[56]

- [22] Filed: Jan. 16, 1986
- [52]

Patent Number: Des. 300,624 [11] Date of Patent: ** Apr. 11, 1989 [45]

Primary Examiner-Wallace R. Burke Assistant Examiner—Ruth Takemoto Attorney, Agent, or Firm-Jeffrey M. Morris

[57] CLAIM

The ornamental design for an electronic housing for breadboarding blocks, as shown.

DESCRIPTION

FIG. 1 is a perspective view of an electronic housing for breadboarding blocks, showing my new design; FIG. 2 is a top plan view thereof; FIG. 3 is a bottom plan view thereof; FIG. 4 is a side elevational view thereof; FIG. 5 is a side elevational view of the opposite side thereof; FIG. 6 is an end elevational view thereof; FIG. 7 is an end elevational view of the opposite end thereof.

D13/35, 40-41, 99, 24; 174/52 R, 52 PE; 206/328-331, 334; 361/380-381, 384, 390-394, 331-335, 399, 403, 413; 269/903

References Cited

U.S. PATENT DOCUMENTS

D. 249,790 10/1978 Ainscow D13/41 D. 269,345 6/1983 Payne, Jr. et al. D13/41



.

. .

.

• •

U.S. Patent

.

•

.

•

.

•

.

.

•

.

.

.

٠

Apr. 11, 1989



FIG. I BAARA BRAR BARRA 0000 0000 88080 DDDDD



FIG. 2



FIG. 3



	السنسب

.

.

.

÷."

.

.

• . .

FIG. 4



.

. -

•

FIG. 5

-

.



.





FIG. 6

.

3 . .

. . ,

FIG. 7