

[54] FRONT PANEL FOR A SIMULATED ELECTRONIC COMPONENT SYSTEM

[75] Inventor: John C. Columbus, Bartlett, Ill.

[73] Assignee: Protronics Co., Northbrook, Ill.

[**] Term: 14 Years

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[52] U.S. Cl. D20/29

[58] Field of Search D20/10, 29; D14/1, 2, D14/6, 14, 18, 20, 22; 40/538, 539, 124.1

[56] References Cited

U.S. PATENT DOCUMENTS

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Primary Examiner—Catherine E. Kemper
Attorney, Agent, or Firm—Jacques M. Dulin

[57] CLAIM

The ornamental design for a front panel for a simulated electronic component system, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of a simulated electronic component system, showing my new design; FIG. 2 is a sectional view taken along line 2—2 of FIG. 1; and FIG. 3 is a sectional view taken along line 3—3 of FIG. 1.

The broken line disclosure shown in FIG. 2 is for illustrative purposes only and forms no part of the claimed design. The rear surface is substantially planar and unornamented.

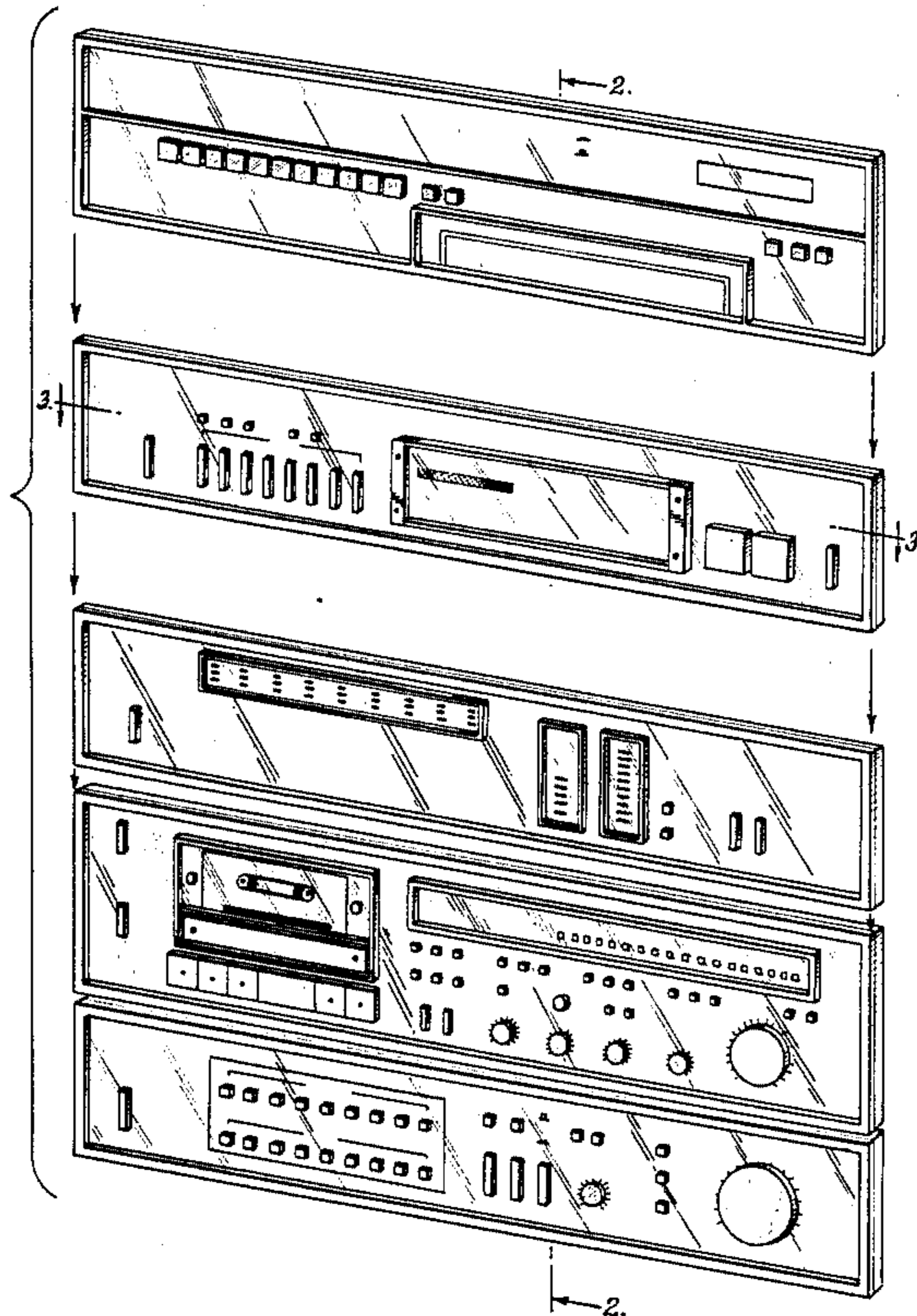


Fig. 1.

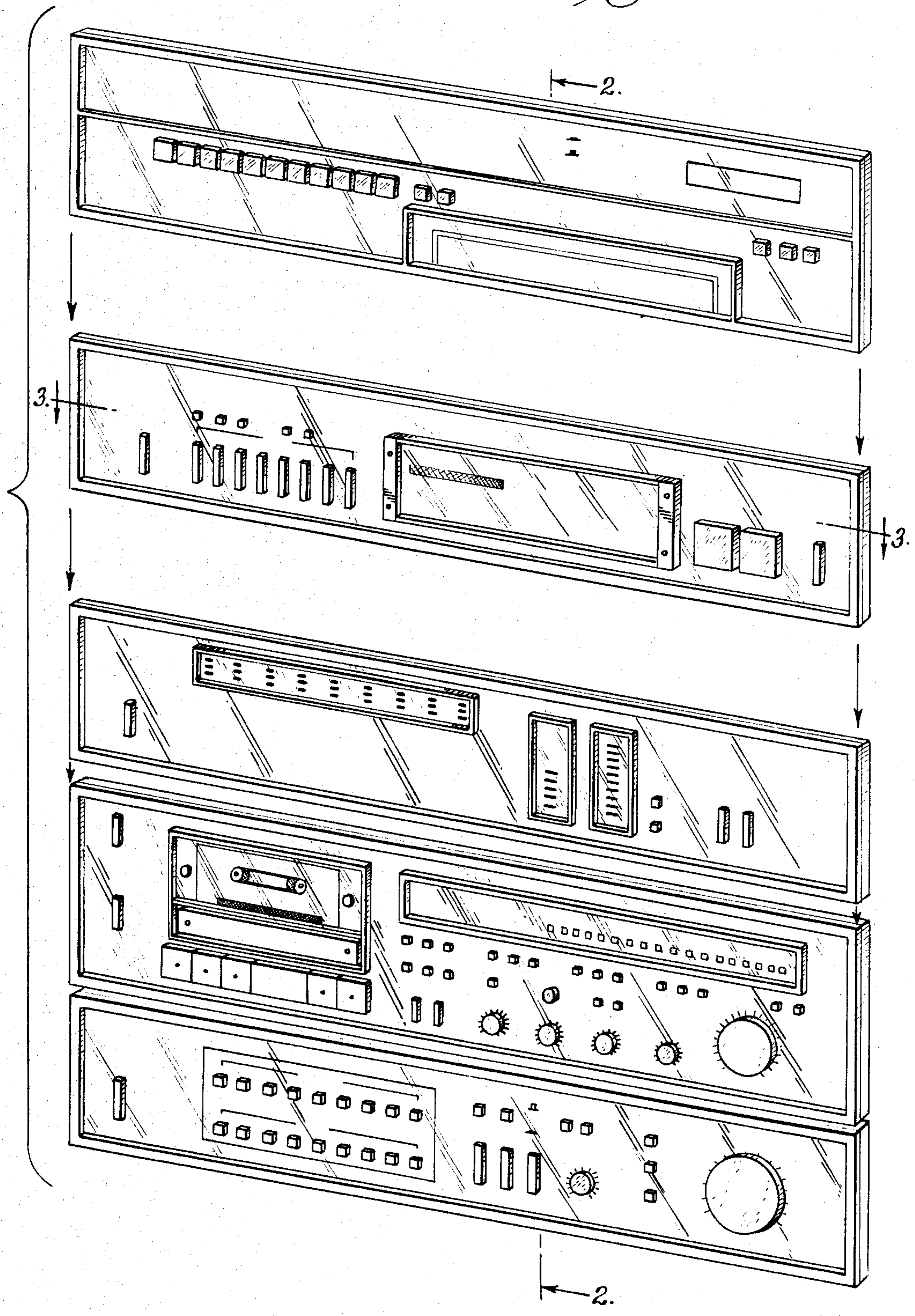


Fig. 2.

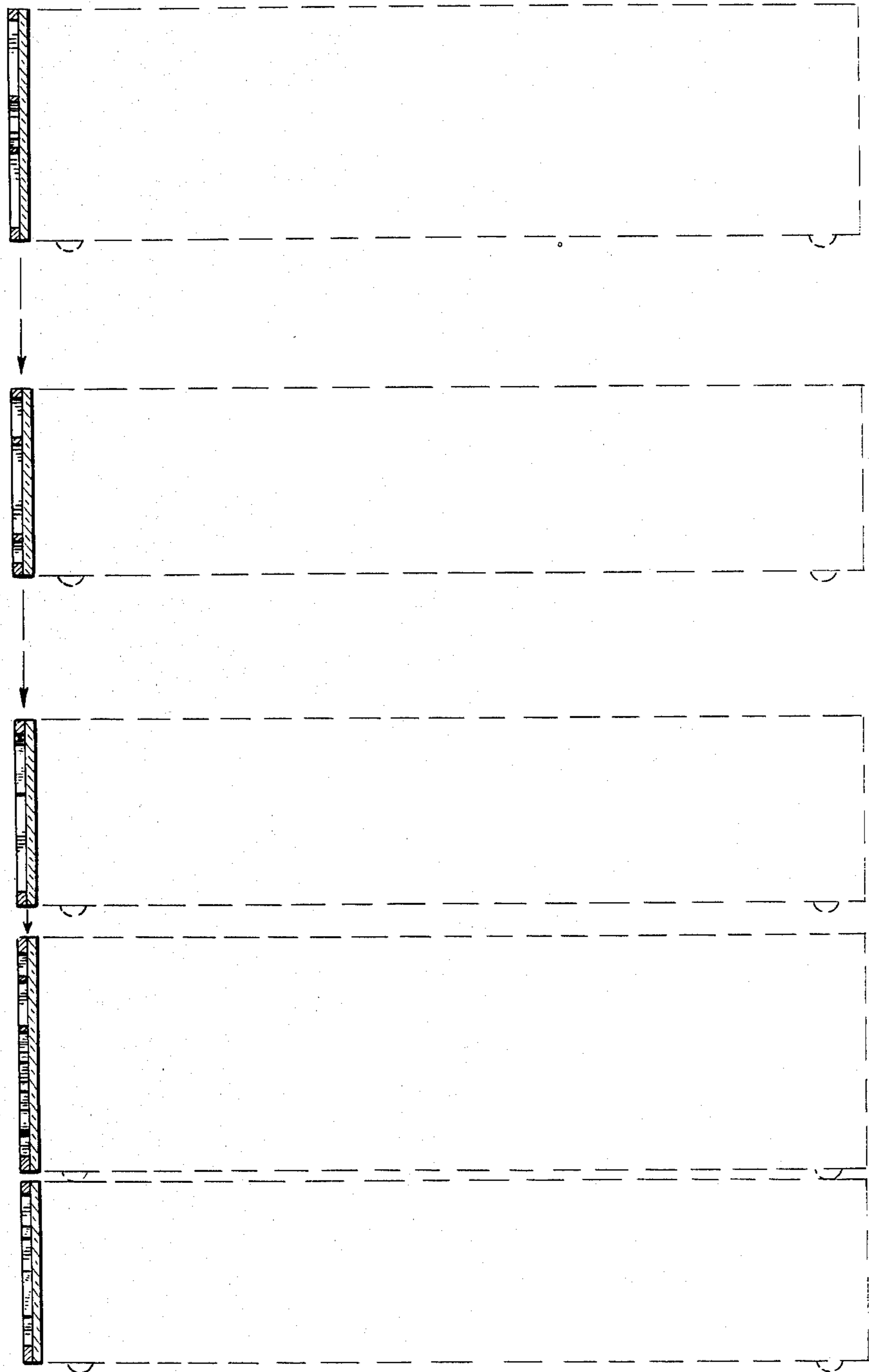


Fig. 3.

