

[54] ELECTRONIC CLOCK

D. 281,083 10/1985 Gabriel D19/62

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OTHER PUBLICATIONS

Principles & Practices of Digital ICS & LEDS—Tab Books—Blueridge Summit, PA. 17214—by Doninbody ©1984—p. 205—LED Clock.

[**] Term: 14 Years

Primary Examiner—Nelson C. Holtje
Attorney, Agent, or Firm—William D. Zahrt, II

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[22] Filed: Sep. 3, 1985

[57] CLAIM

[52] U.S. Cl. D10/15; D10/6; D10/125

The ornamental design for an electronic clock, substantially as shown and described.

[58] Field of Search D10/1-39, D10/122-126; 368/239-242, 10, 11; D19/56, 62; D13/12, 24, 99

DESCRIPTION

[56] References Cited

U.S. PATENT DOCUMENTS

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- D. 239,107 3/1976 Covert D19/62
- D. 239,110 3/1976 Covert D19/62
- D. 251,598 4/1979 Gabriel D19/62
- D. 255,223 6/1980 Odom D10/15
- D. 256,679 9/1980 Vaughan D10/15 X
- D. 265,322 7/1982 Gabriel D19/62
- D. 273,950 5/1984 Itoh D14/99 X

FIG. 1 is a front elevational view of an electronic clock showing my new design;
 FIG. 2 is a bottom plan view thereof as taken from line 2—2 of FIG. 1;
 FIG. 3 is a rear elevational view thereof;
 FIG. 4 is a top plan view thereof as taken from line 4—4 of FIG. 3;
 FIG. 5 is a left side elevational view thereof as taken from line 2—2 of FIG. 1;
 FIG. 6 is a right side elevational view thereof as taken from line 6—6 of FIG. 1.

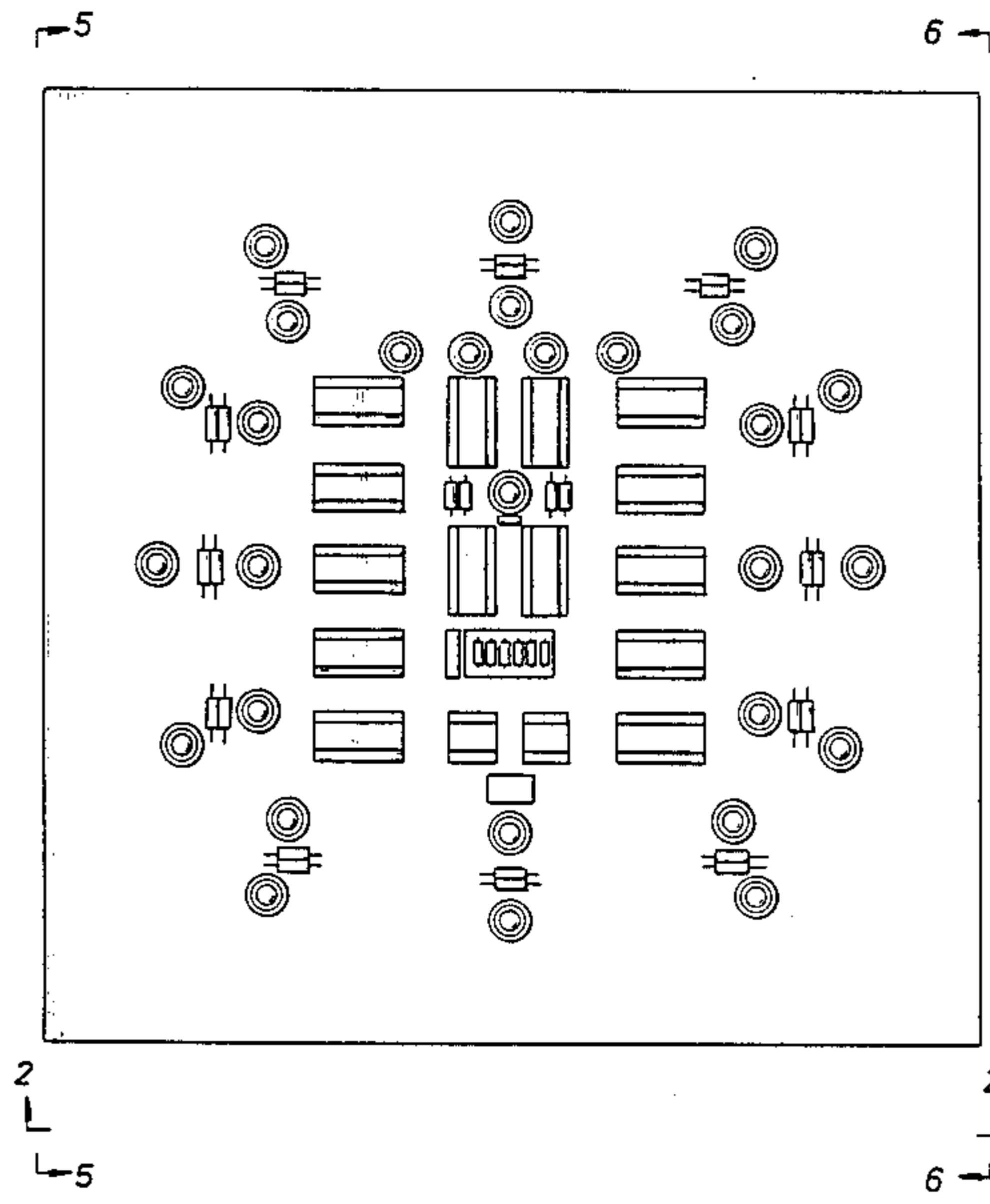
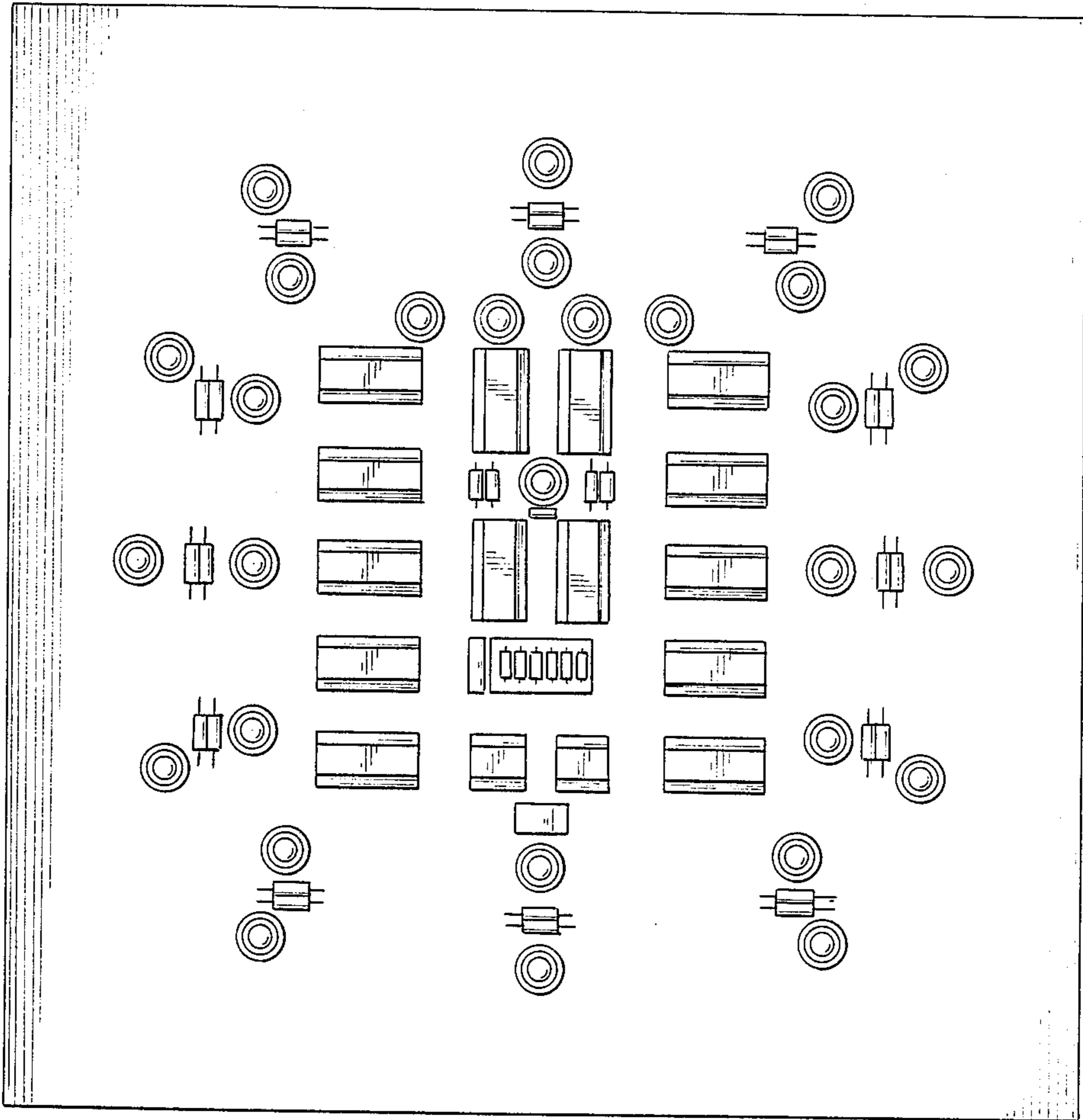


FIG. 1

5

6



2
5

2
6

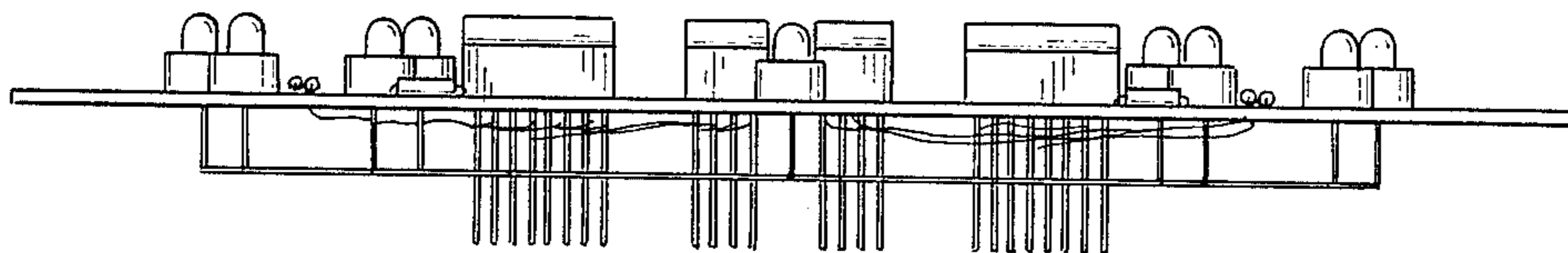


FIG. 2

FIG. 3

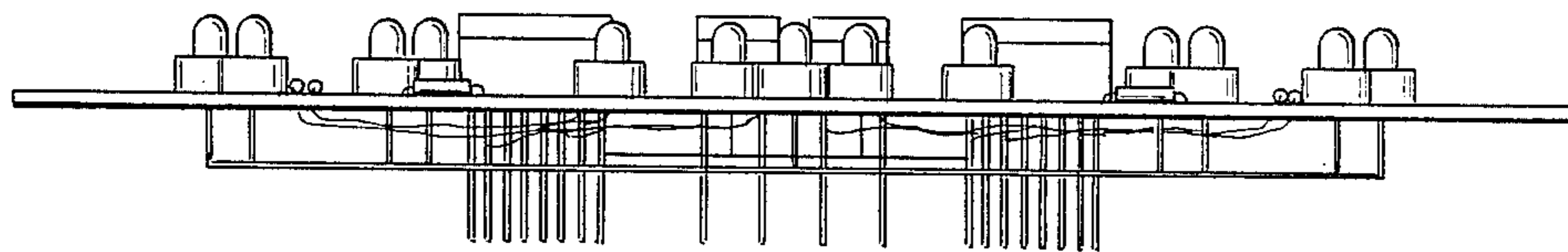
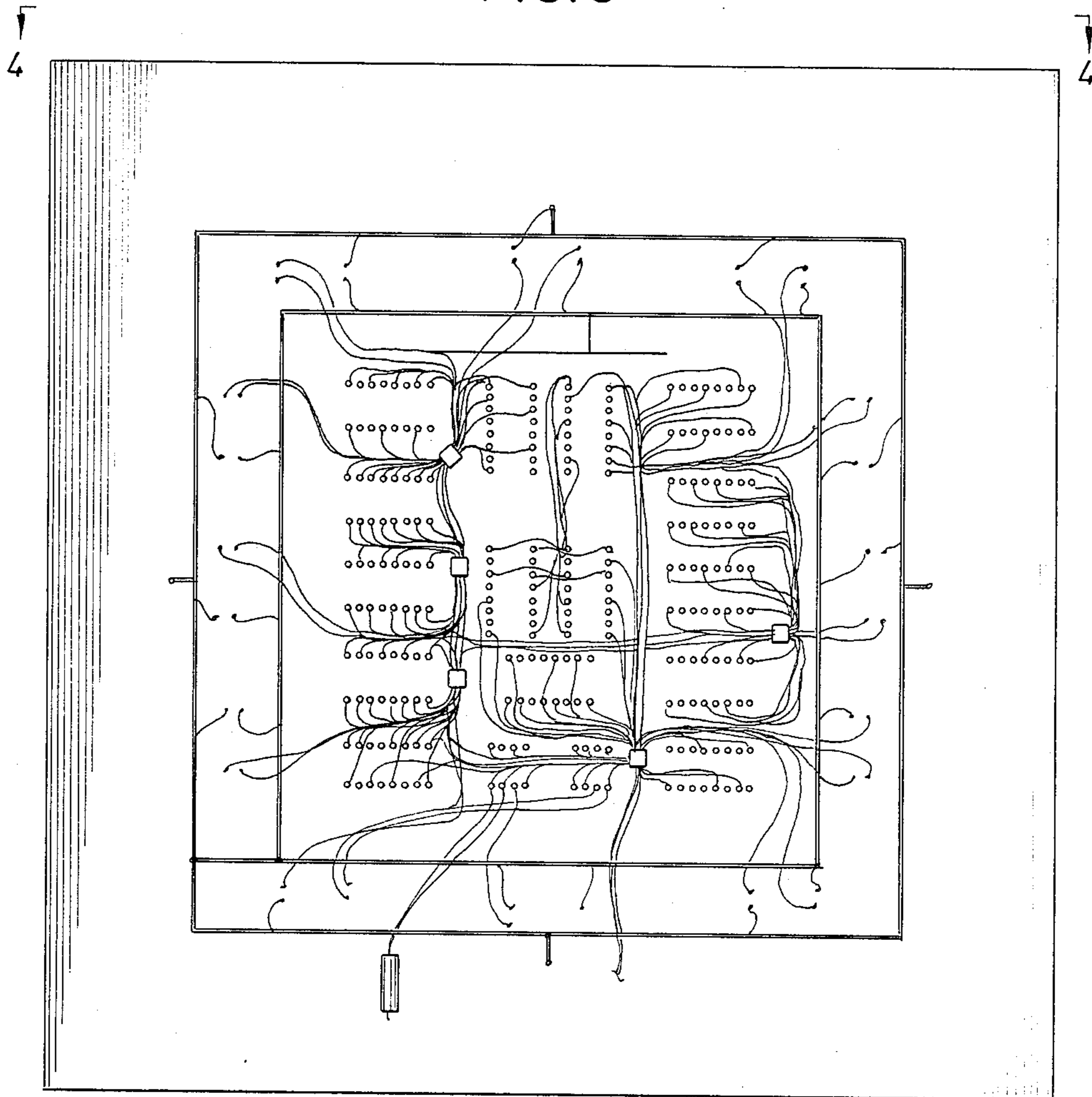


FIG. 4

FIG. 5

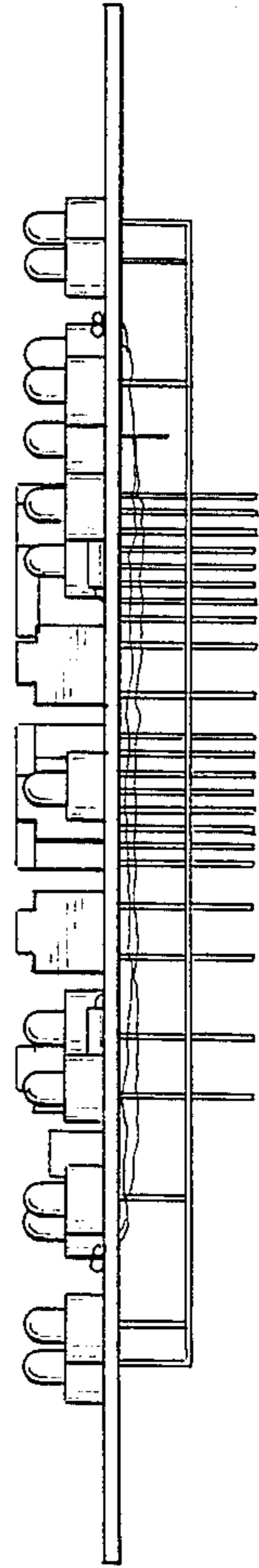
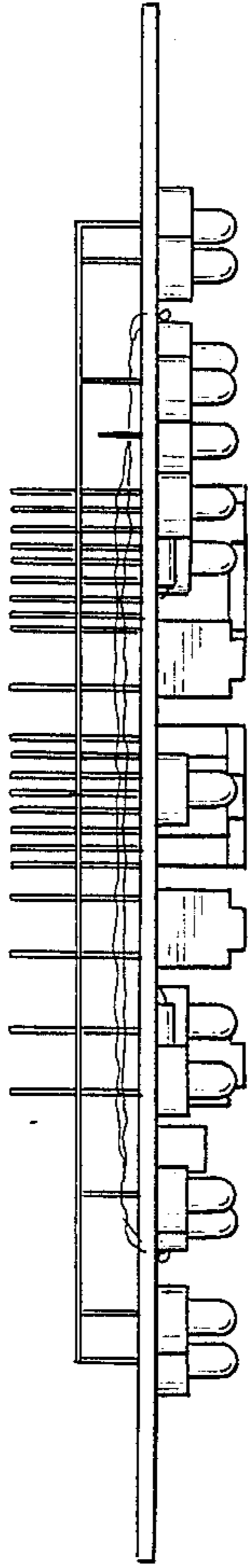


FIG. 6