

[54] ALARM DISPLAY PROCESSOR

D. 281,401 11/1985 Perrins D10/106 X

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OTHER PUBLICATIONS

Japanese Design Registration No. 573420, registered Jan. 25, 1982, published Mar. 25, 1982.

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Alarm Processor in the catalog of Colorado Electro-Optics, Inc. published Sep. 1982.

[**] Term: 14 Years

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[22] Filed: Jan. 4, 1985

[52] U.S. Cl. D10/106

[58] Field of Search D10/104, 106; 340/539, 340/546, 311; 455/347, 353, 351

[57] CLAIM

The ornamental design for an alarm display processor, as shown and described.

[56] References Cited

DESCRIPTION

U.S. PATENT DOCUMENTS

D. 246,034	10/1977	Sparks et al.	D10/106
D. 249,248	9/1978	Gibbs	D10/106
D. 249,249	9/1978	Gibbs	D10/106
D. 261,628	11/1981	Schaeffer	D10/106
D. 264,194	5/1982	Jaretsky	D10/106
D. 272,050	1/1984	Gibbs et al.	D10/106
D. 273,573	4/1984	Aulicino	D10/106
D. 273,786	5/1984	Owerko	D10/106 X

FIG. 1 is a front, top and right side perspective view of an alarm display processor showing our new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a top plan view thereof; FIG. 4 is a right side elevational view thereof, the left side being a mirror image of the right; FIG. 5 is a rear elevational view thereof; and FIG. 6 is a bottom plan view thereof.

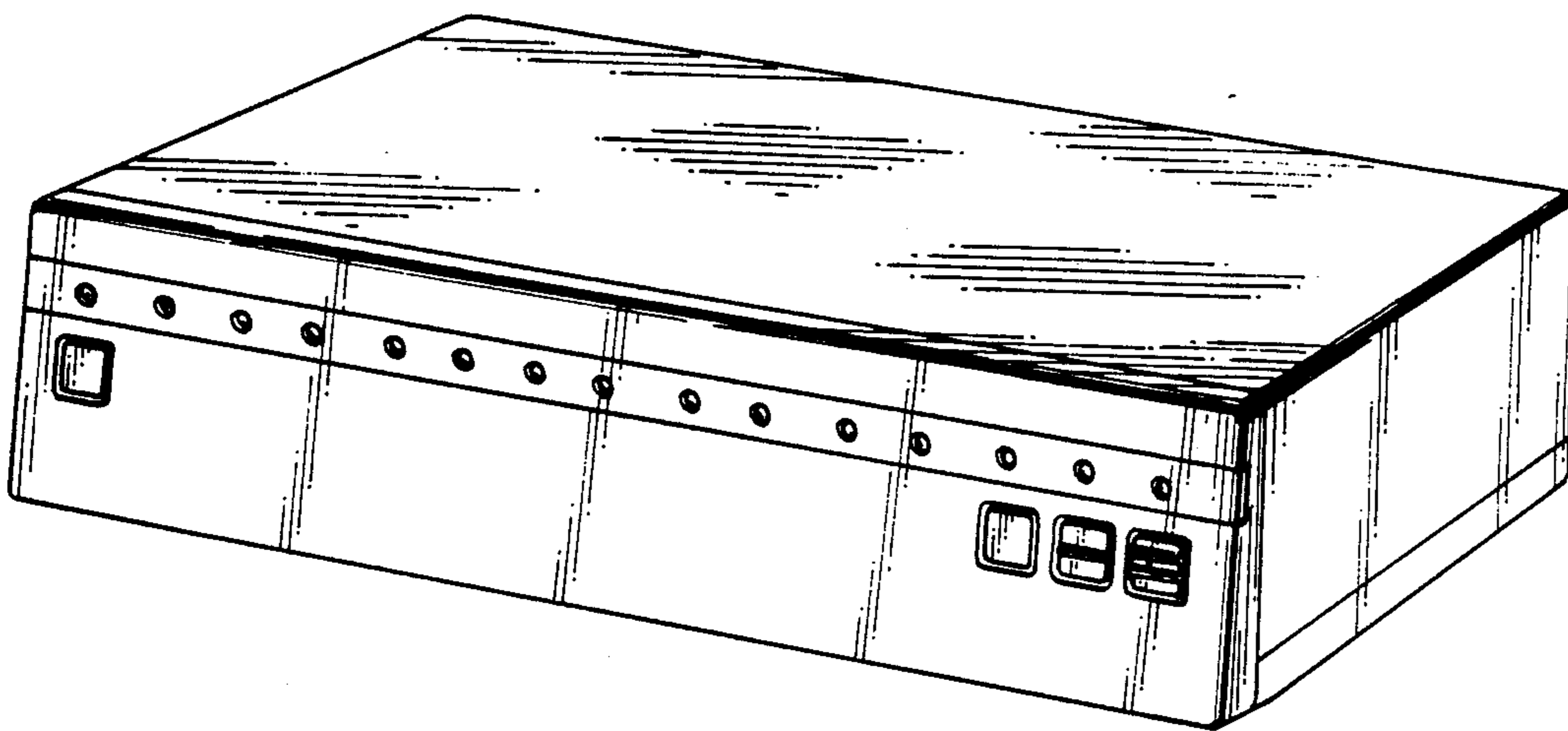


FIG. 1

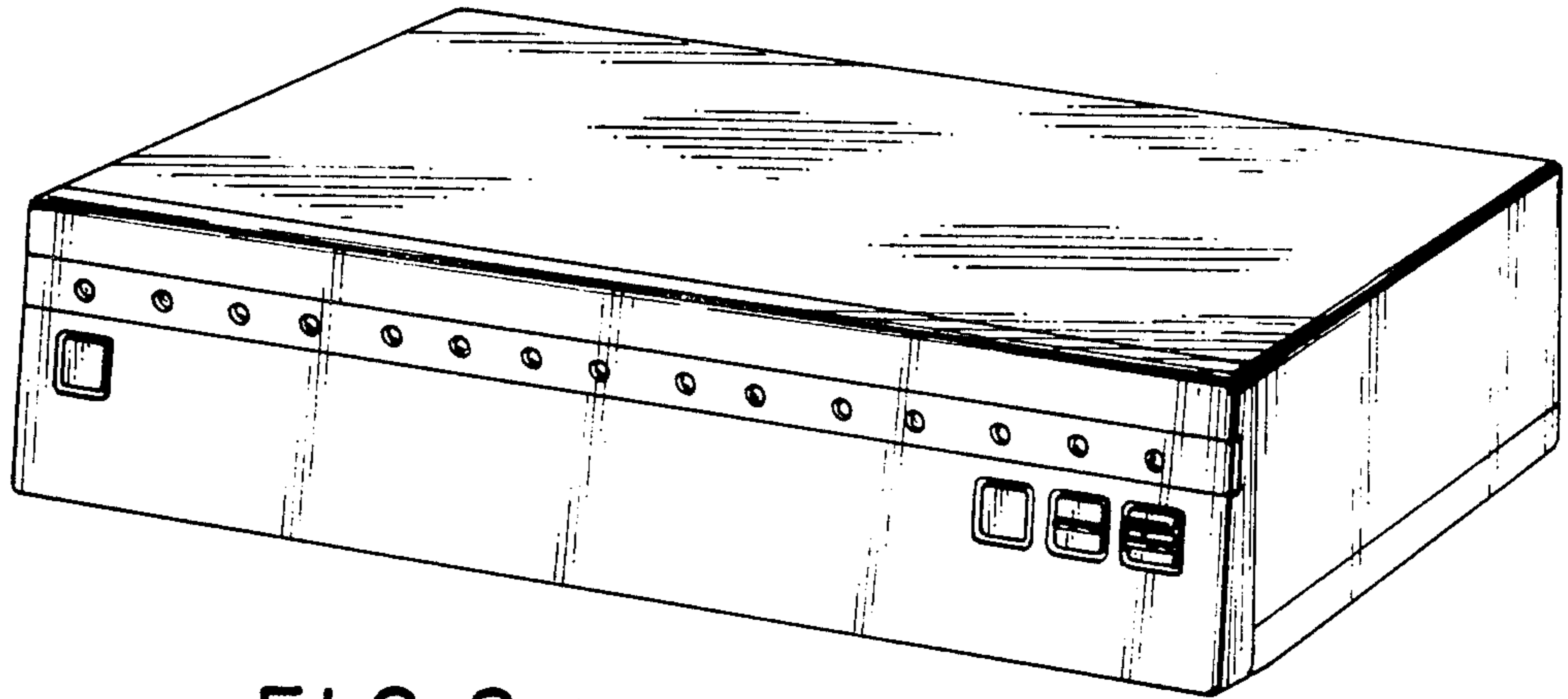


FIG. 2

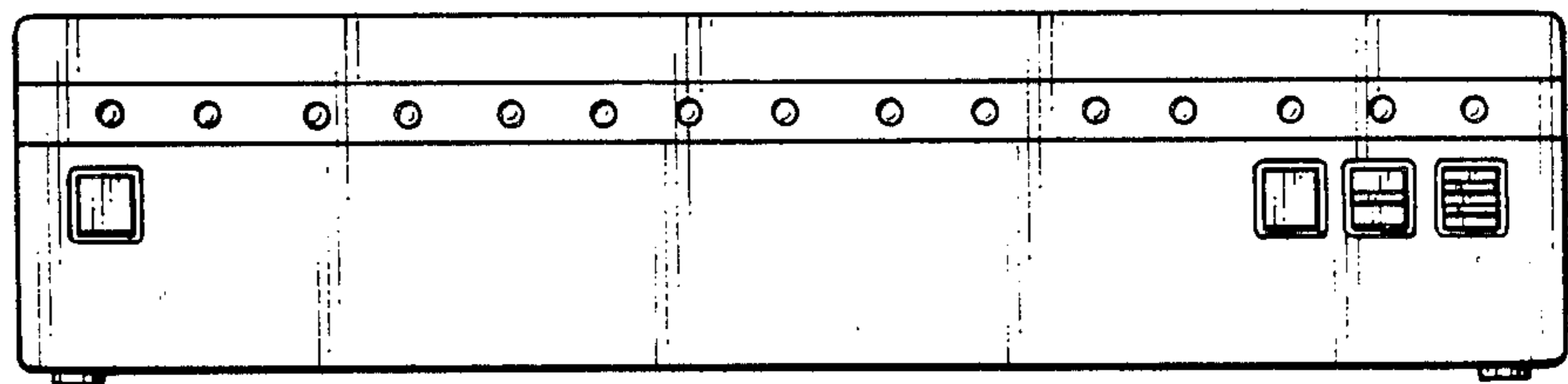


FIG. 3

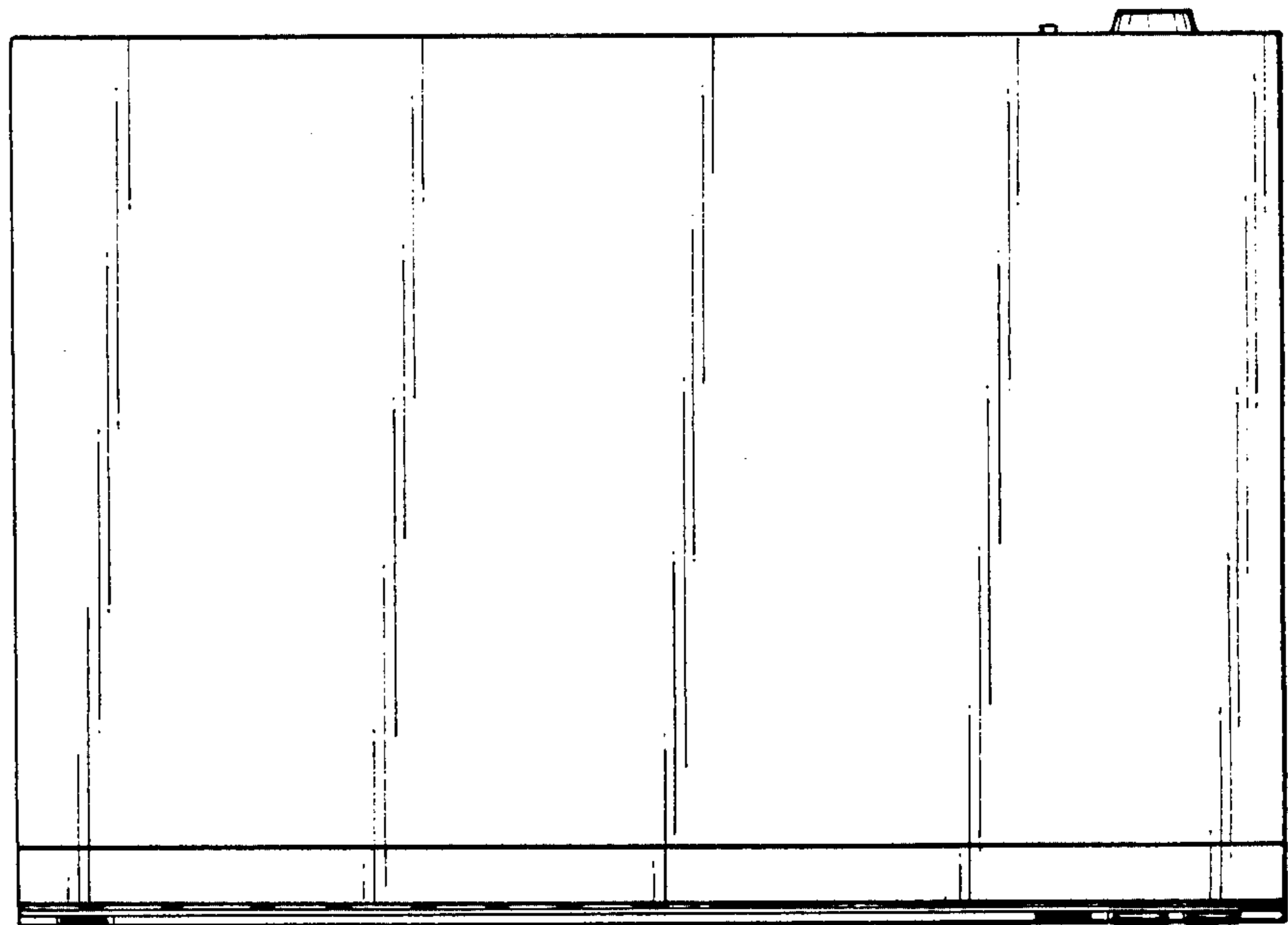


FIG. 4

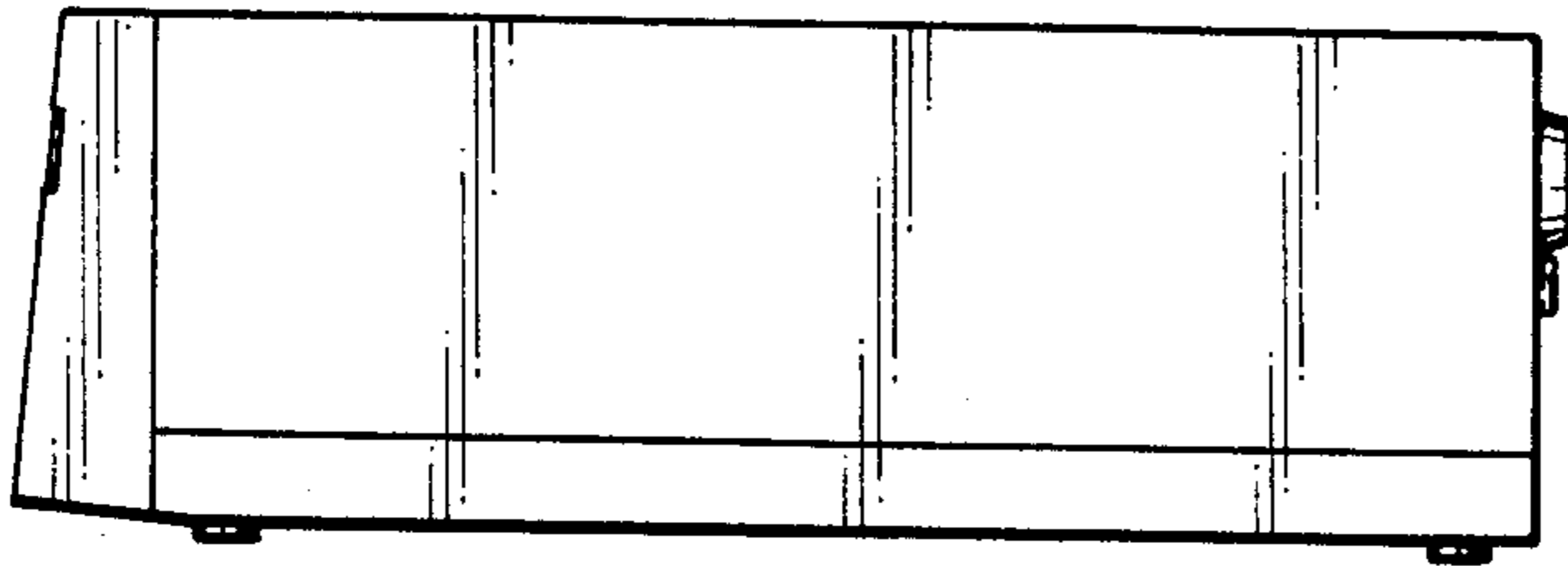


FIG. 5

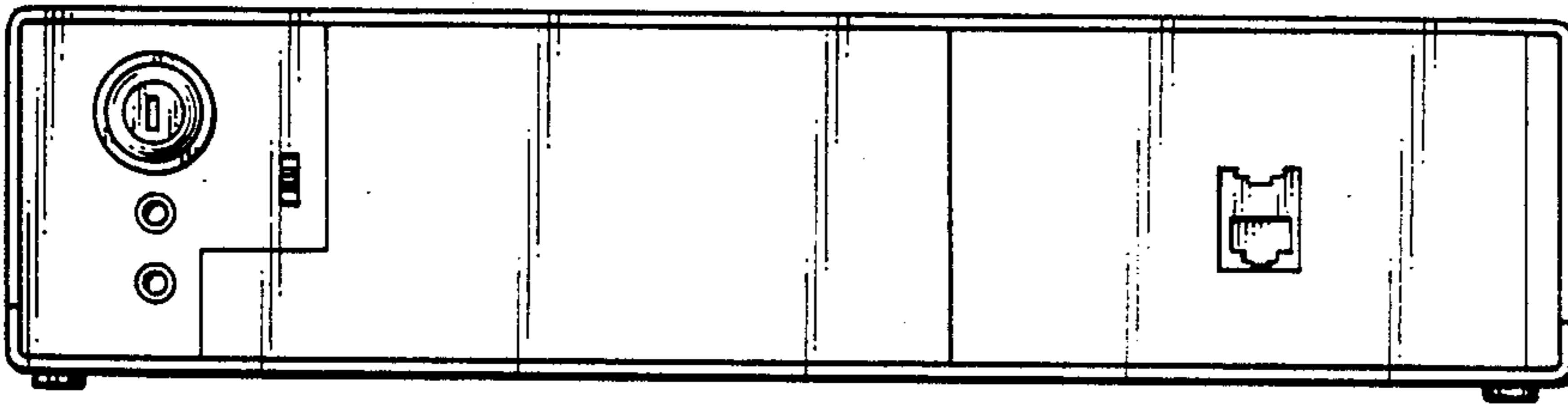


FIG. 6

