

[54] **DATA PROCESSING LOGIC UNIT**

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[73] **Assignee: International Business Machines Corporation, Armonk, N.Y.**

[**] **Term: 14 Years**

[21] **Appl. No.: 675,929**

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[30] **Foreign Application Priority Data**

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[52] **U.S. Cl. D14/114**

[58] **Field of Search D14/100, 106, 107, 113, D14/114; 340/700, 711, 720; D13/40, 41; 364/708; 174/52 R**

[56] **References Cited**

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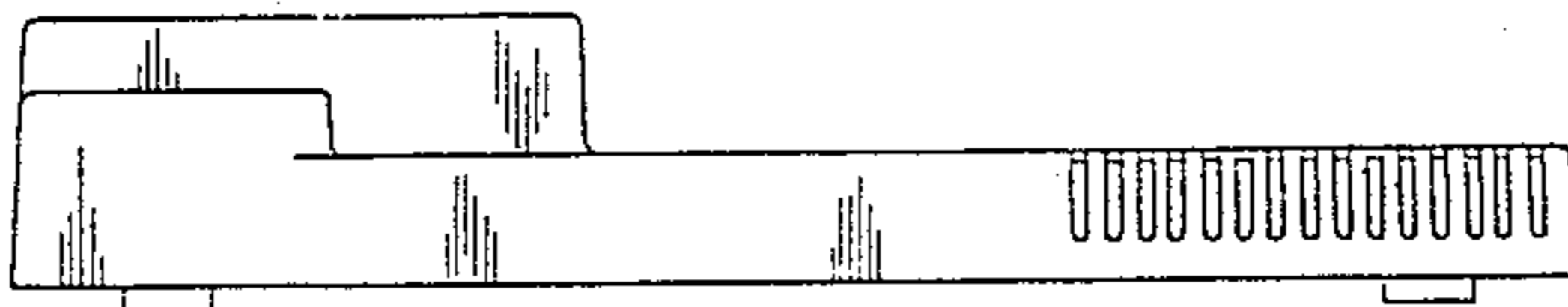
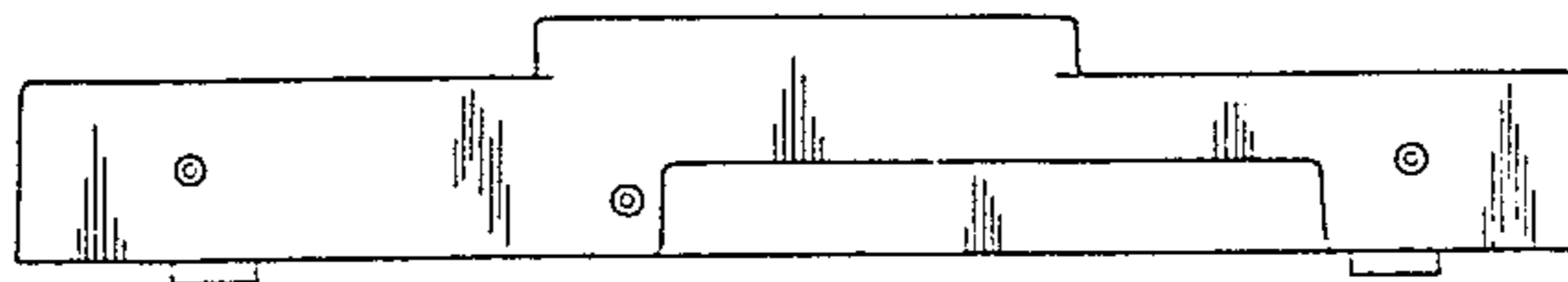
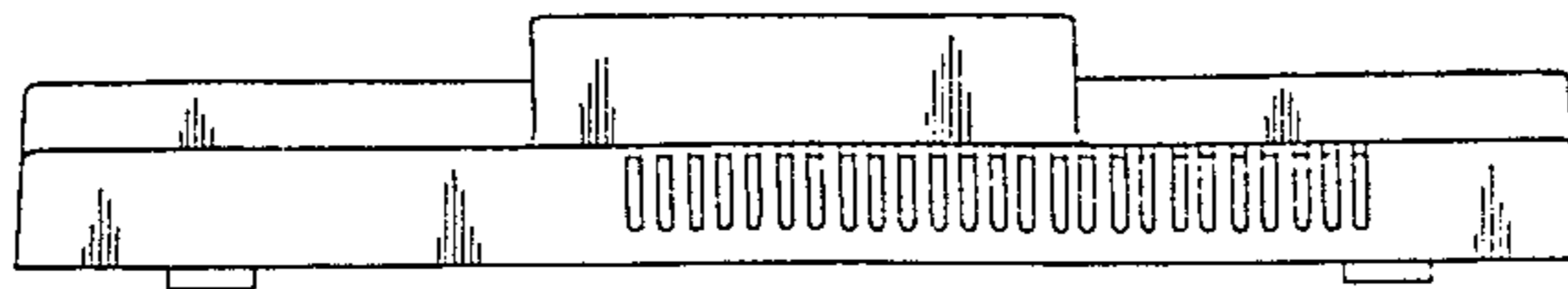
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[57] **CLAIM**

The ornamental design for a data processing logic unit, substantially as shown.

DESCRIPTION

FIG. 1 is a front elevational view of a data processing logic unit showing my new design;
FIG. 2 is a rear elevational view thereof;
FIG. 3 is a left side elevational view thereof;
FIG. 4 is a right side elevational view thereof;
FIG. 5 is a top plan view thereof;
FIG. 6 is a bottom plan view thereof.



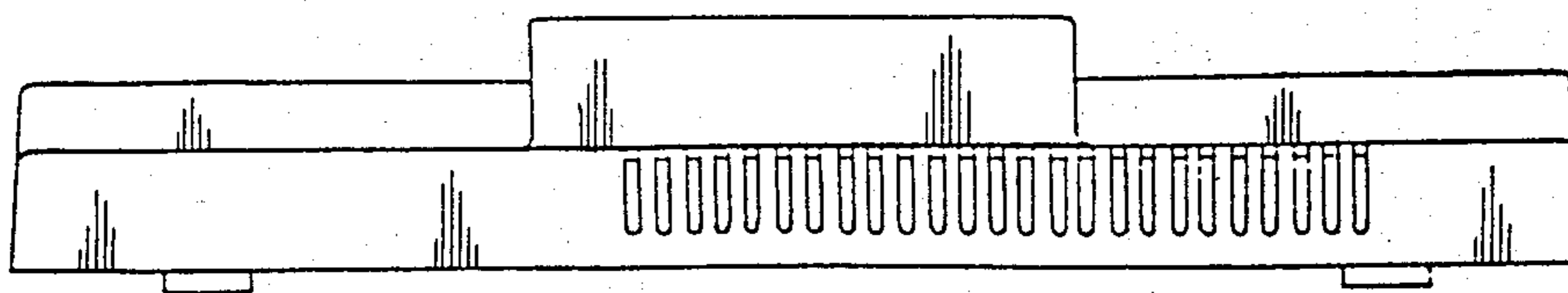


FIG. 1

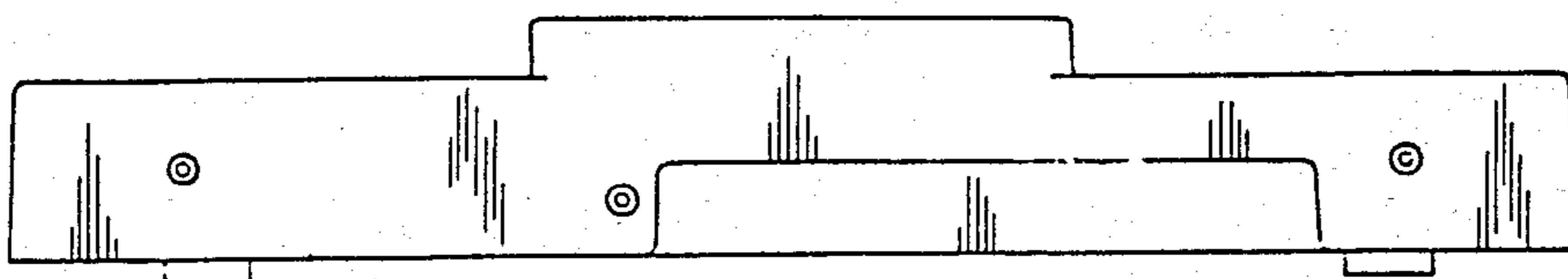


FIG. 2

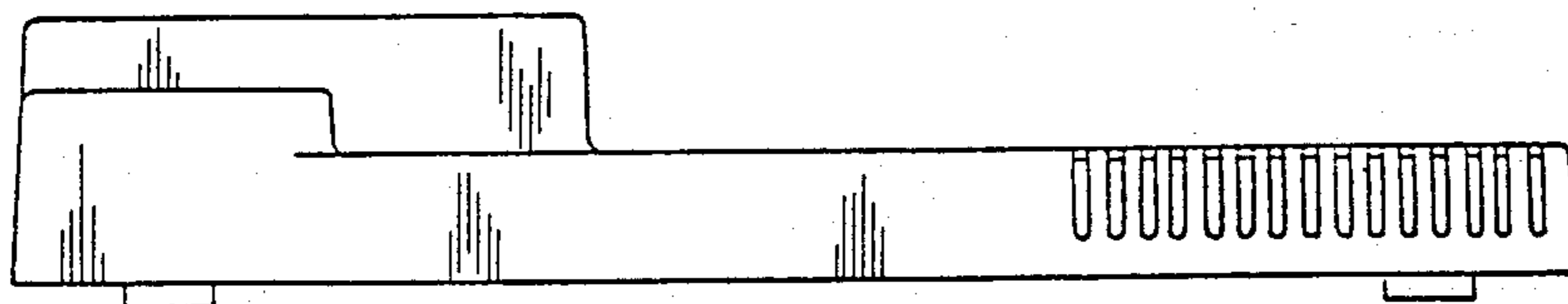


FIG. 3

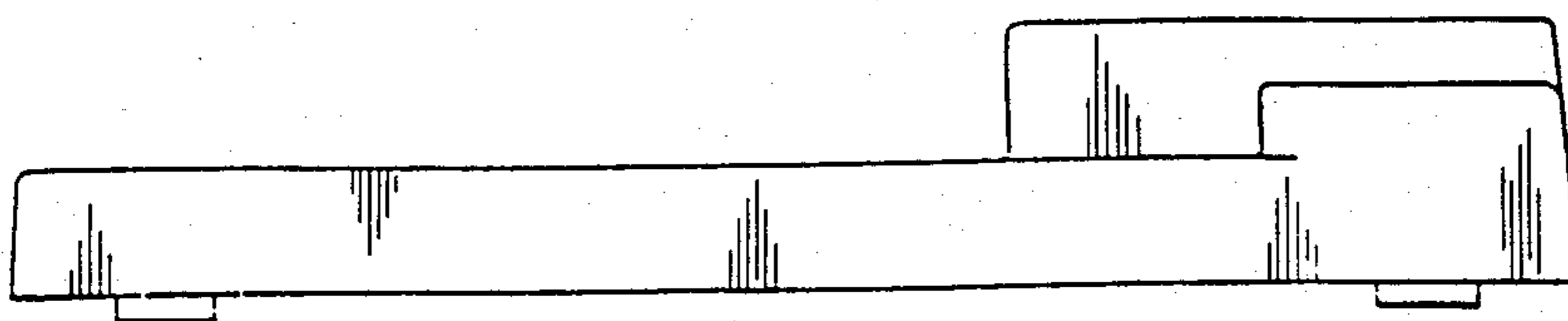


FIG. 4

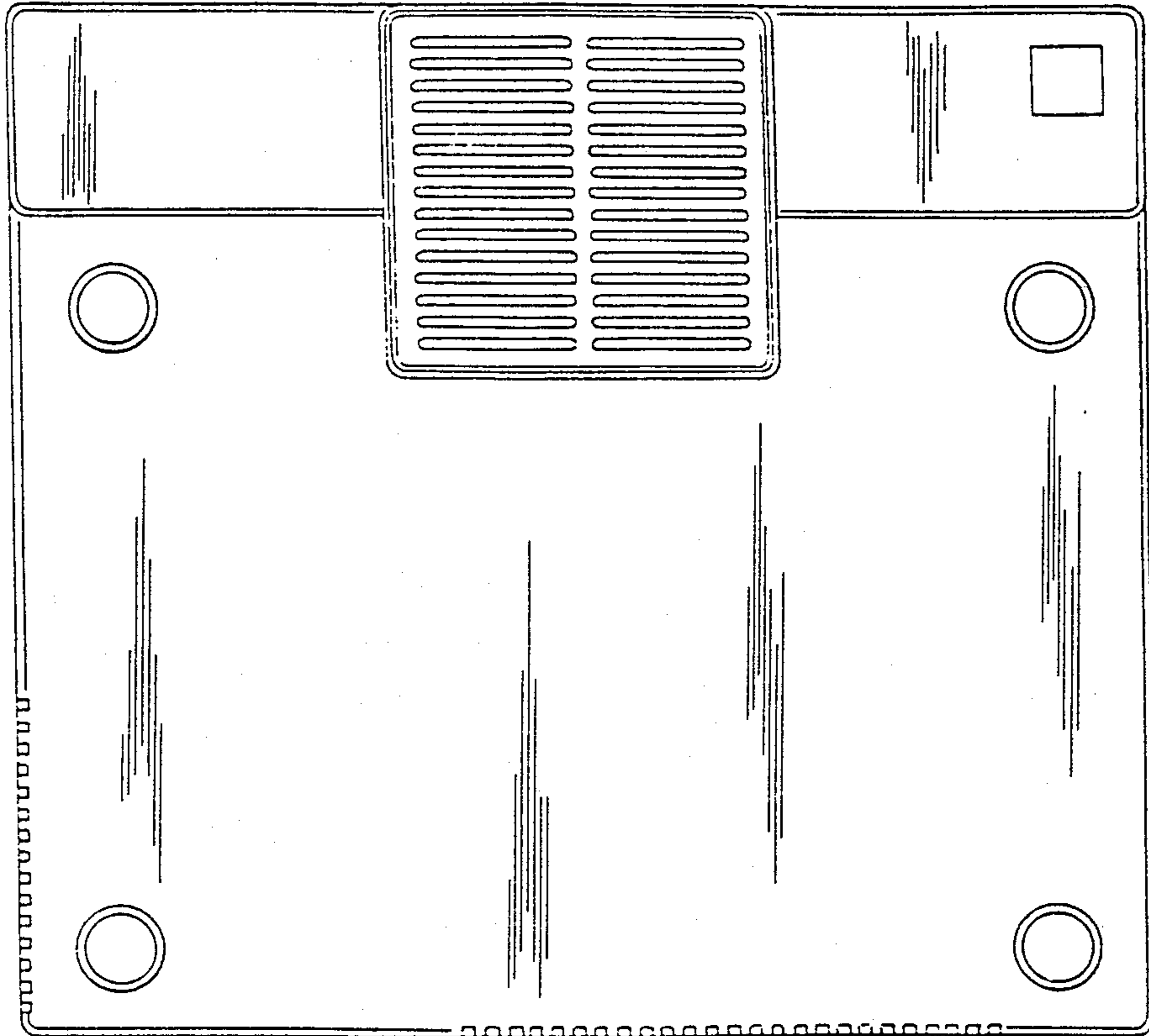


FIG. 5

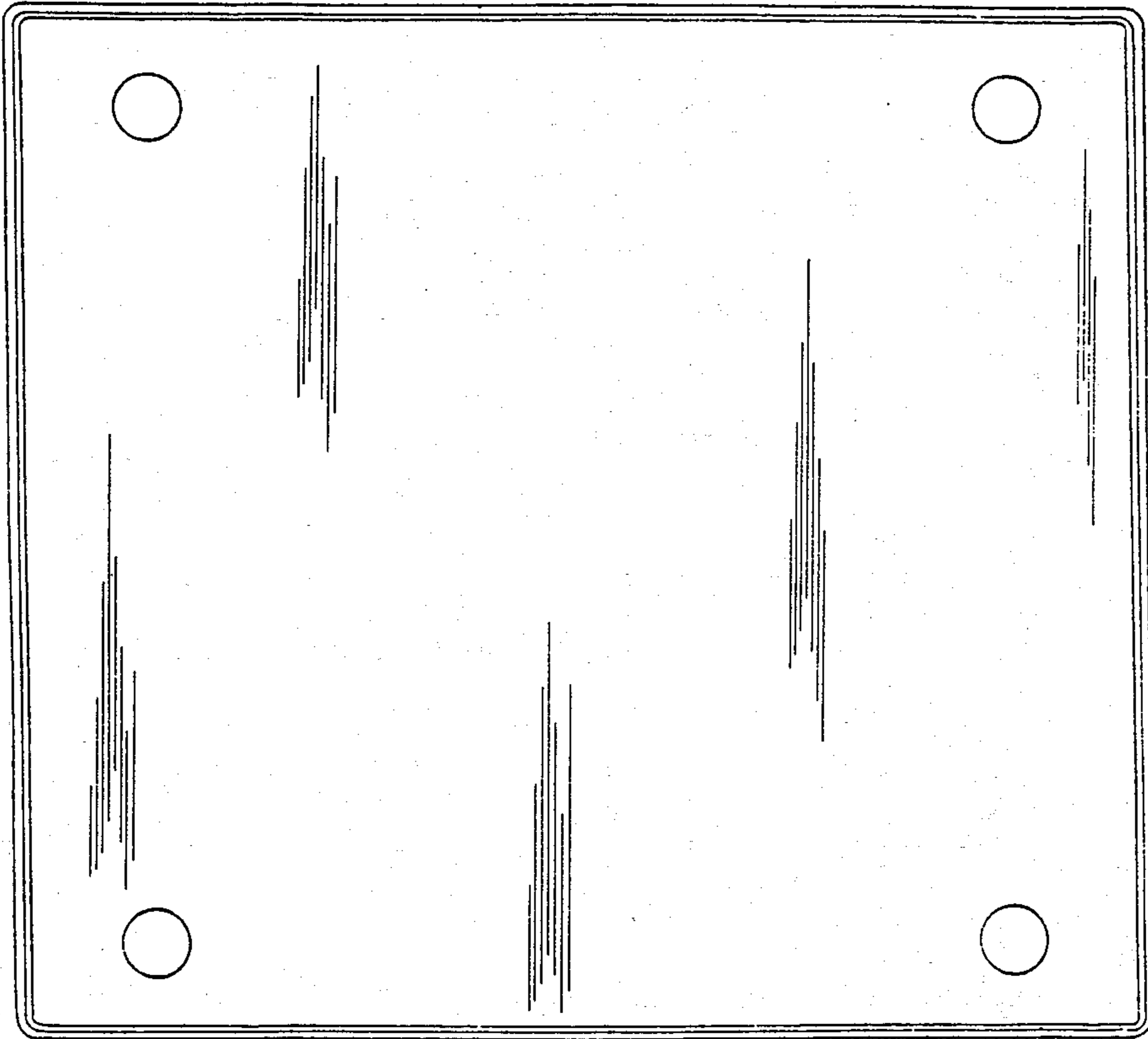


FIG. 6