

[54] CIRCUIT BOARD COVER

3,316,460 4/1967 Scoville 361/415
4,092,698 5/1978 Brefka 361/346

[76] Inventor: Ross Capawana, 3250 Pollux Ave.,
Las Vegas, Nev. 89102

[**] Term: 14 Years

Primary Examiner—Wallace R. Burke
Assistant Examiner—Ruth E. Takemoto
Attorney, Agent, or Firm—Seiler, Quirk & Tratos

[21] Appl. No.: 642,500

[57] CLAIM

[22] Filed: Aug. 20, 1984

The ornamental design for a circuit board cover, as shown.

[52] U.S. Cl. D13/40

[58] Field of Search D13/34, 35, 40-41,
D13/99; 361/331-332, 346, 352-353, 358,
397-399, 412, 415, 426-429; 174/50, 51, 52 R;
200/293-294, 296, 302-303

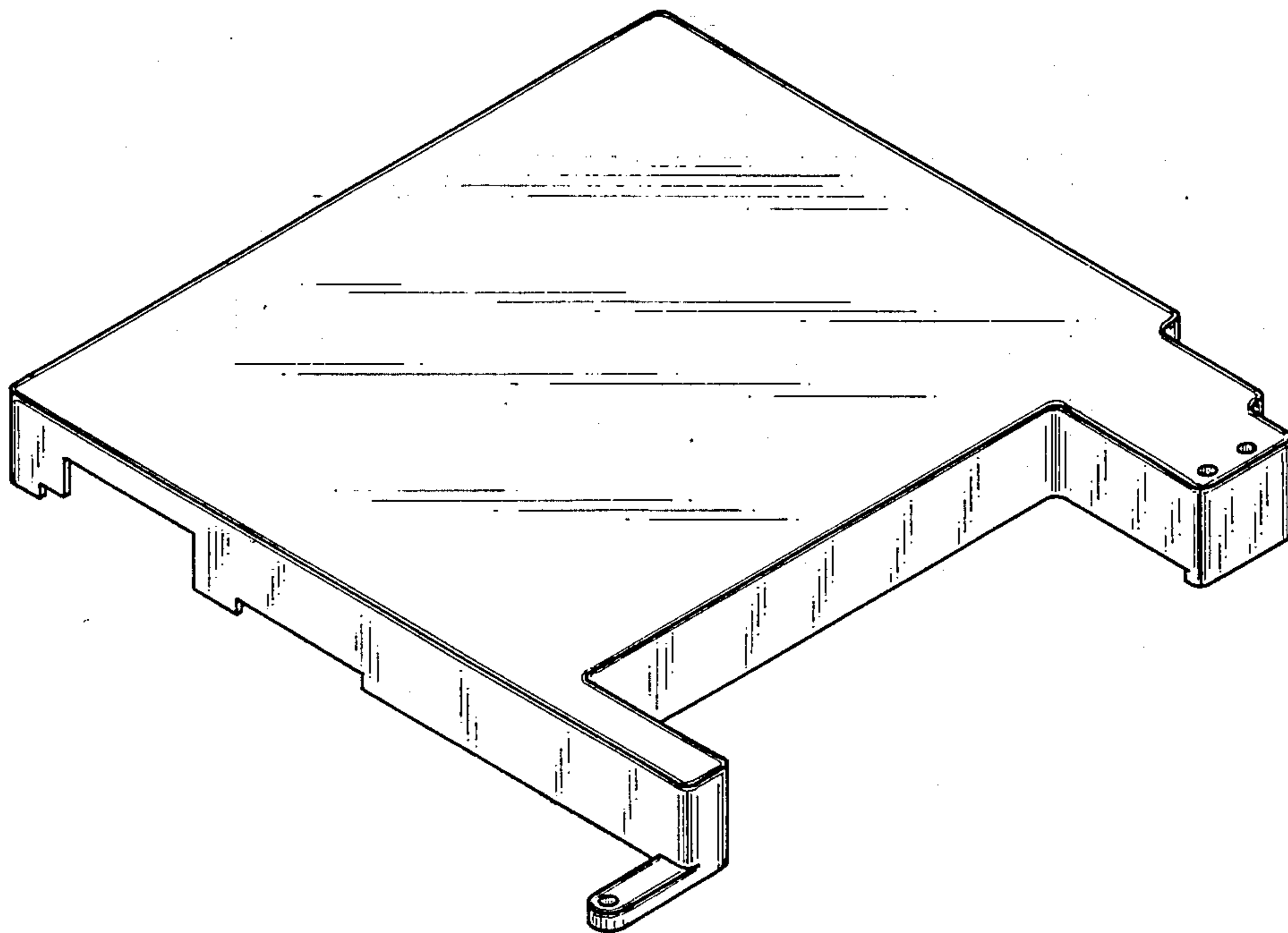
DESCRIPTION

[56] References Cited

U.S. PATENT DOCUMENTS

D. 187,227 2/1960 Collins, Jr. D13/35
D. 257,723 12/1980 Stockdale D13/40
D. 272,340 1/1984 Korzik D13/41

FIG. 1 is a front perspective view of the circuit board cover showing my new design;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a rear elevational view thereof;
FIG. 4 is a top plan view thereof;
FIG. 5 is a bottom plan view thereof;
FIG. 6 is a side elevational view thereof; and
FIG. 7 is a side elevational view thereof of the opposite side of FIG. 6.



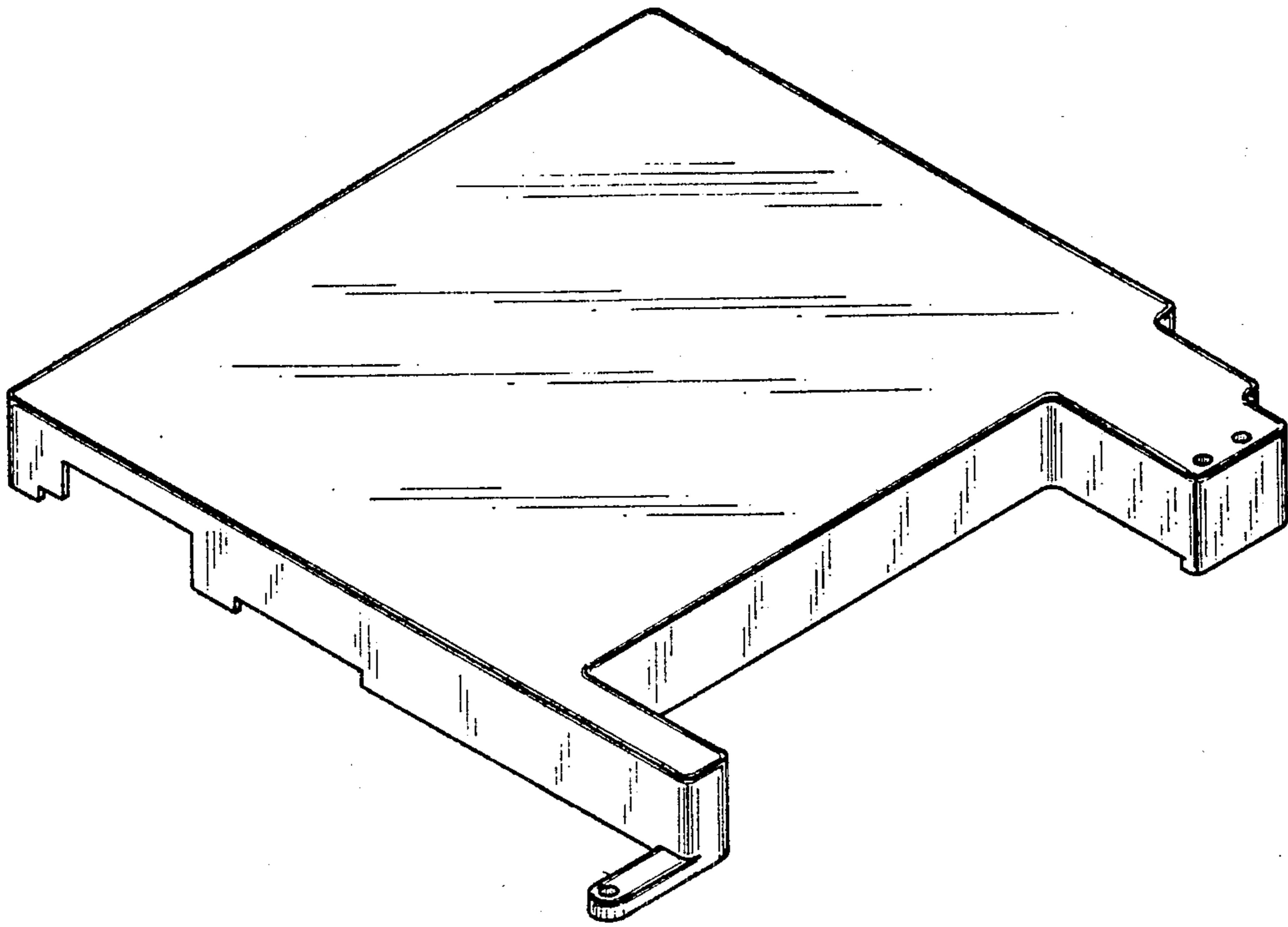


FIG. 1

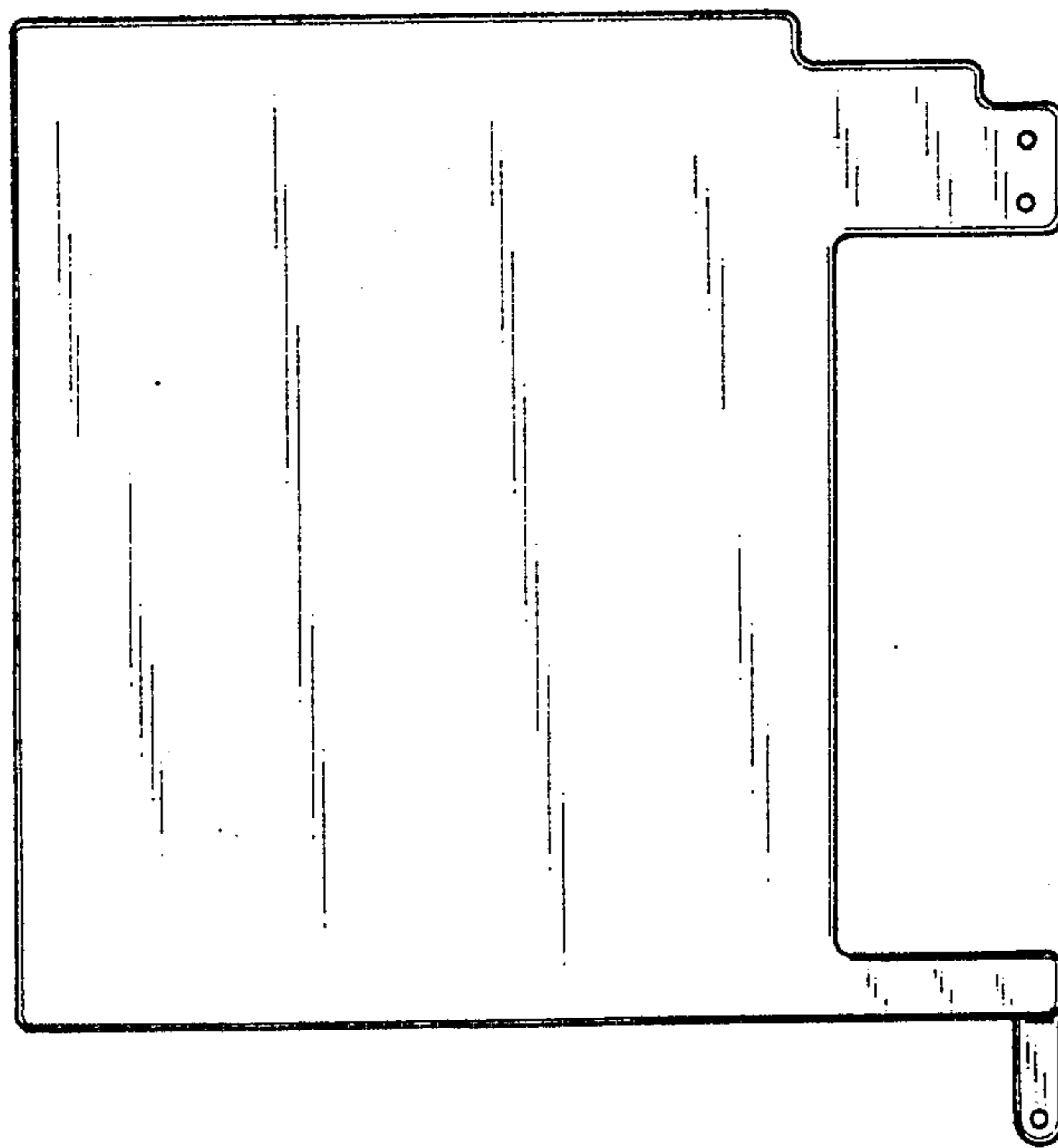


FIG. 2

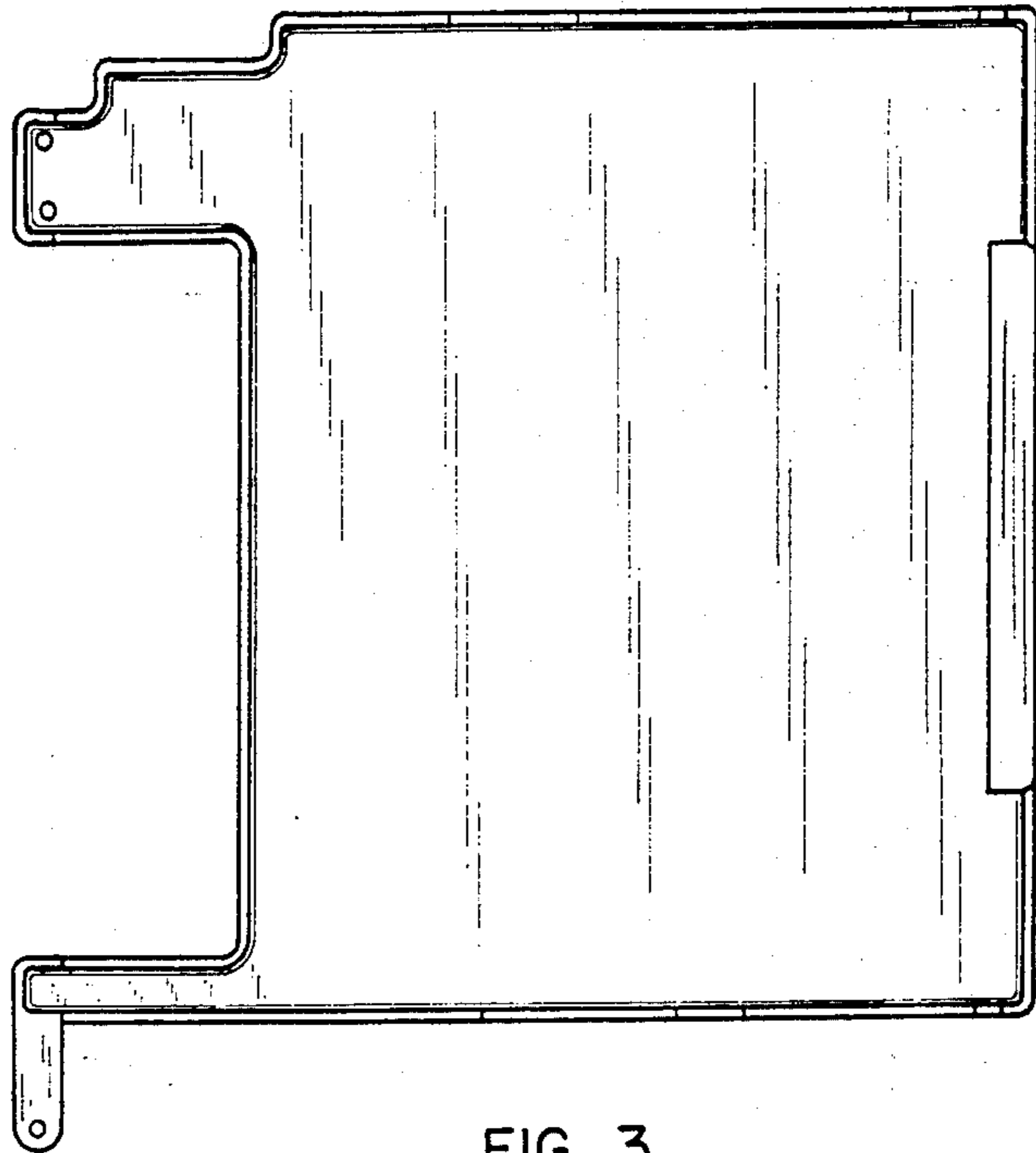


FIG. 3



FIG. 4

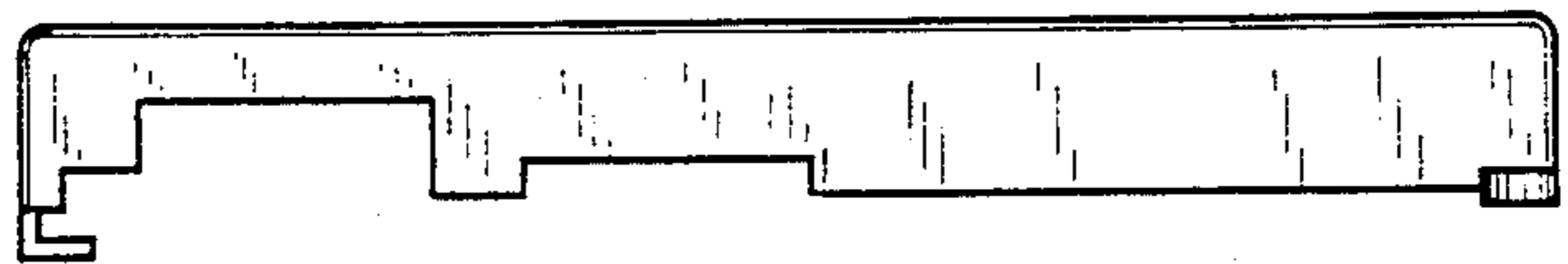


FIG. 5



FIG. 6

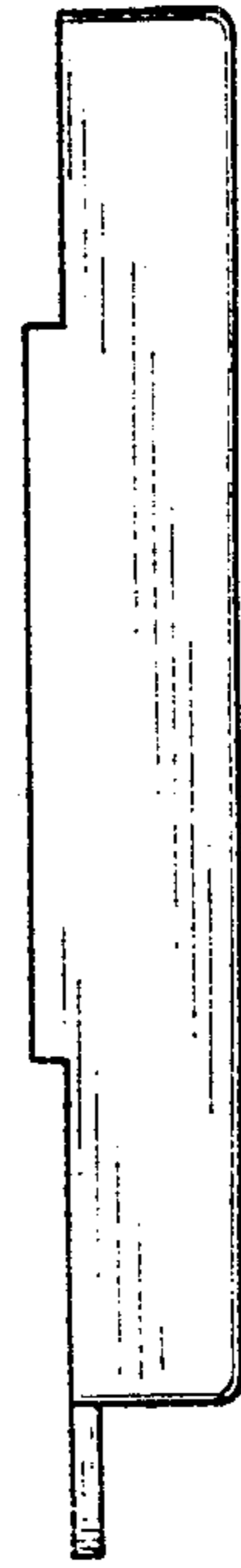


FIG. 7