

United States Patent [19]

Johnson

[11] Patent Number: **Des. 286,081**

[45] Date of Patent: **** Oct. 7, 1986**

[54] **COMBINED HIGH INTENSITY LIGHT AND HOLDER FOR SILICON WAFERS OR SIMILAR ARTICLES**

2,559,627 7/1951 Johnson 362/197 X
2,725,788 12/1955 Pflieger D26/49 X

[75] Inventor: **Ardelle R. Johnson**, Minnetonka, Minn.

FOREIGN PATENT DOCUMENTS

294325 9/1916 Fed. Rep. of Germany 362/198

[73] Assignee: **FSI Corporation**, Chaska, Minn.

Primary Examiner—Susan J. Lucas
Attorney, Agent, or Firm—Palmatier & Sjoquist

[**] Term: **14 Years**

[57] CLAIM

[21] Appl. No.: **613,429**

The ornamental design for a combined high intensity light and holder for silicon wafers or similar articles, as shown.

[22] Filed: **May 24, 1984**

[52] U.S. Cl. **D26/38; D26/43**

[58] Field of Search **D26/24, 37-51; 362/157, 158, 183-208, 804**

DESCRIPTION

[56] References Cited

U.S. PATENT DOCUMENTS

D. 182,053 2/1958 Weston et al. D26/38
D. 218,311 8/1970 Foster D26/38 X
2,222,879 11/1940 Porter D26/49 X
2,331,526 10/1943 Wappler 362/804 X

FIG. 1 is a front side elevation view of the combined high intensity light and holder for silicon wafers or similar articles;

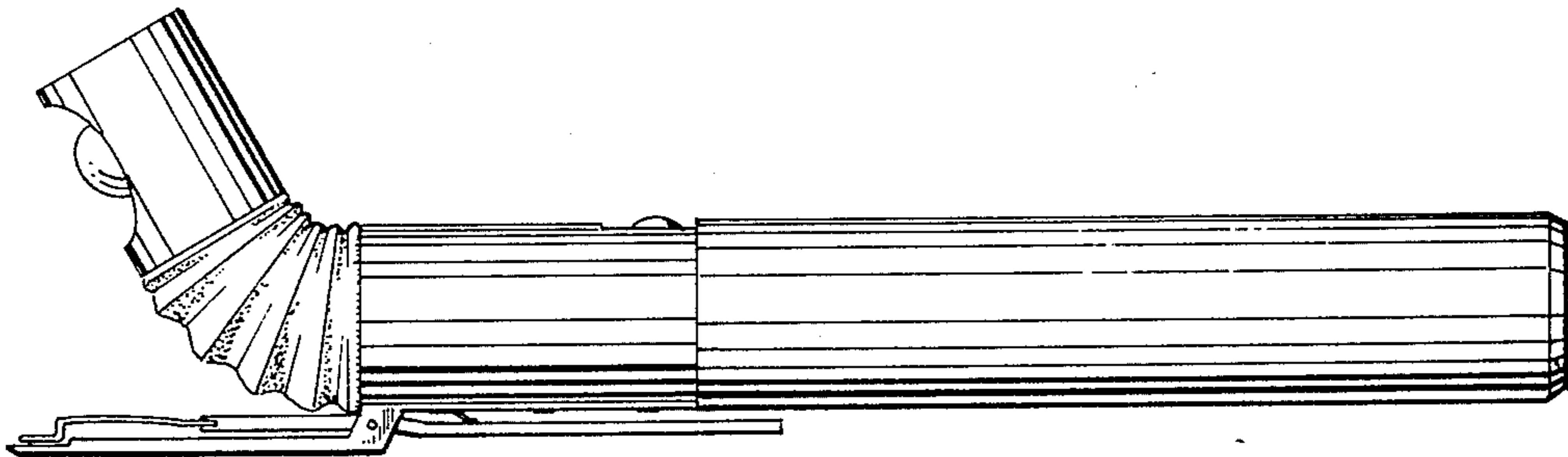
FIG. 2 is a top plan view thereof;

FIG. 3 is a bottom plan view thereof;

FIG. 4 is a rear side elevation view thereof;

FIG. 5 is an elevation view of the right end thereof; and

FIG. 6 is an elevation view of the left end thereof.



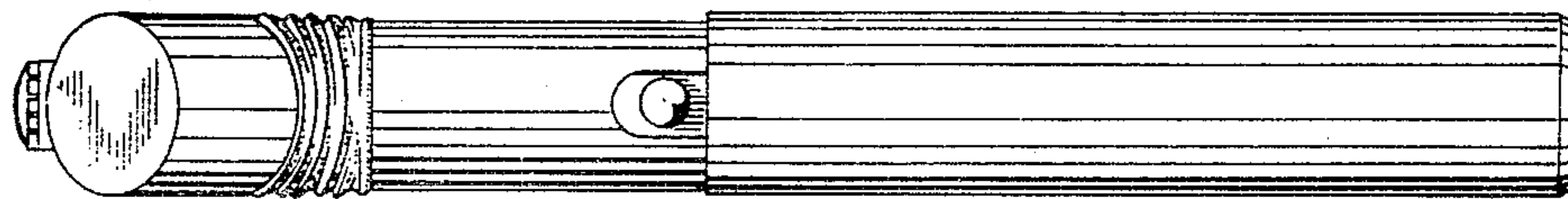


Fig. 2.

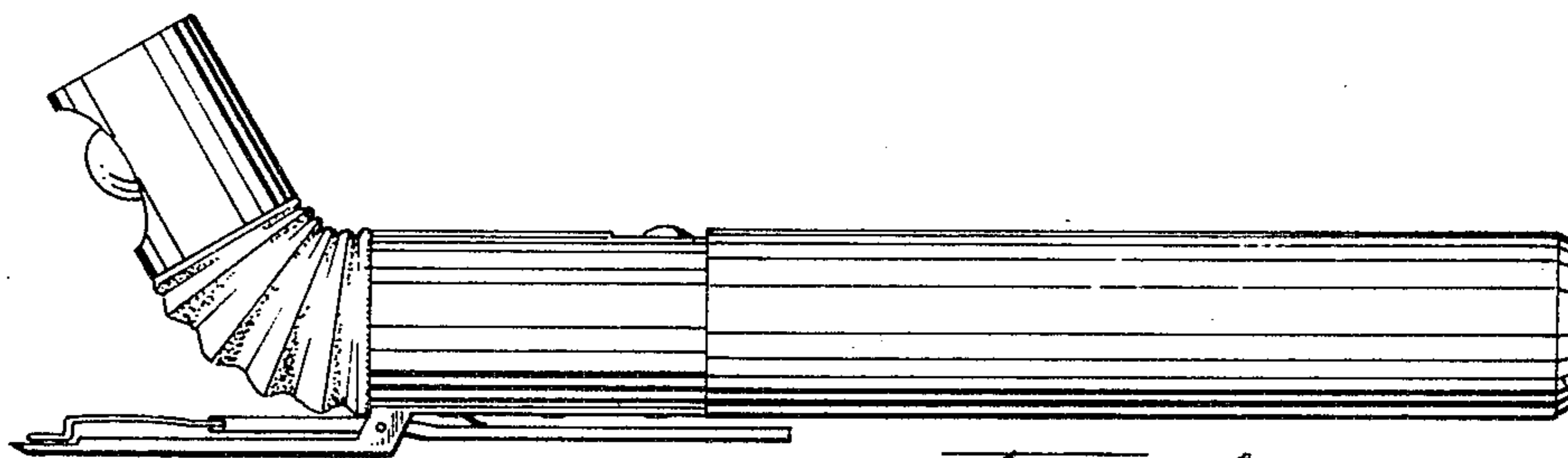


Fig. 1.

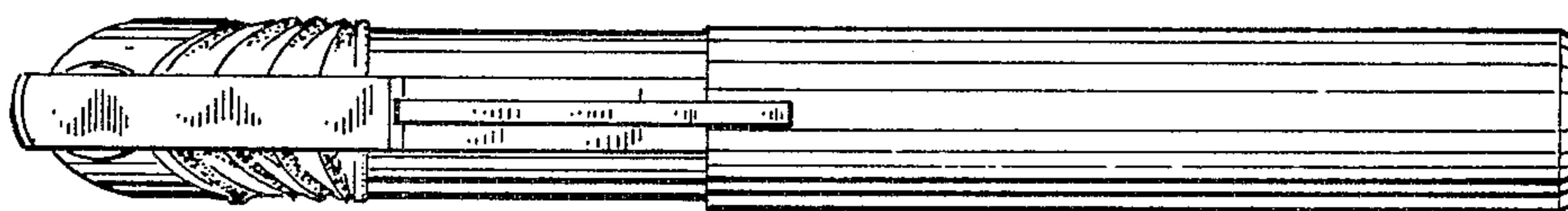


Fig. 3.

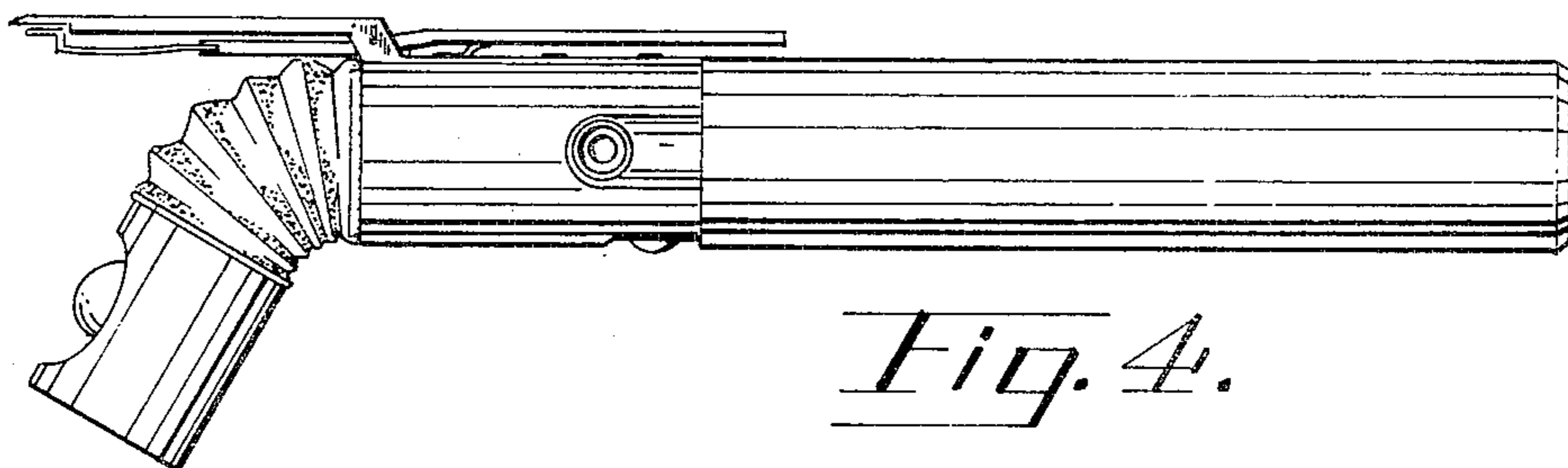


Fig. 4.

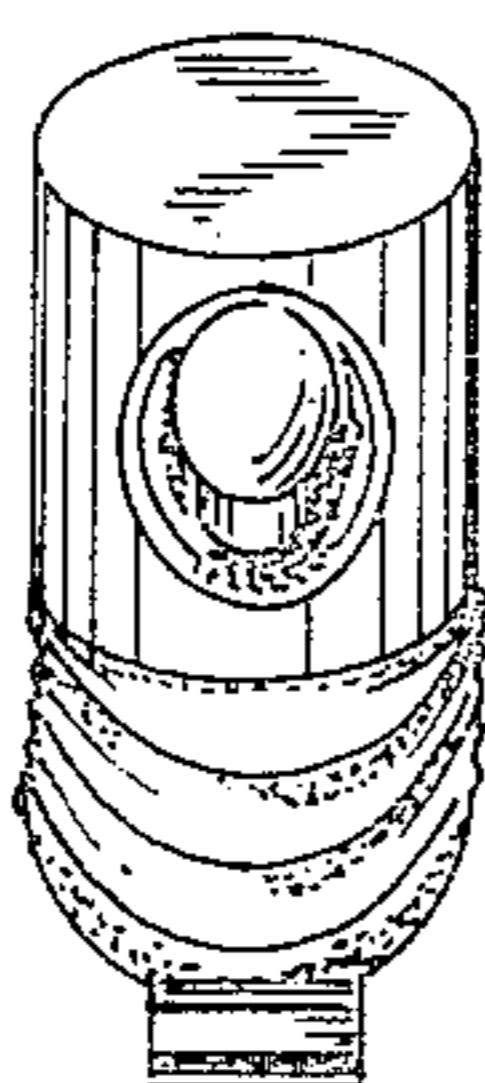


Fig. 5.

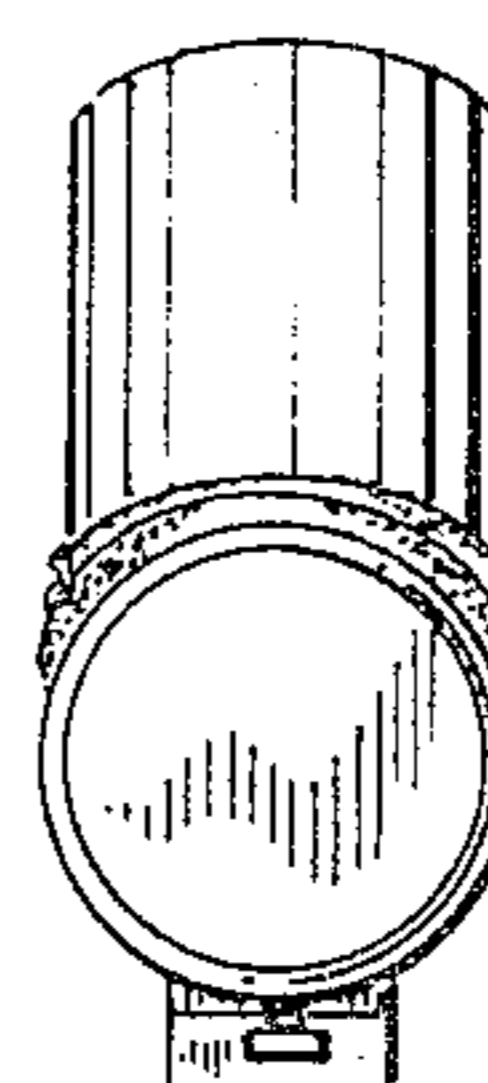


Fig. 6.