

[54] PROCESSOR UNIT FOR USE WITH COMPUTERS

[75] Inventor: Junichi Sakamoto, Osaka, Japan

[73] Assignee: Sharp Corporation, Osaka, Japan

[**] Term: 14 Years

[21] Appl. No.: 504,297

[22] Filed: Jun. 14, 1983

[30] Foreign Application Priority Data

Dec. 14, 1982 [JP] Japan 57-56207

[52] U.S. Cl. D14/100; D14/109

[58] Field of Search D14/100-109, D14/113-116; D13/40, 41

[56] References Cited

PUBLICATIONS

Computer Design, 11-1980, p. 213, Motorola Inc. Exorciser® II Unit.

Fortune Systems Brochure ©1982, Computer. Heathkit Catalog #801MO, ©1982, p. 4, 8-inch Floppy Disk System.

Rair Black Box Microcomputer Model 3/30 Flyer, 1981.

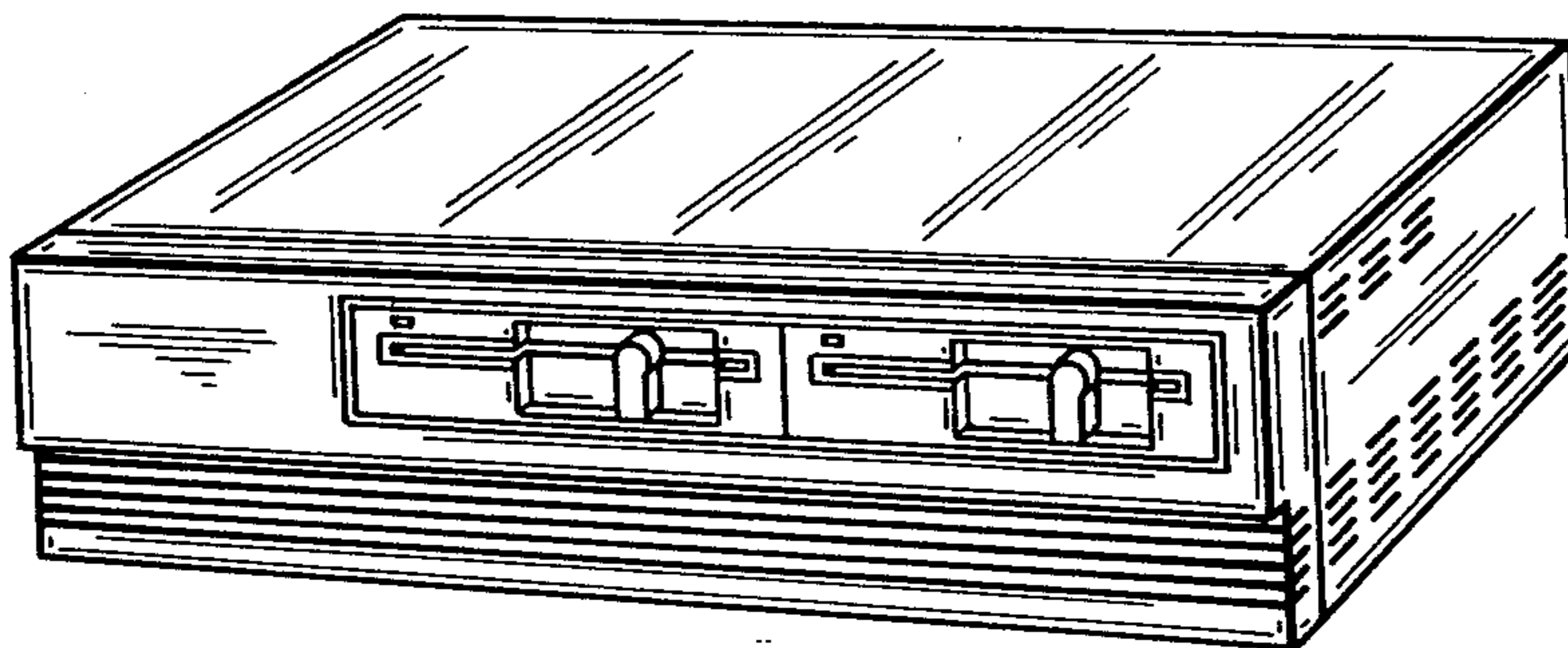
Primary Examiner—Susan J. Lucas
Attorney, Agent, or Firm—Flehr, Hohbach, Test, Albritton & Herbert

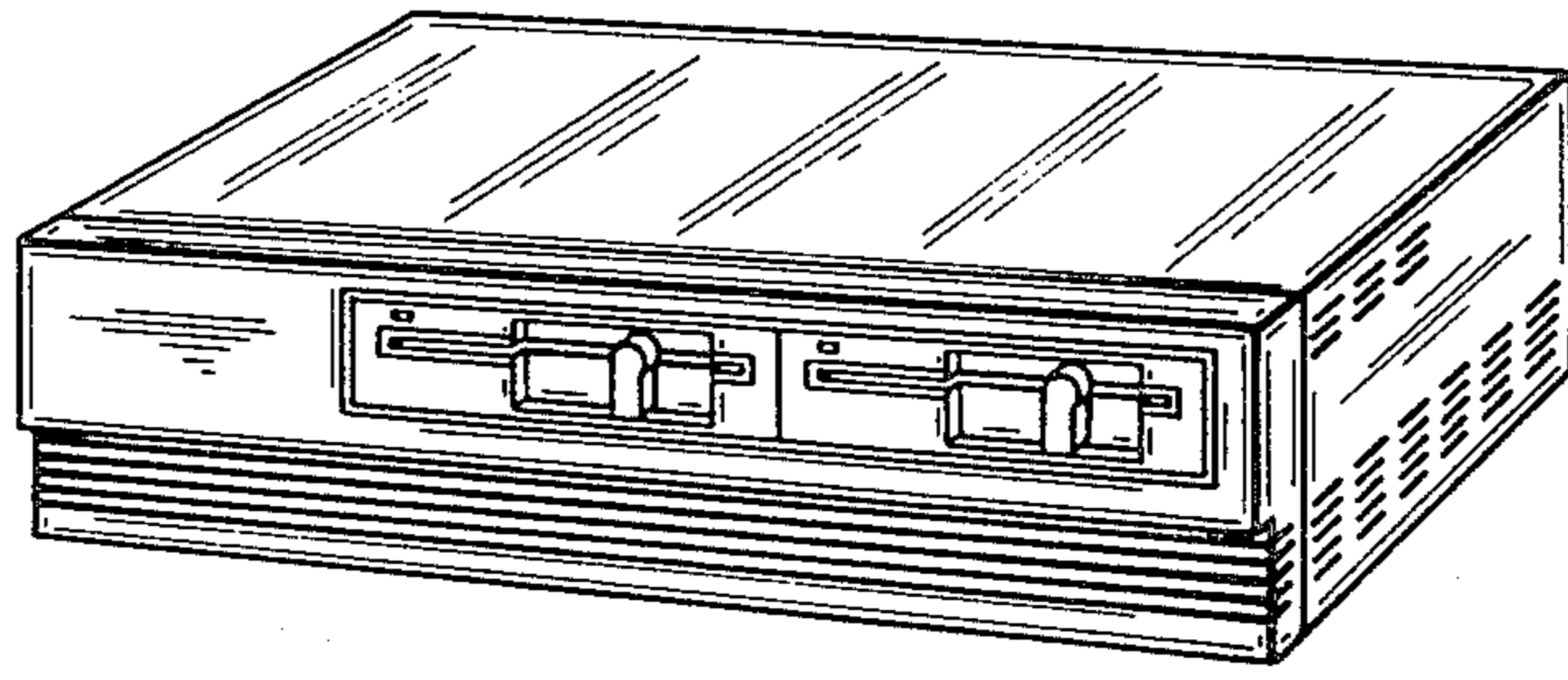
[57] CLAIM

The ornamental design for a processor unit for use with computers, as shown and described.

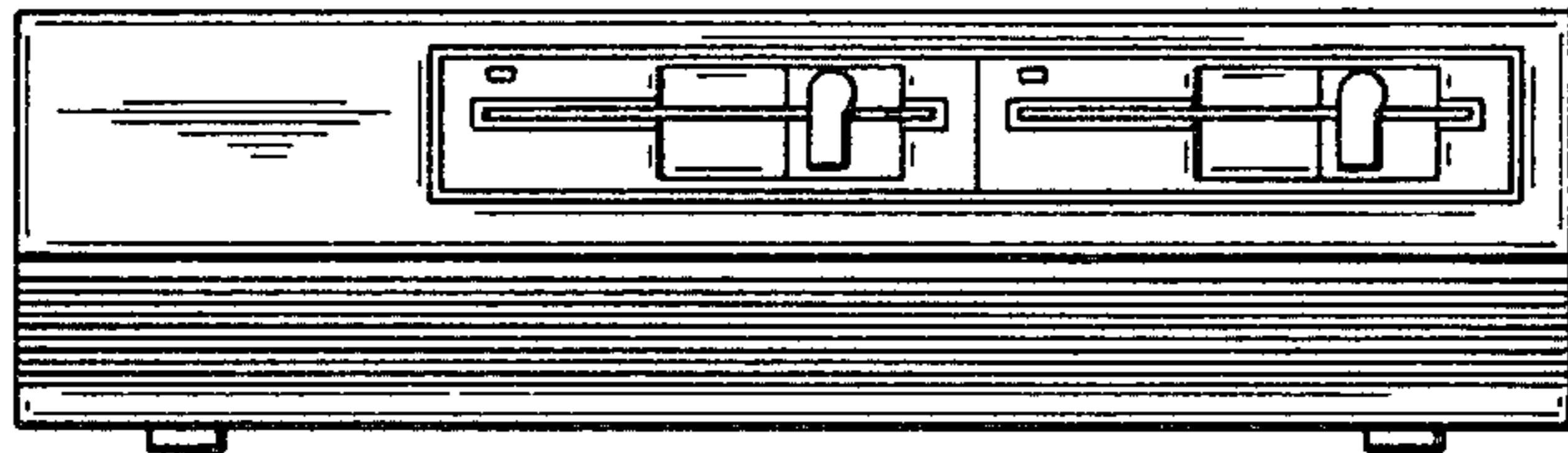
DESCRIPTION

FIG. 1 is a perspective view of the processor unit for use with computers showing my new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a rear elevational view thereof; FIG. 4 is a top plan view thereof; FIG. 5 is a bottom plan view thereof; FIG. 6 is a right side elevational view thereof; and FIG. 7 is a left side elevational view thereof.

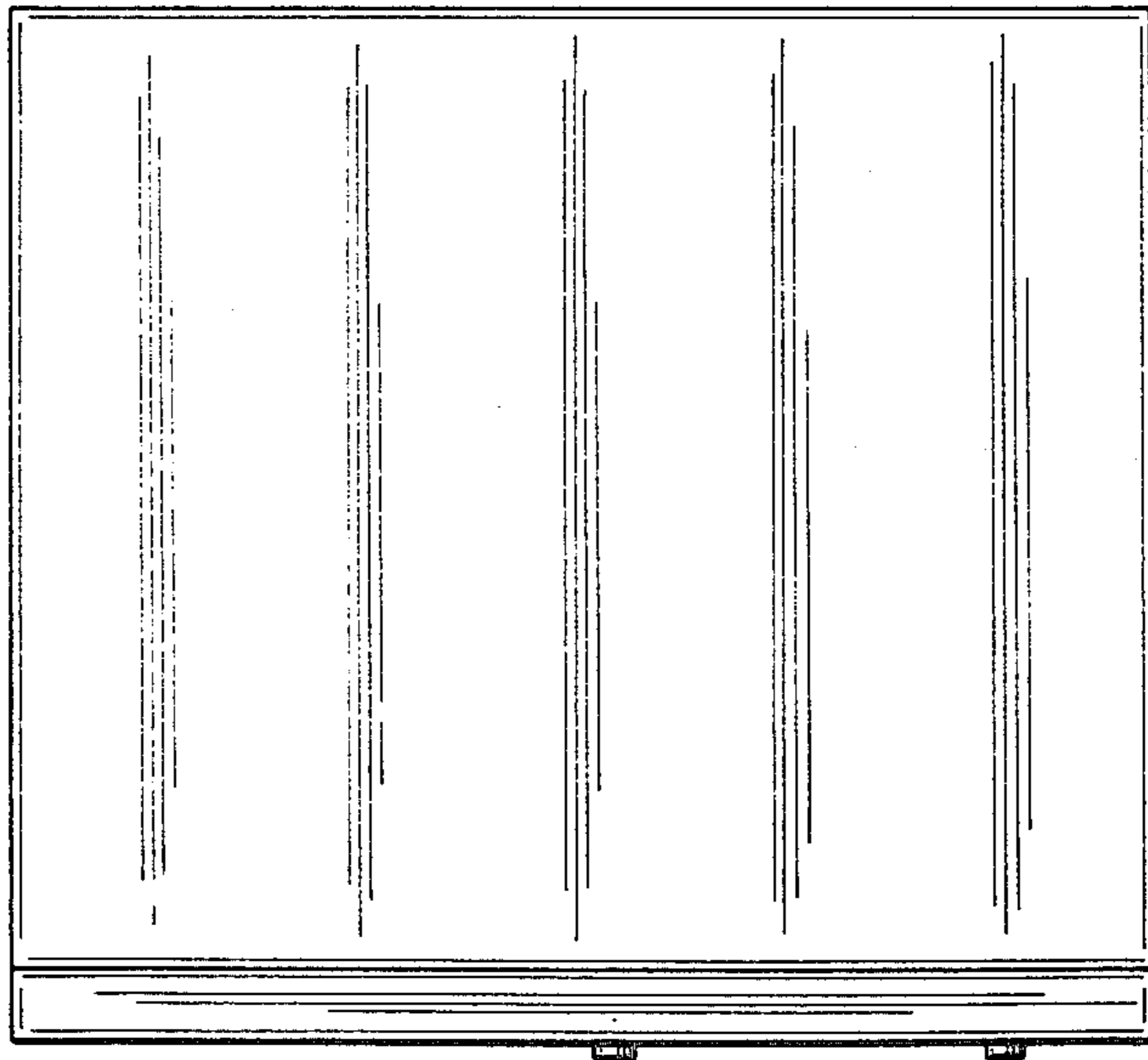




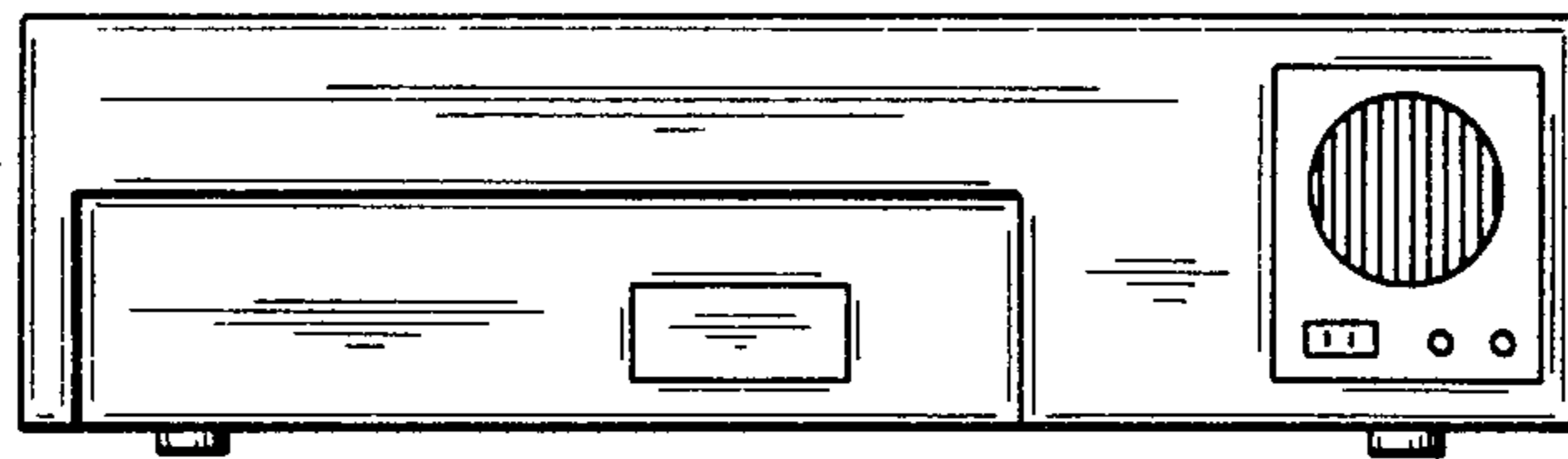
FIG_1



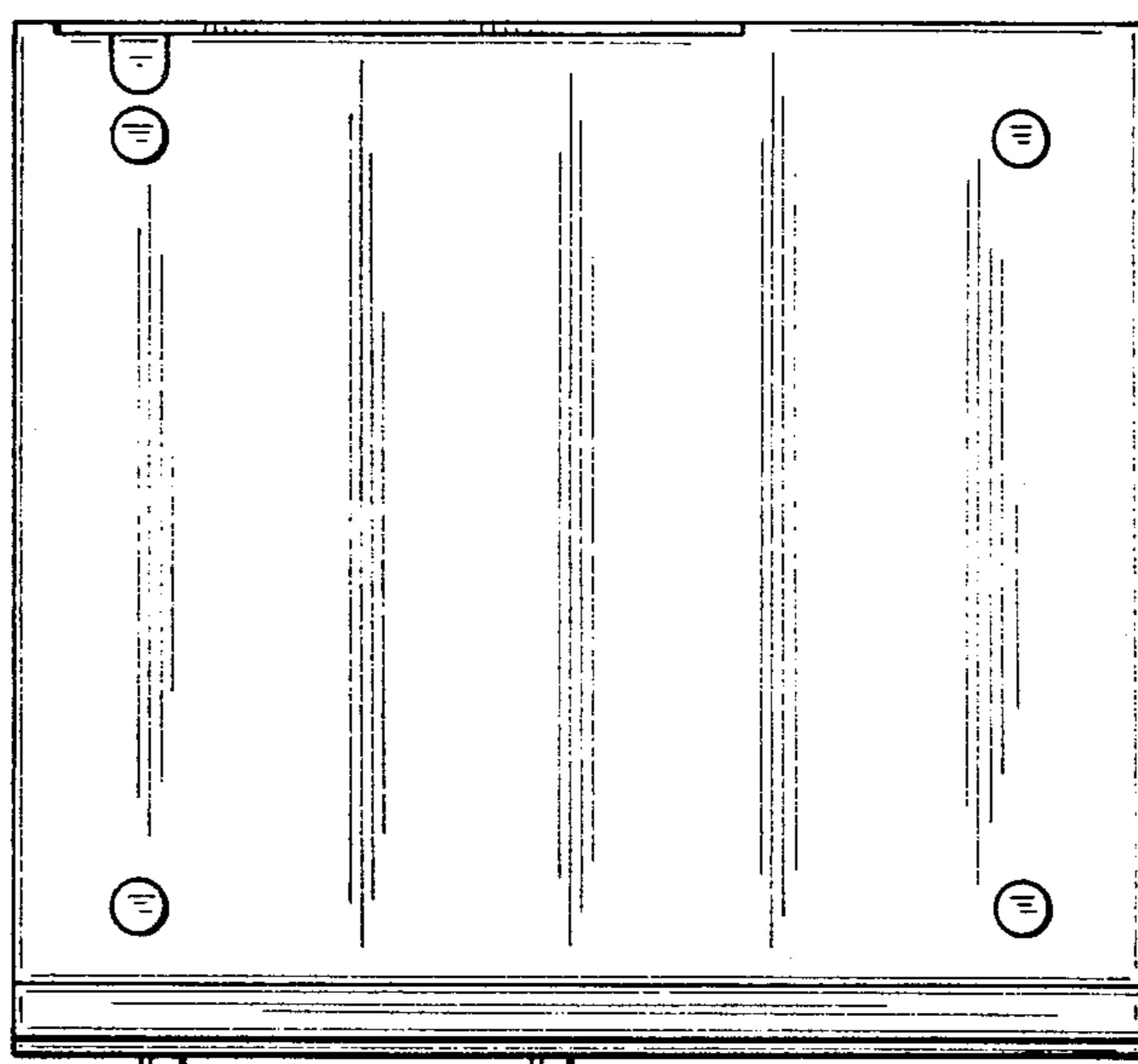
FIG_2



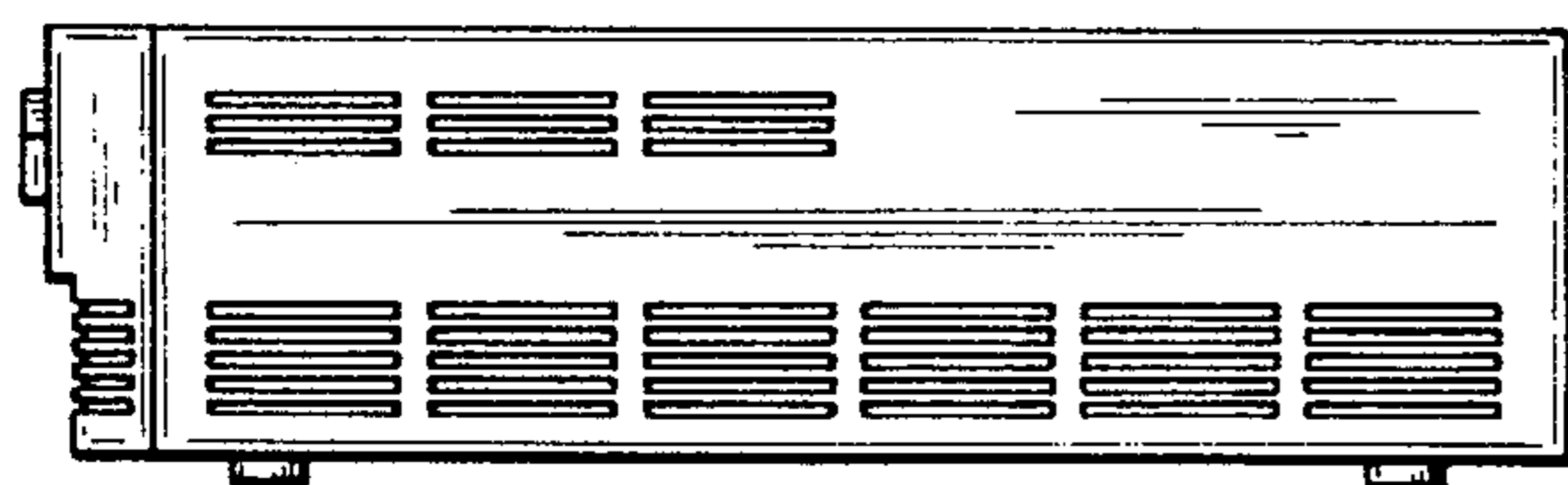
FIG_4



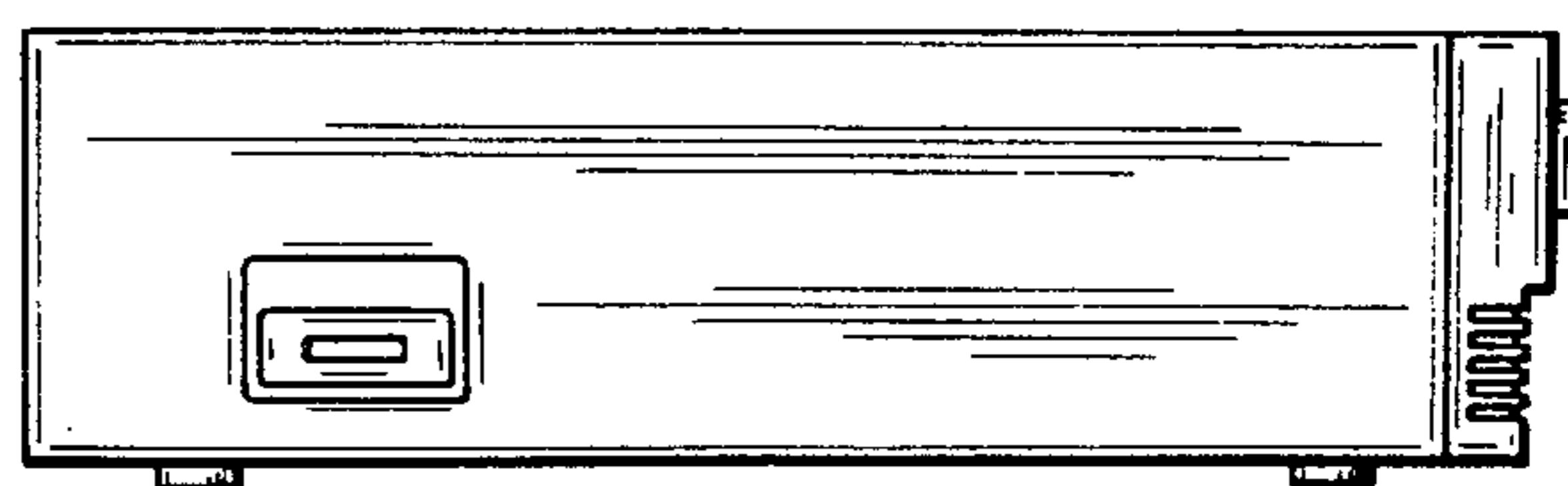
FIG_3



FIG_5



FIG_6



FIG_7