**United States Patent** [19]
**Takahashi**

[11] **Patent Number: Des. 278,048**
[45] **Date of Patent:** ** **Mar. 19, 1985**

[54] **LIGHT-EMITTING DIODE SEMICONDUCTOR CHIP WITH LEADS**

[75] Inventor: **Kaoru Takahashi,** Zama, Japan

[73] Assignee: **Stanley Electric Co., Ltd.,** Tokyo, Japan

[**] Term: **14 Years**

[21] Appl. No.: **372,642**

[22] Filed: **Apr. 28, 1982**

[30] **Foreign Application Priority Data**

| | | | |
|---|---|---|---|
| Oct. 30, 1981 | [JP] | Japan | 56-48471 |
| Oct. 30, 1981 | [JP] | Japan | 56-48472 |

[52] **U.S. Cl.** .................................... **D13/12;** D13/99

[58] **Field of Search** ........... D13/99; D26/1; 362/800; 313/499, 500, 501, 512; 357/17, 74

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[57] **CLAIM**

The ornamental design for a light-emitting diode semiconductor chip with leads, substantially as shown and described.

**DESCRIPTION**

FIG. **1** is a perspective view of a light-emitting diode semiconductor chip with leads showing my new design;
FIG. **2** is a front elevational view thereof;
FIG. **3** is a side elevational view thereof, as seen from the right side of FIG. **1;**
FIG. **4** is a side elevational view thereof, as seen from the left side of FIG. **1;**
FIG. **5** is a top plan view thereof;
FIG. **6** is a bottom plan view thereof;
FIG. **7** is a perspective veiw of a light-emitting diode semiconductor chip with leads showing the second embodiment of my new design;
FIG. **8** is a front elevational view thereof;
FIG. **9** is a side elevational view thereof, as seen from the right side of FIG. **7;**
FIG. **10** is a side elevational view thereof, as seen from the left side of FIG. **7;**
FIG. **11** is a top plan view thereof; and
FIG. **12** is a bottom plan view thereof.

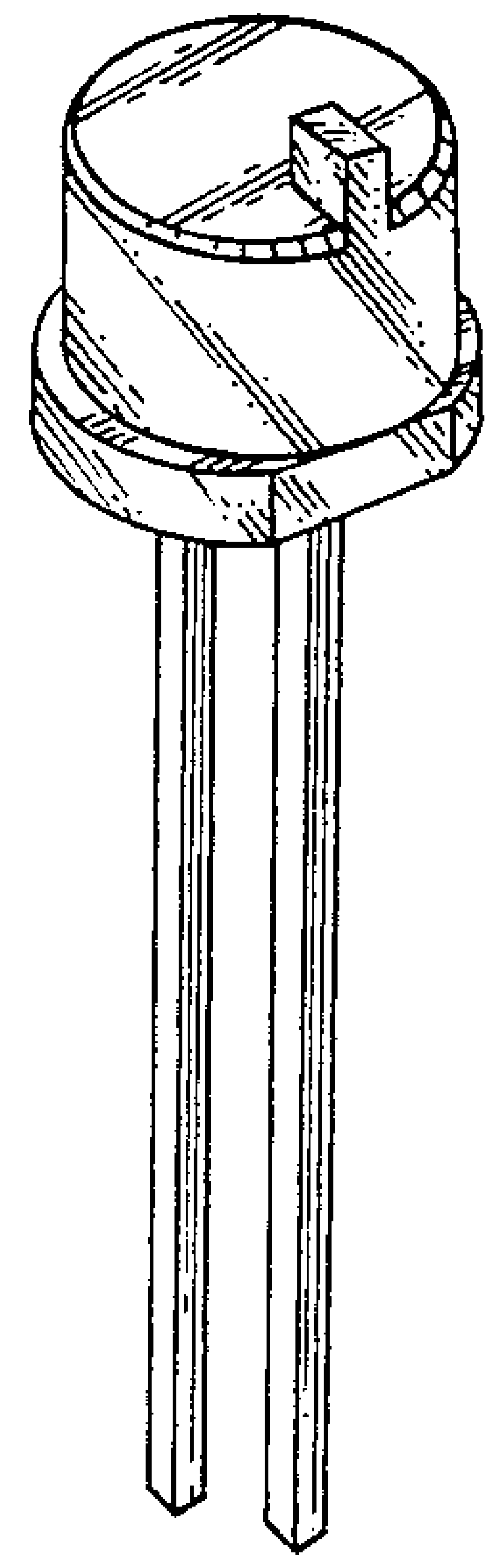

Fig.5

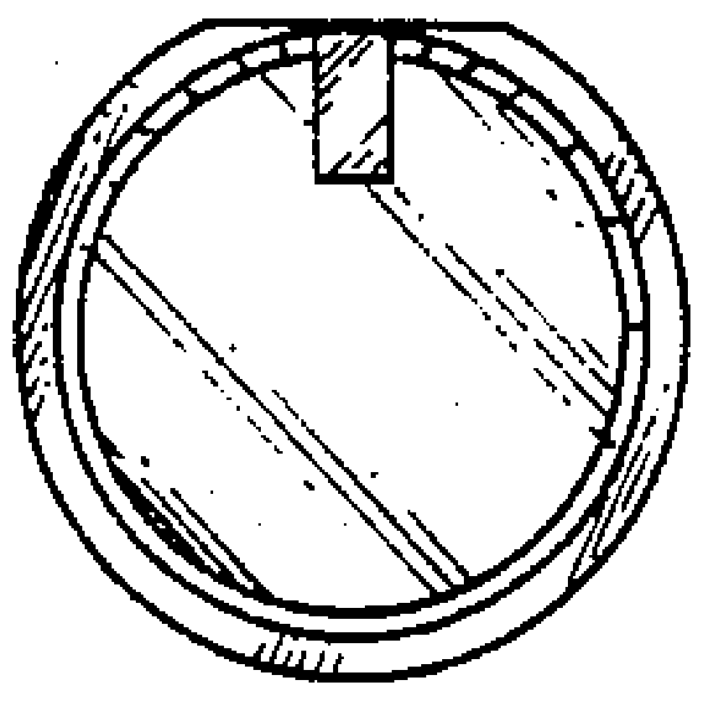

Fig.6

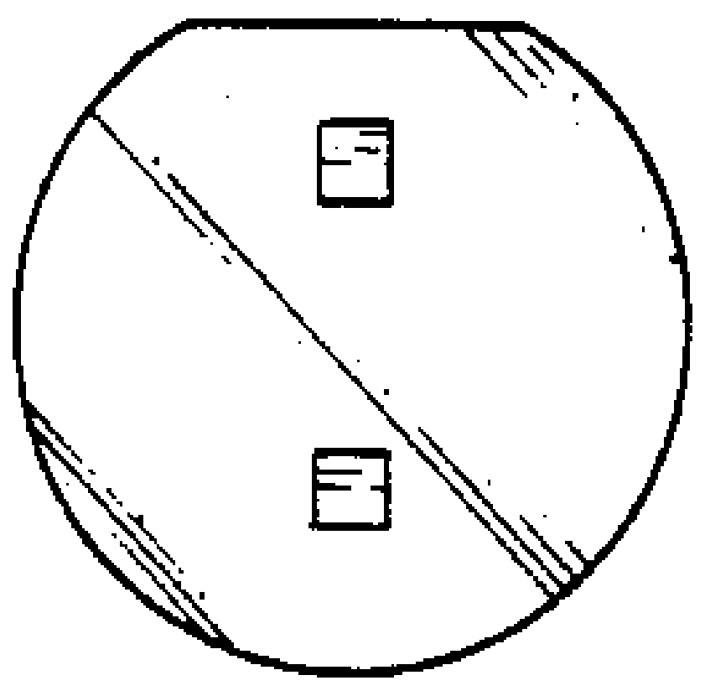

Fig.4

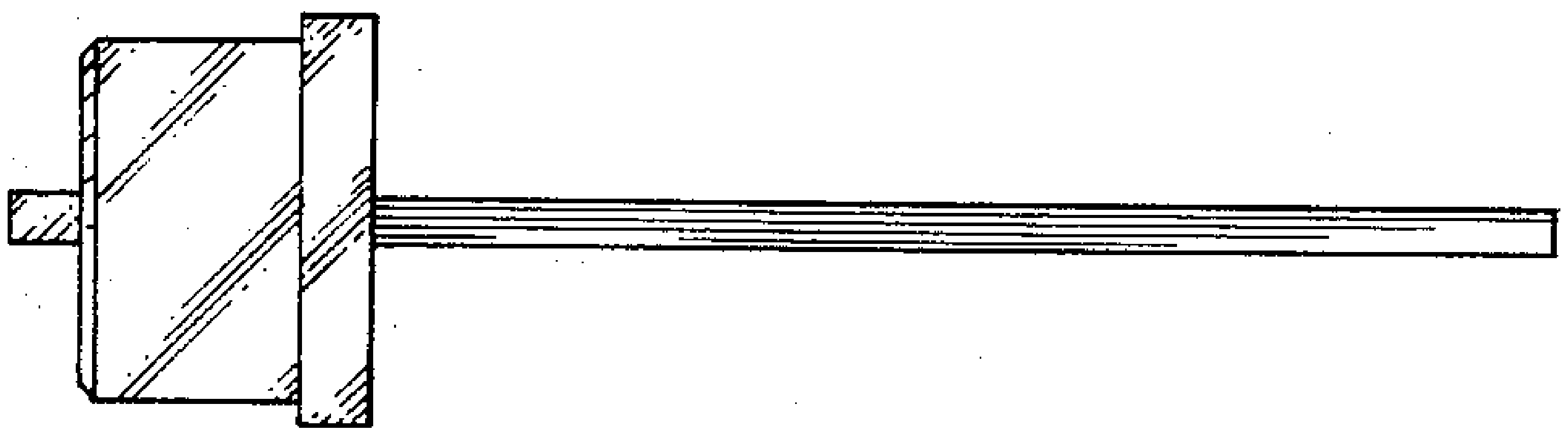

Fig. 3

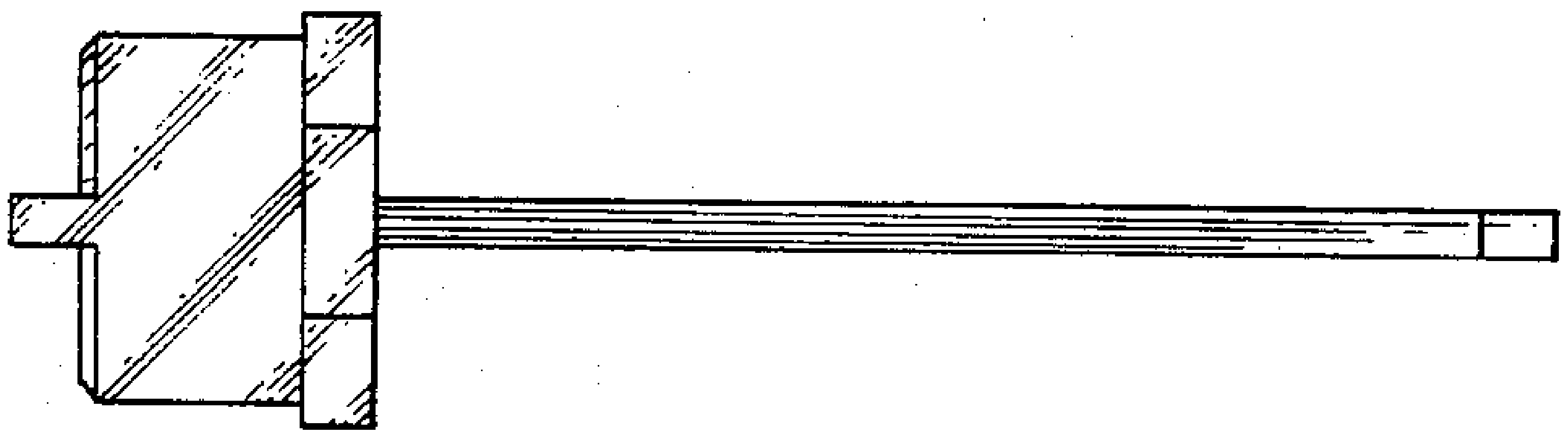

Fig.2

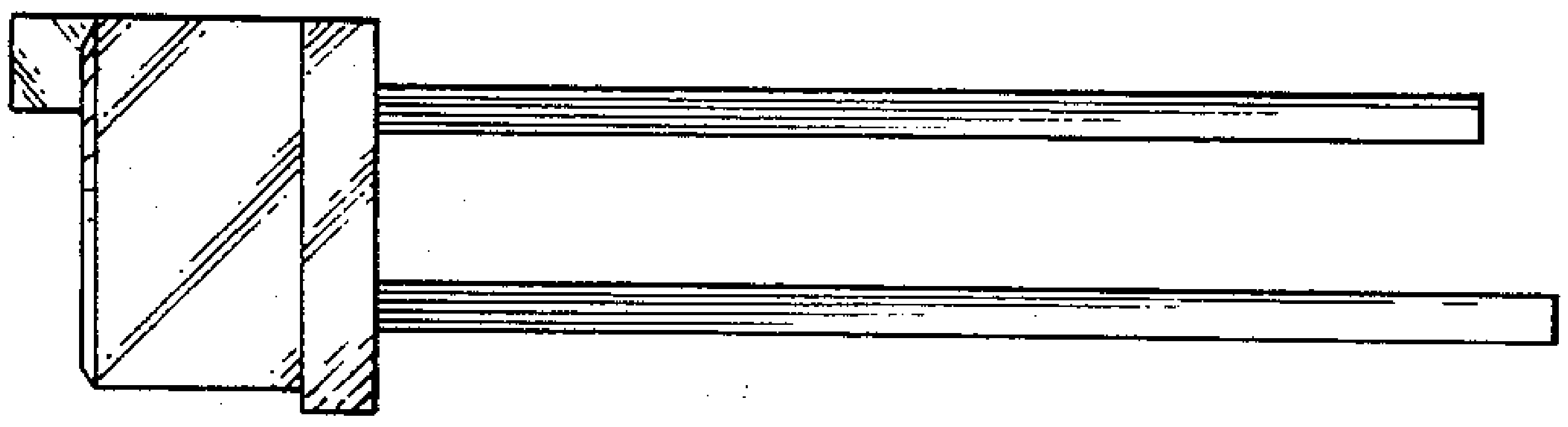

Fig.1

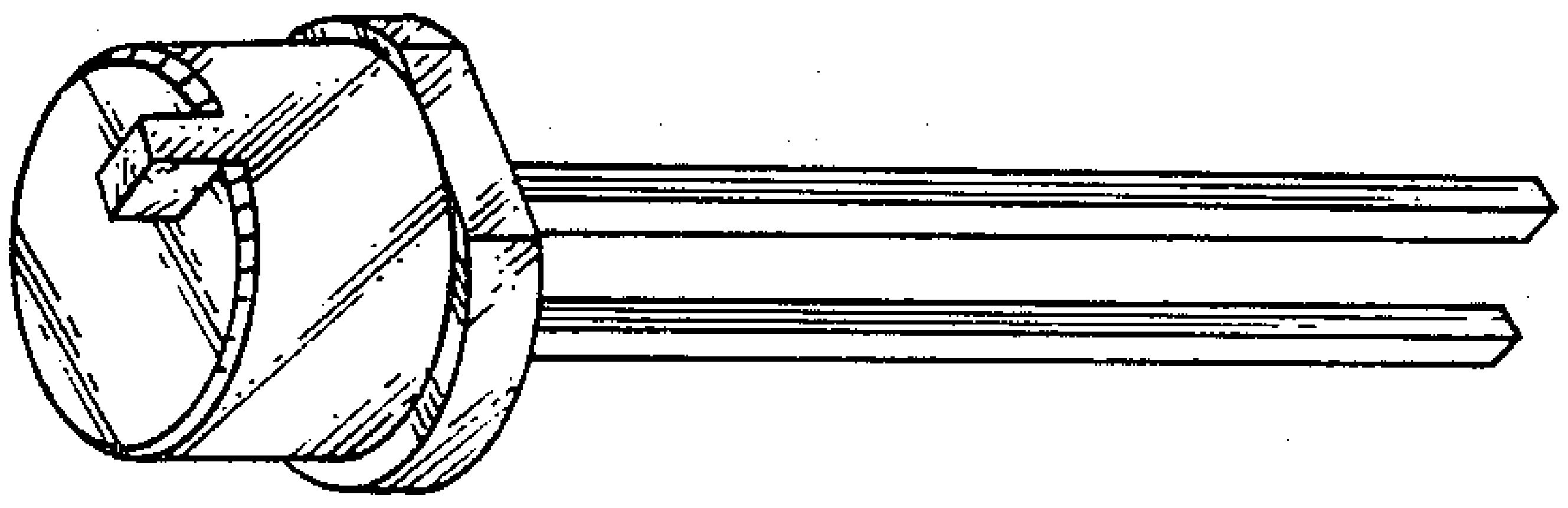

Fig. 11

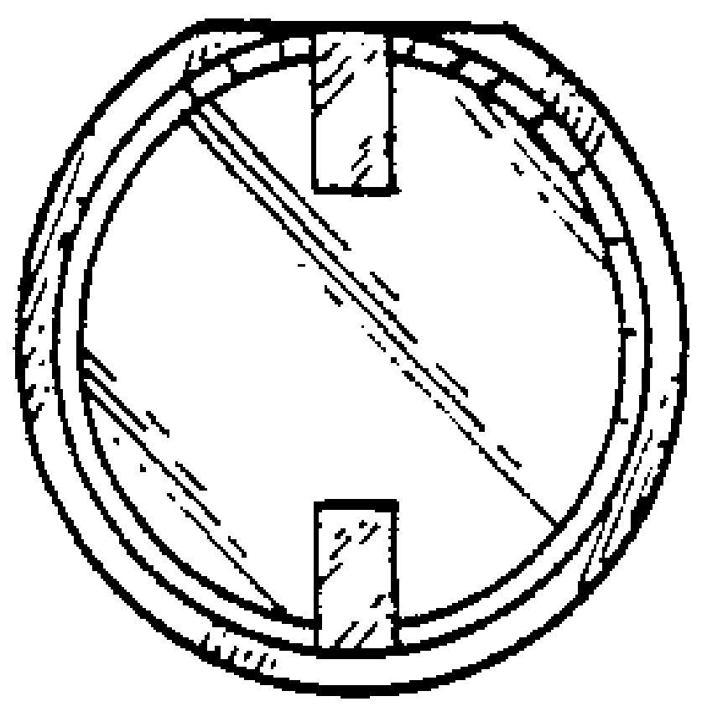

Fig. 12

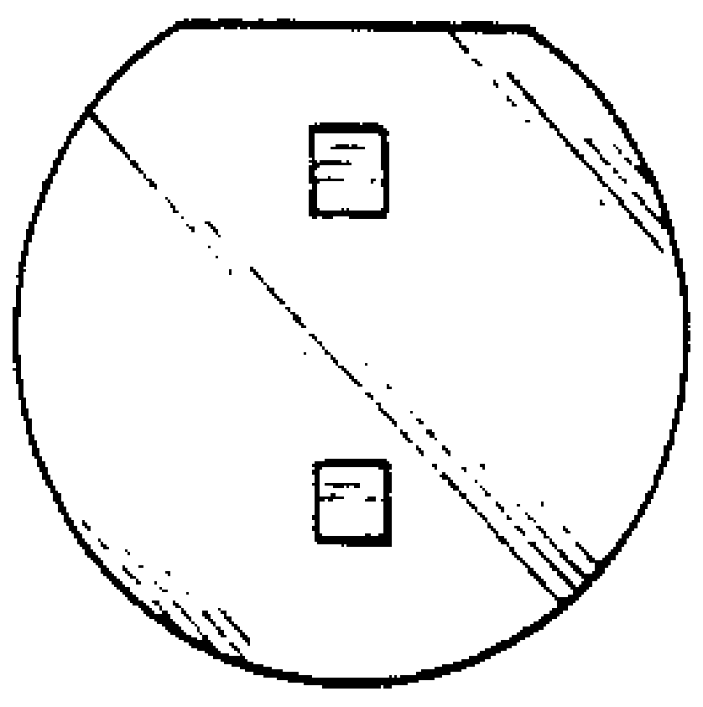

Fig. 10

Fig. 9

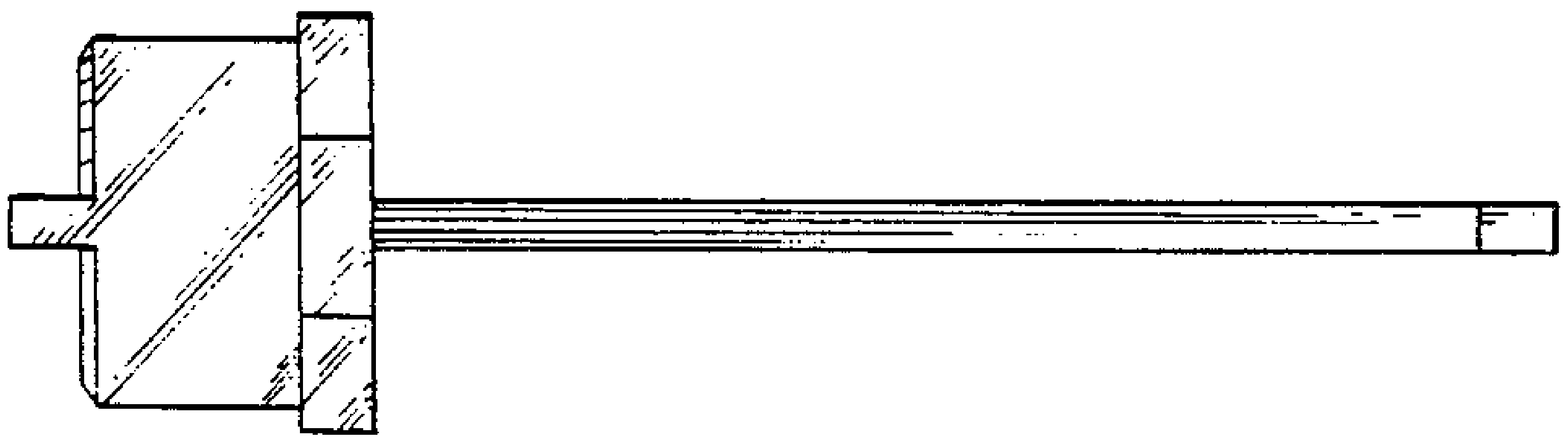

Fig. 8

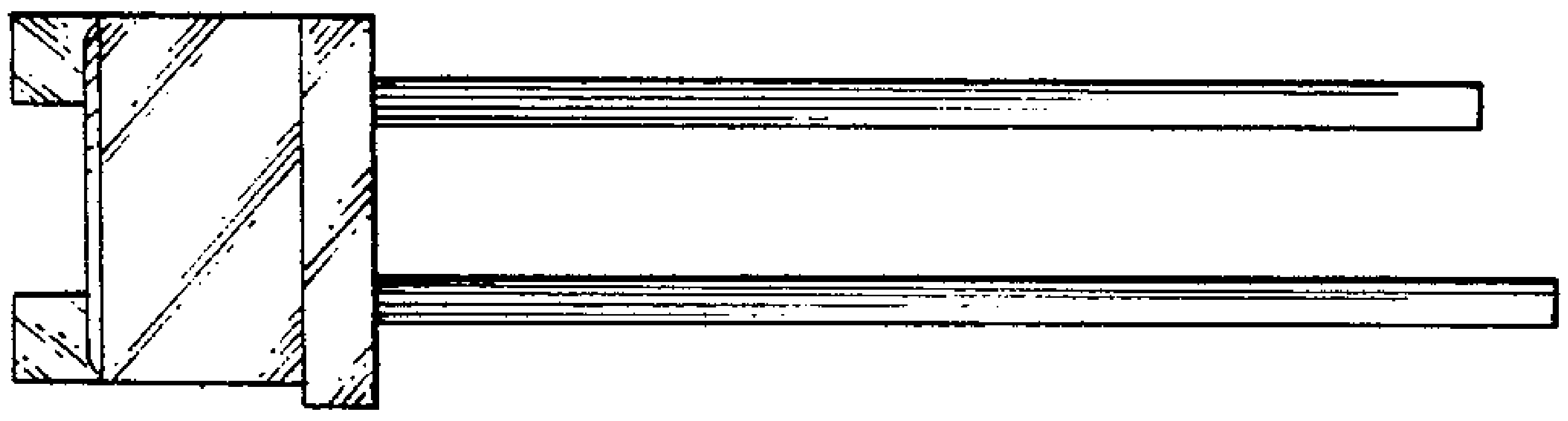

Fig. 7