

[54] **AUTOMATIC CIRCUIT-PLATE ASSEMBLER**

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[58] **Field of Search D15/122, 199, 144; 29/739, 740**

[56]

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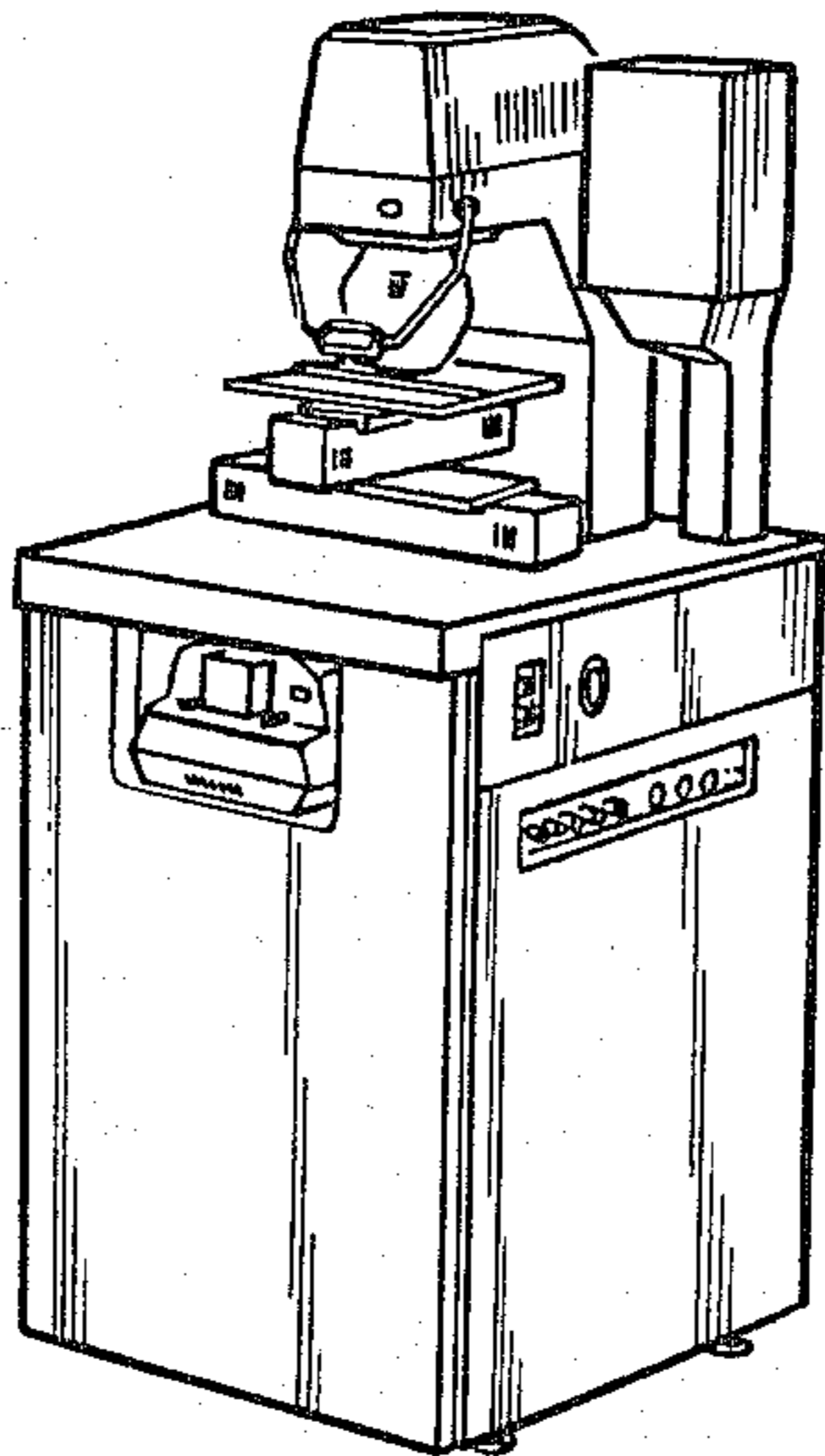
[57]

CLAIM

The ornamental design for an automatic circuit-plate assembler, substantially as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an automatic circuit-plate assembler showing our new design; FIG. 2 is a side elevational view taken from the right of FIG. 1; FIG. 3 is a side elevational view taken from the left of FIG. 1; FIG. 4 is a top plan view thereof; FIG. 5 is a bottom plan view thereof; FIG. 6 is a front elevational view thereof; and FIG. 7 is a rear elevational view thereof.



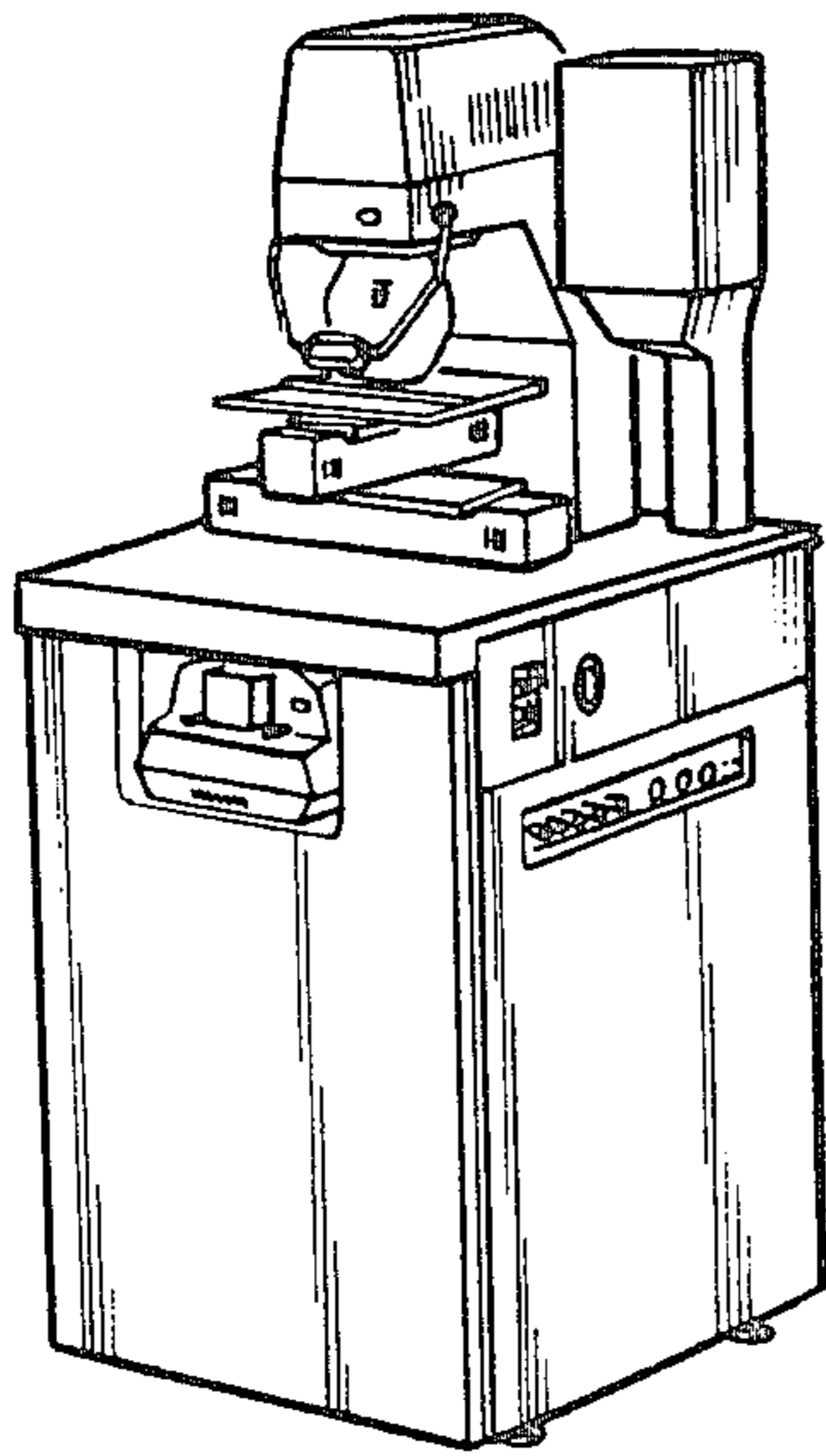


Fig. 1

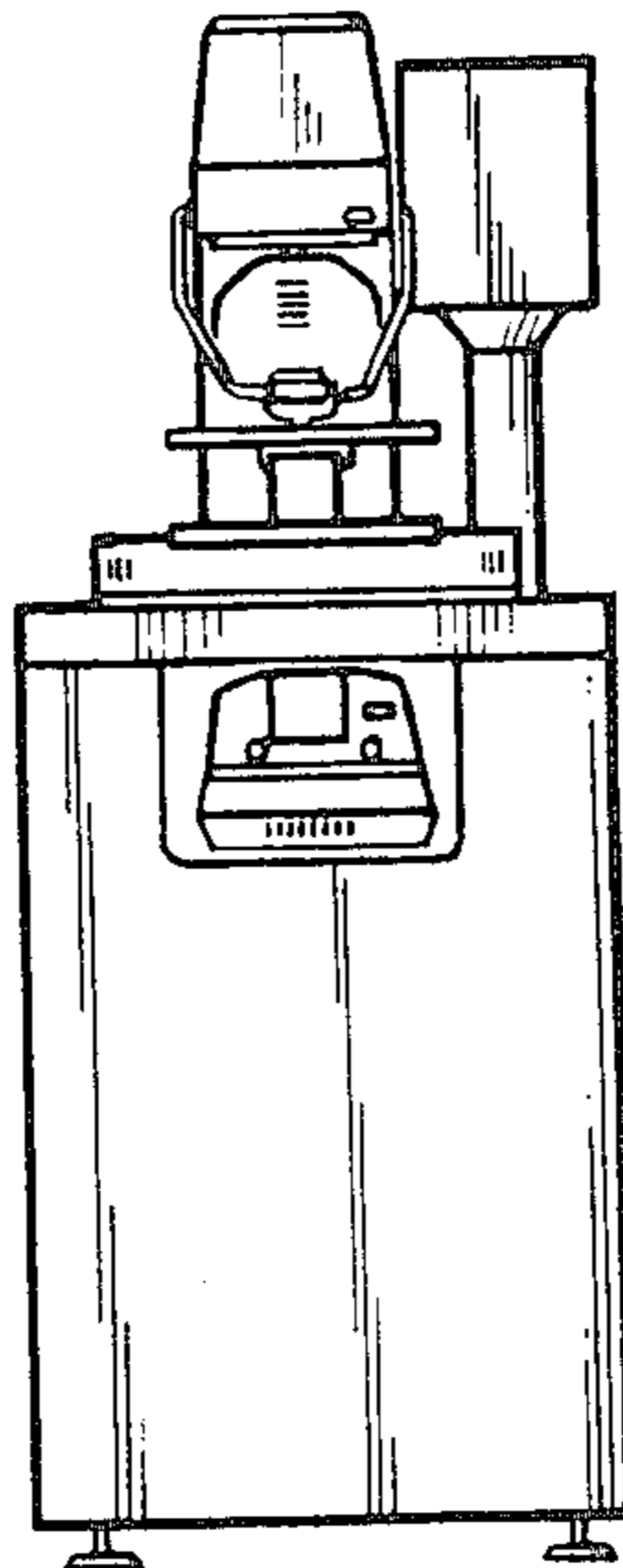


Fig. 6

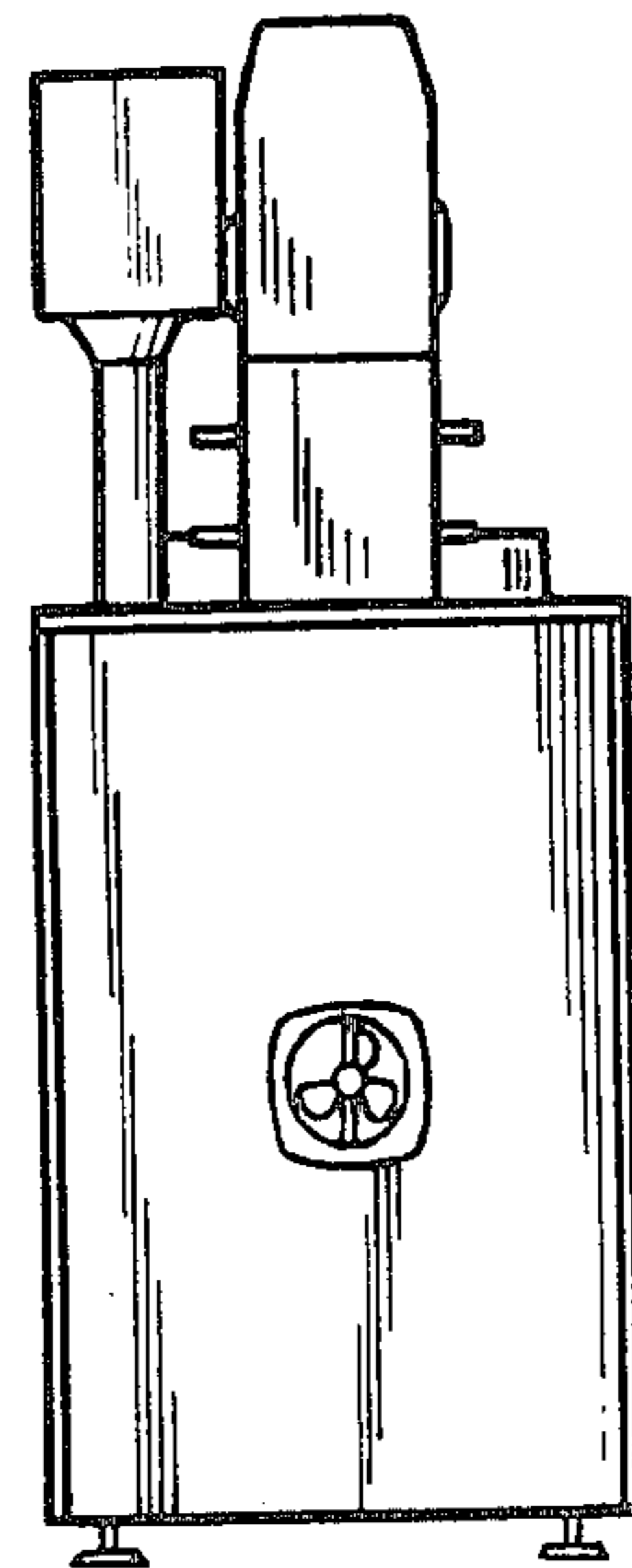


Fig. 7

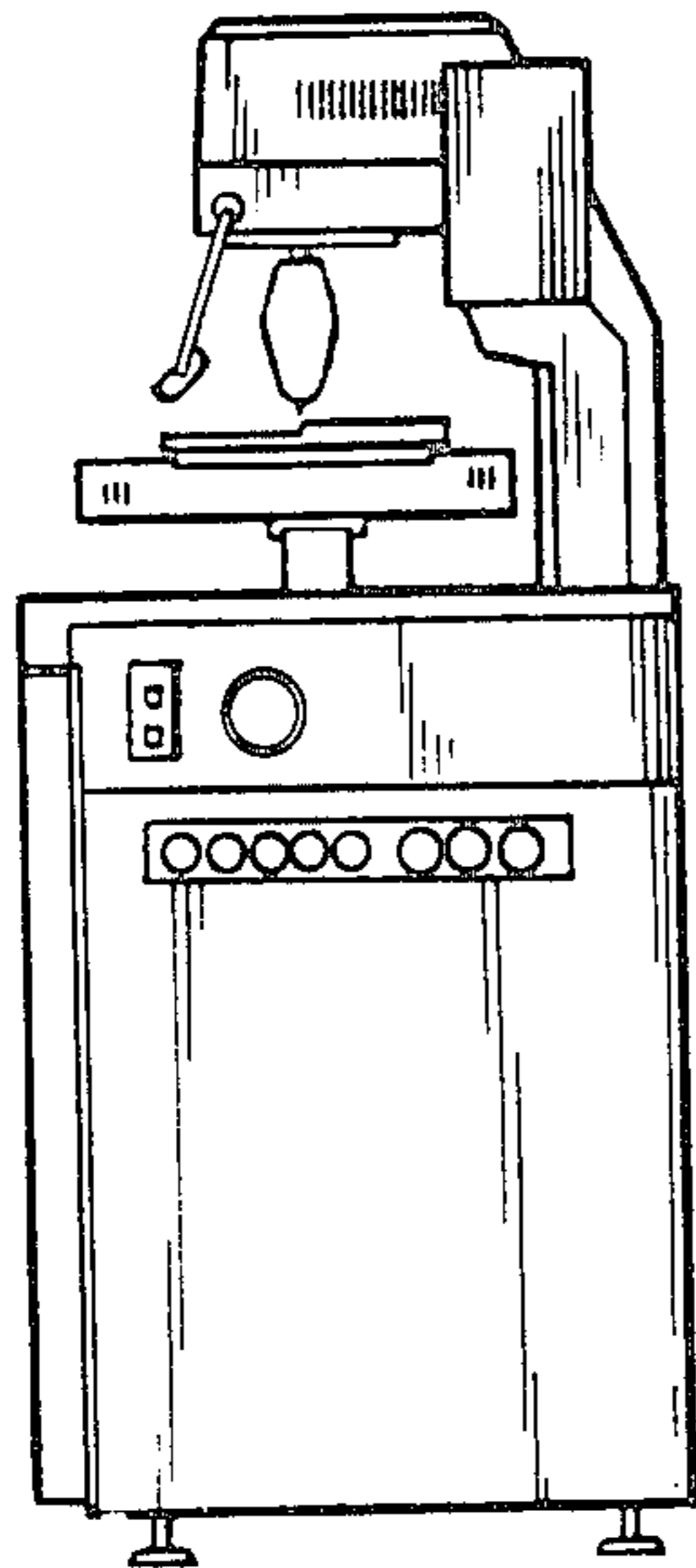


Fig. 2

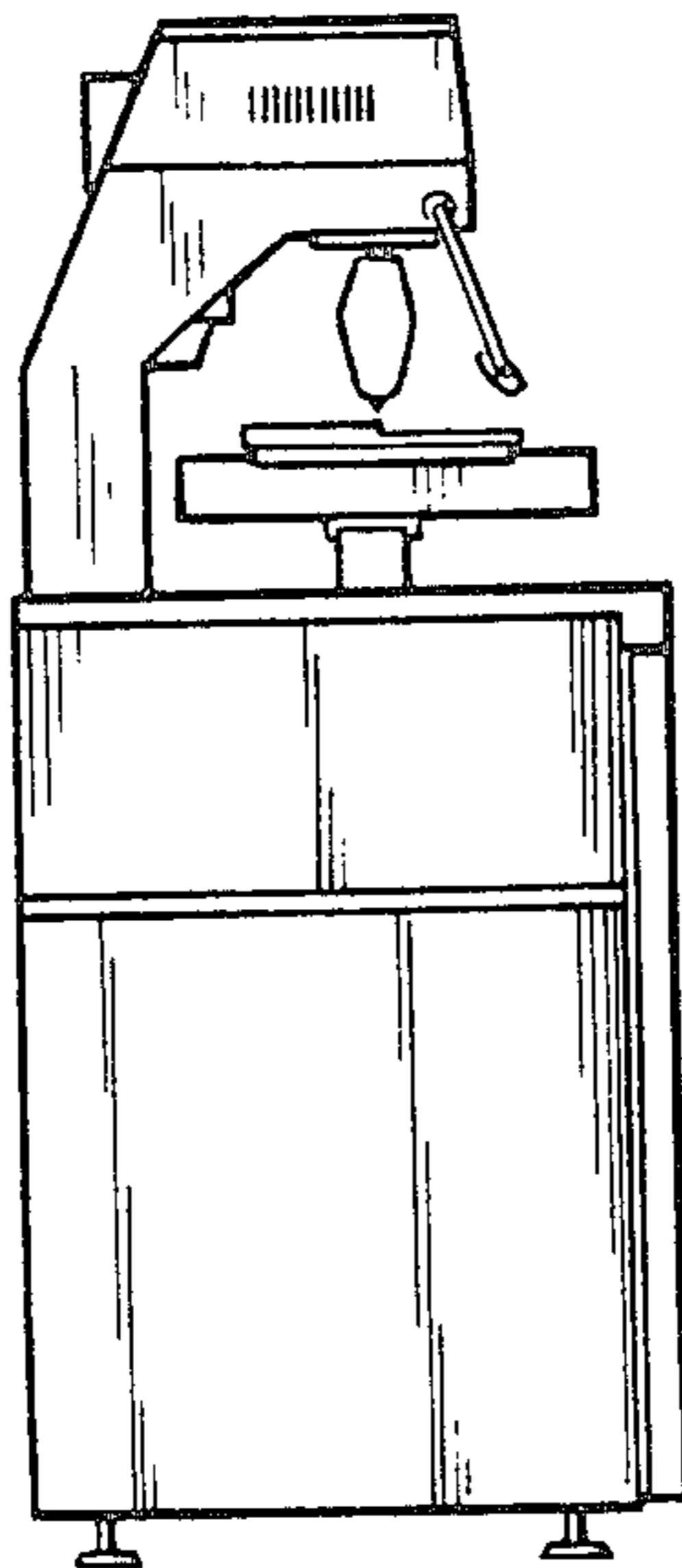


Fig. 3

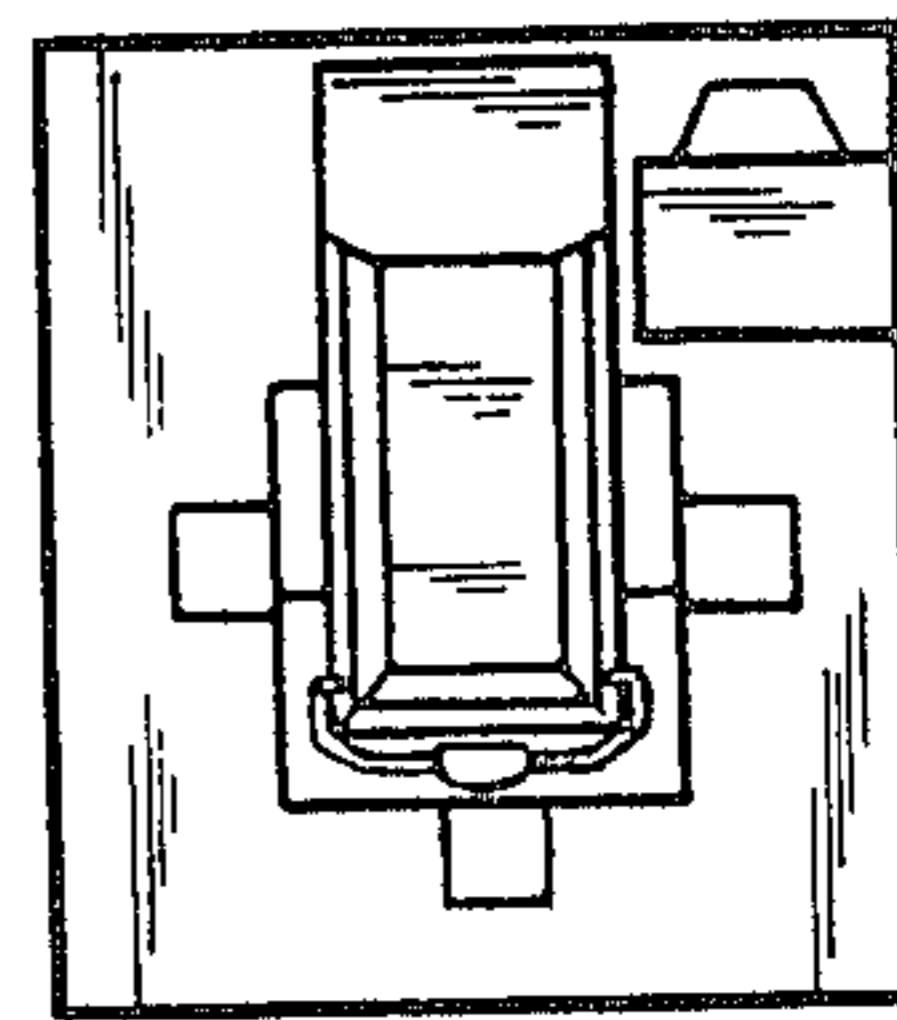


Fig. 4

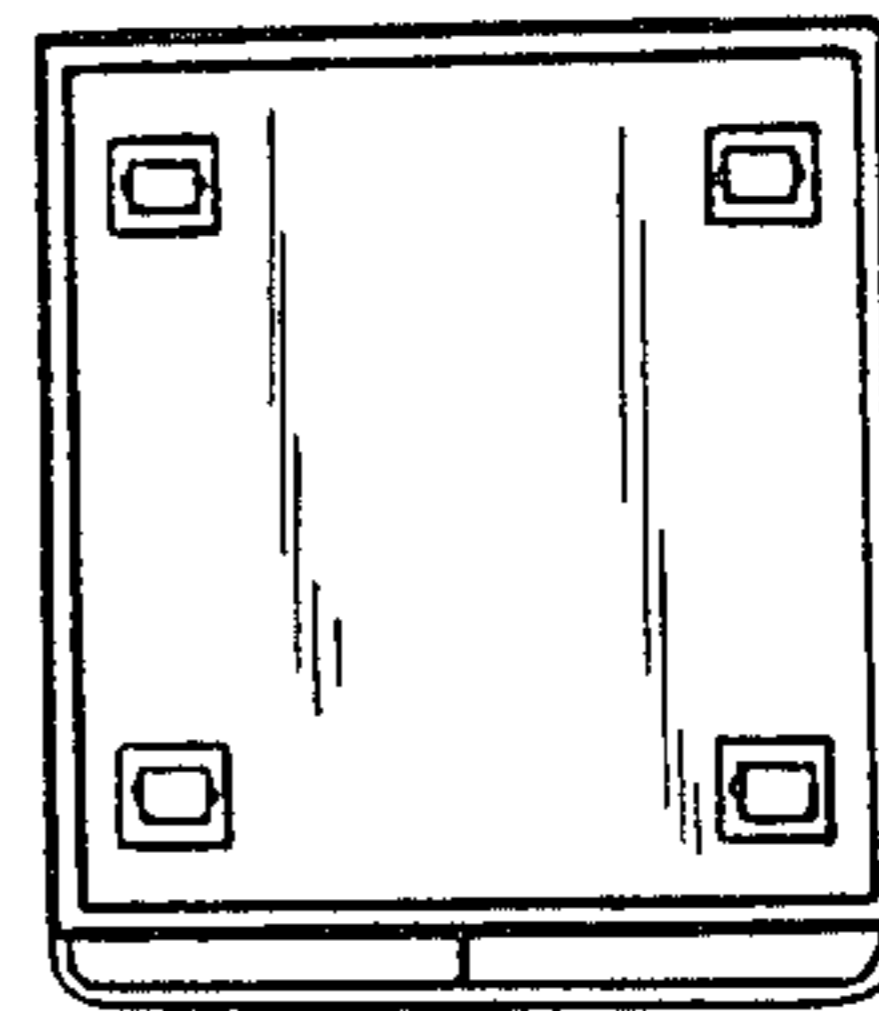


Fig. 5