

[54] MEMORY UNIT CARTRIDGE FOR ELECTRONIC COMPUTERS

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[**] Term: 14 Years

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[52] U.S. Cl. D14/114

[58] Field of Search D14/100, 101, 105-116, D14/11; 364/900; 174/52 R; 360/97, 98, 99, 132, 133; 361/392, 393, 394, 399; D13/40, 41; 206/444

[56] References Cited

U.S. PATENT DOCUMENTS

3,372,310	3/1968	Kantor	361/392
4,028,538	6/1977	Olander, Jr. et al.	D14/105 X
4,095,791	6/1978	Smith et al.	361/399 X
4,149,027	4/1979	Asher et al.	174/52 R

OTHER PUBLICATIONS

Electric Wave Science, 4-1-81, p. 73, Commodore Rampack.

Plug-in ROM Module 92 shown in U.S. Pat. No. 3,859,635 Watson et al., Jan. 7, 1975.

Atari Basic ROM Package CXL4002; "ASCII", 1980, a cover page and p. 125.

'Fanac Bubble Cassette' of Fujitsu Fanac Limited; Nikkei Industrial News, Jul. 23, 1980.

Catalogue of Casio Memory Packages, (ROM Package E-4K, RAM Package D-16K, and RAM Package C-4K) as of May 1, 1981.

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[57] CLAIM

The ornamental design for a memory unit cartridge for electronic computers, substantially as shown and described.

DESCRIPTION

FIG. 1 is a front, top and right side perspective view of a memory unit cartridge for electronic computers showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a top plan view thereof;

FIG. 4 is a side elevational view looking from the right of FIG. 2, the opposite side being a mirror image;

FIG. 5 is a rear elevational view thereof; and

FIG. 6 is a bottom plan view thereof.

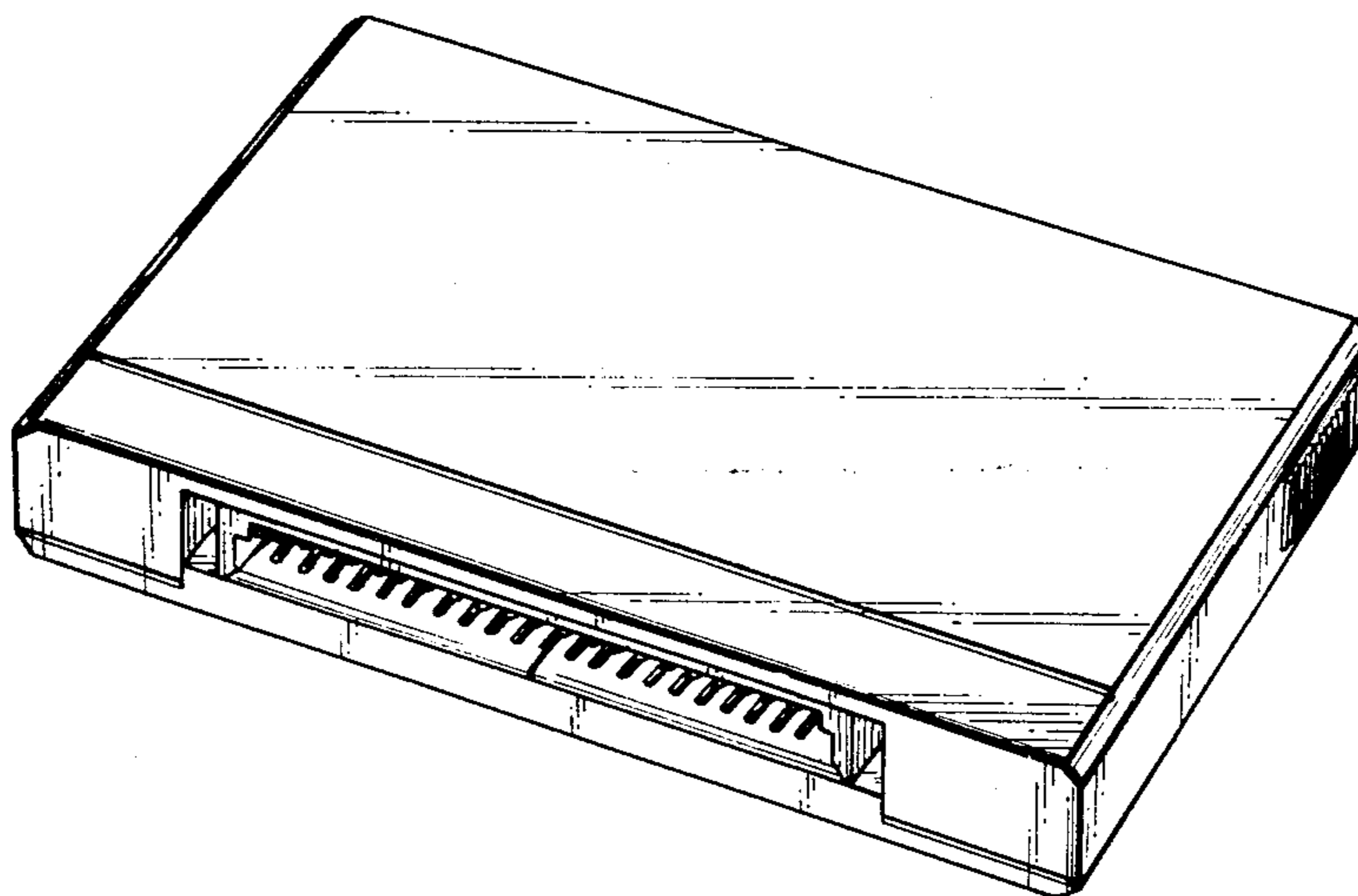


FIG. 1

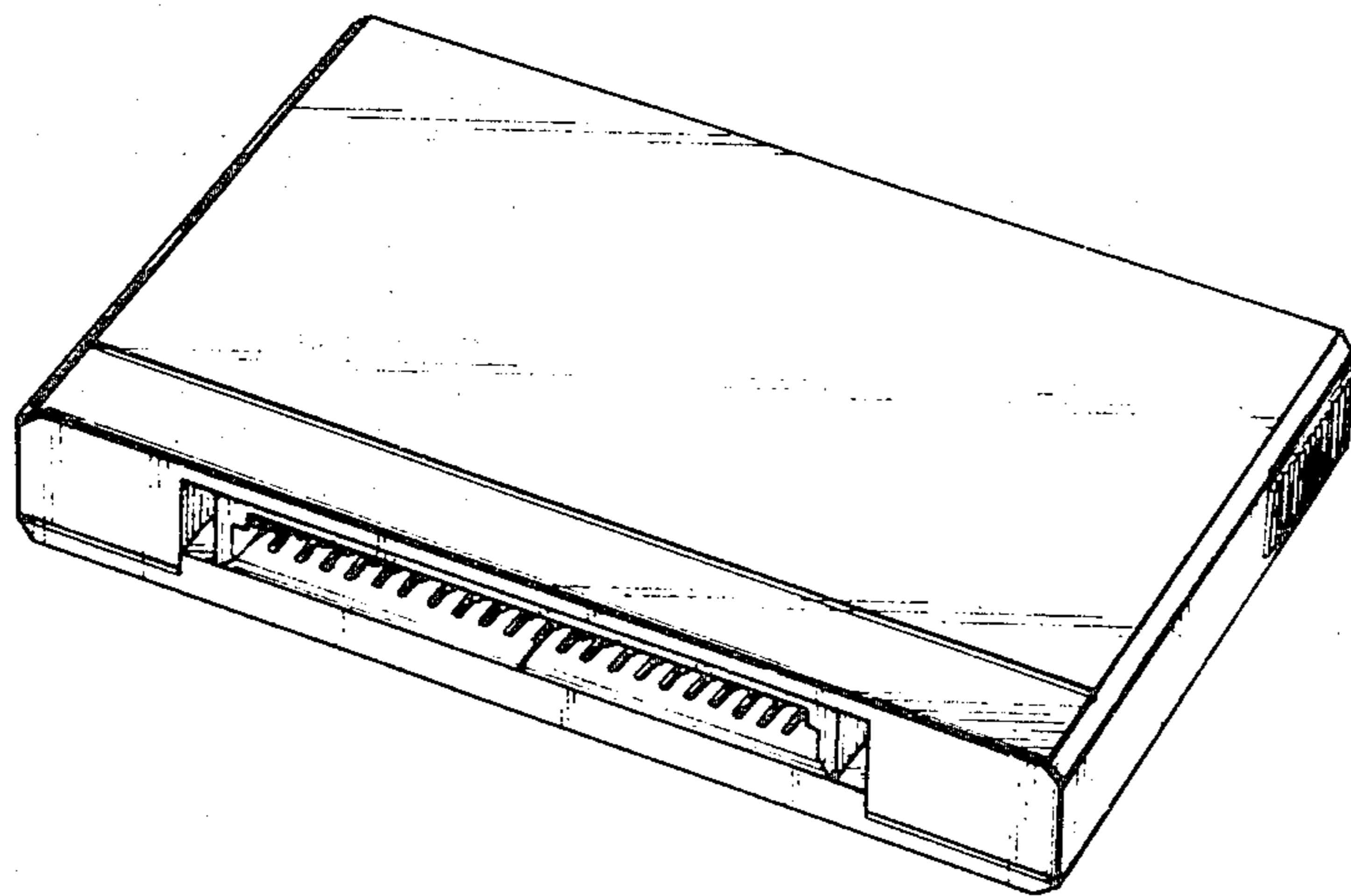


FIG. 2

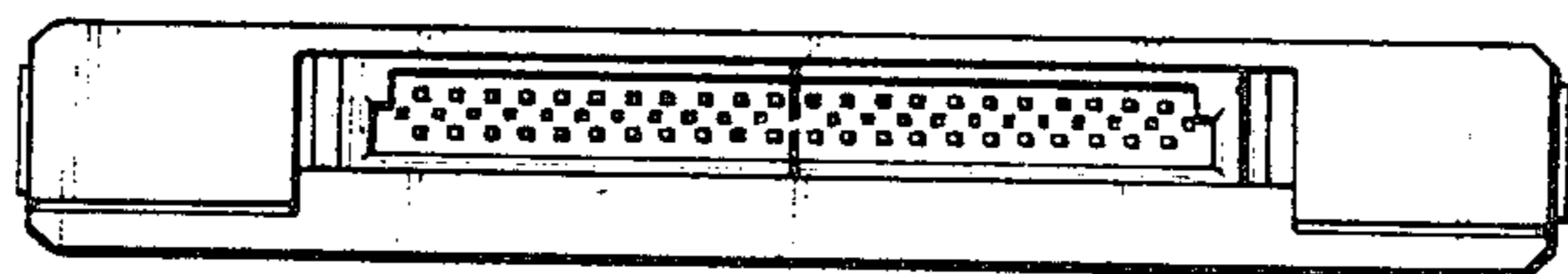


FIG. 3

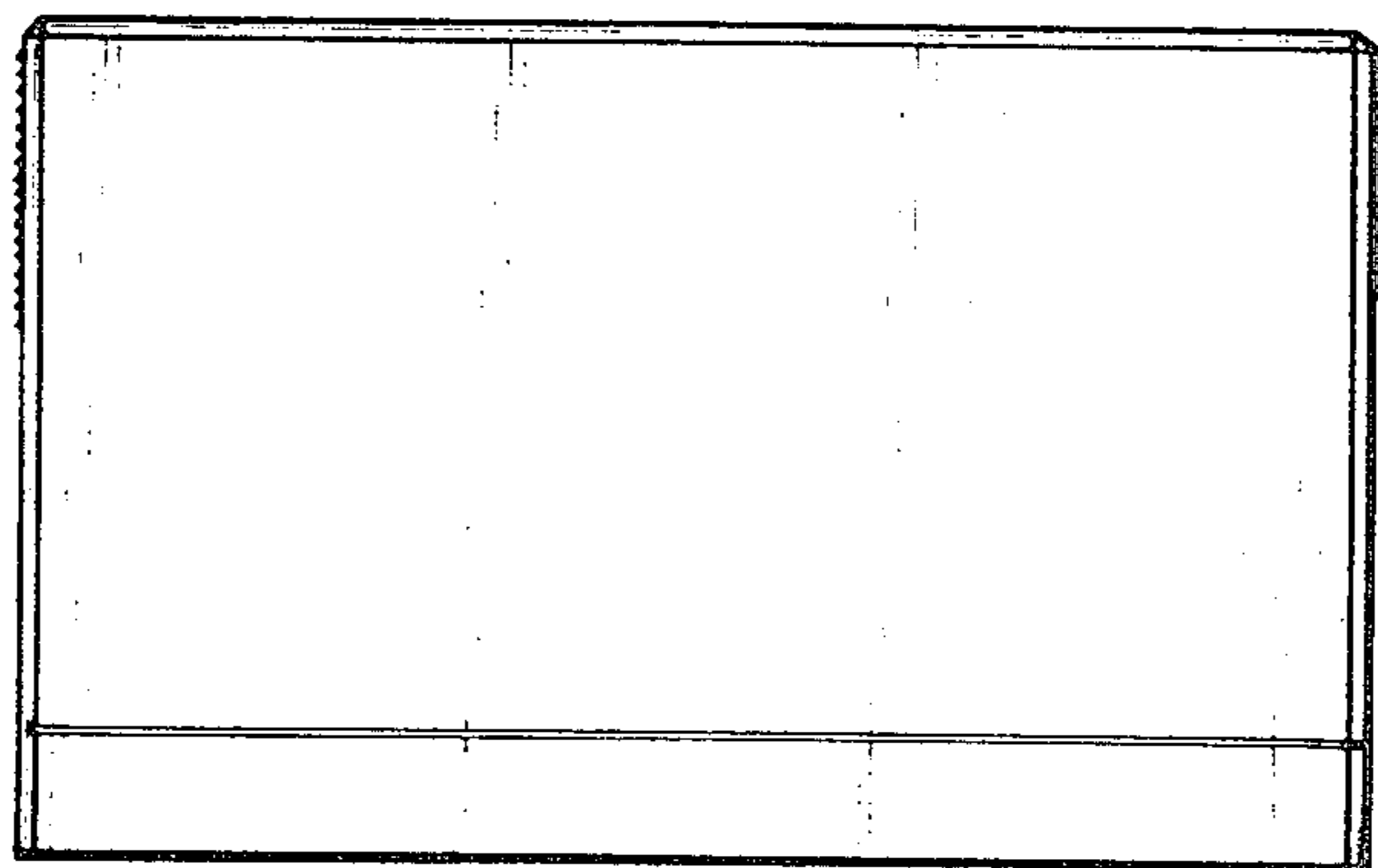


FIG. 4

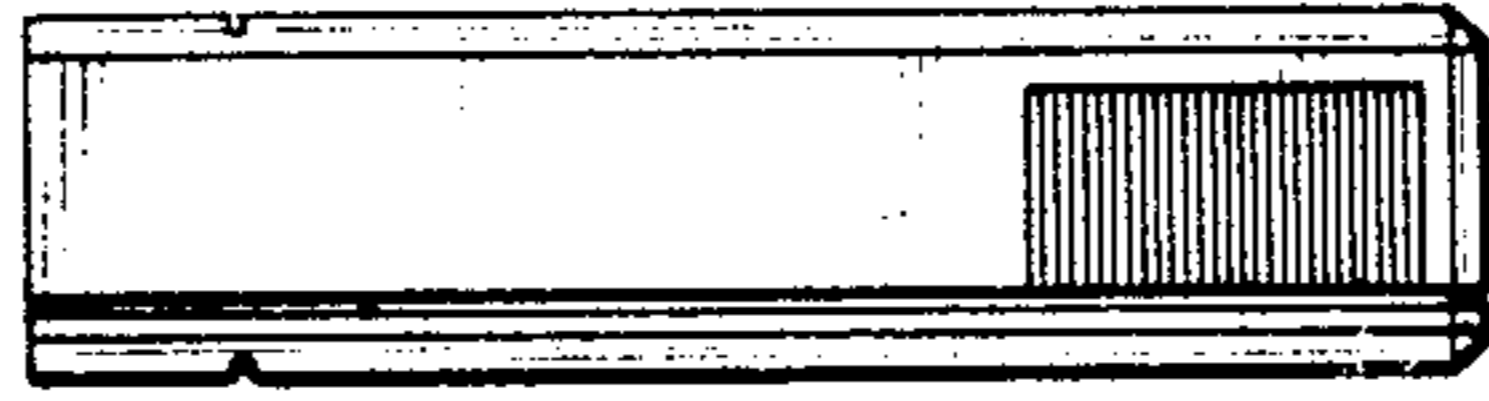


FIG. 5

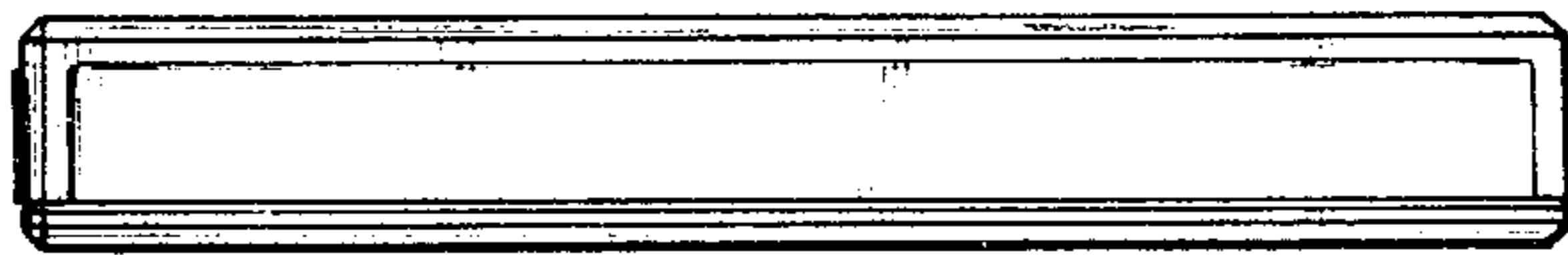


FIG. 6

