

[54] ELECTRONIC TRANSLATOR

[56]

References Cited

U.S. PATENT DOCUMENTS

[75] Inventor: Katsuhiko Ishida, Osaka, Japan

D. 259,347	5/1981	Hirata et al.	D18/1
D. 262,623	1/1982	Fujimoto et al.	D14/100
D. 263,827	4/1982	Pemberton	D18/7 X
D. 264,729	6/1982	Kobayashi	D18/7 X
D. 265,314	7/1982	Kitai	D14/100

[73] Assignee: Sharp Corporation, Osaka, Japan

[**] Term: 14 Years

Primary Examiner—Louis S. Zarfes
Attorney, Agent, or Firm—Flehr, Hohbach, Test,
Albritton & Herbert

[21] Appl. No.: 257,372

[57] CLAIM

[22] Filed: Apr. 24, 1981

The ornamental design for an electronic translator, as shown and described.

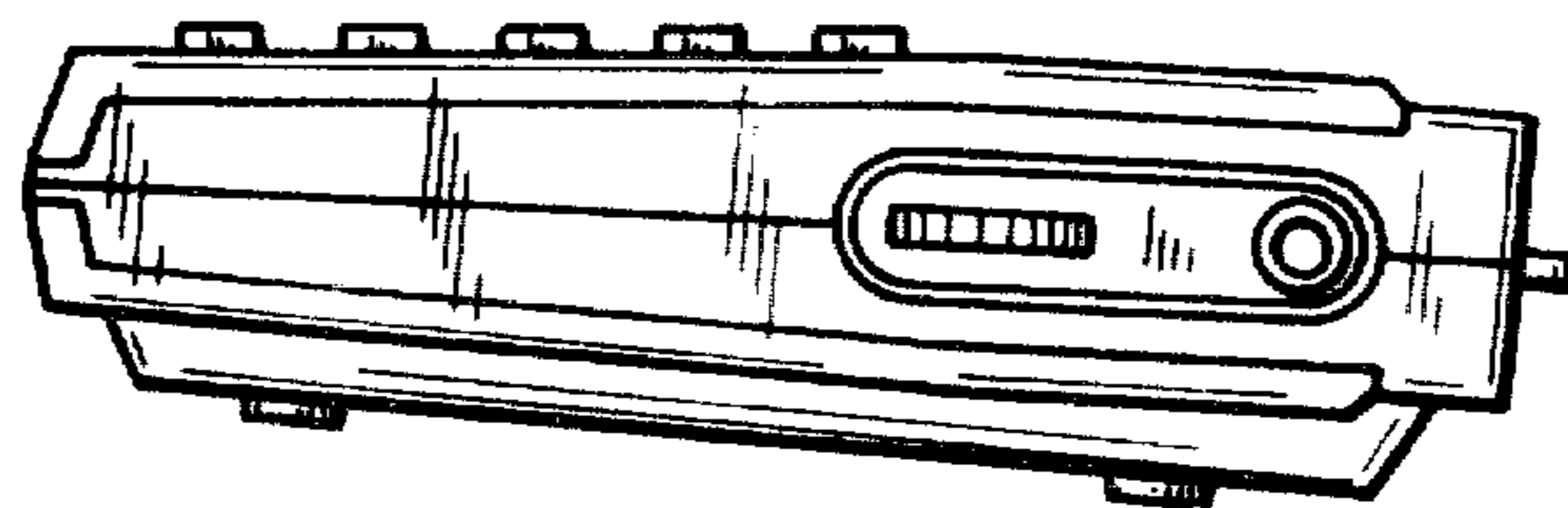
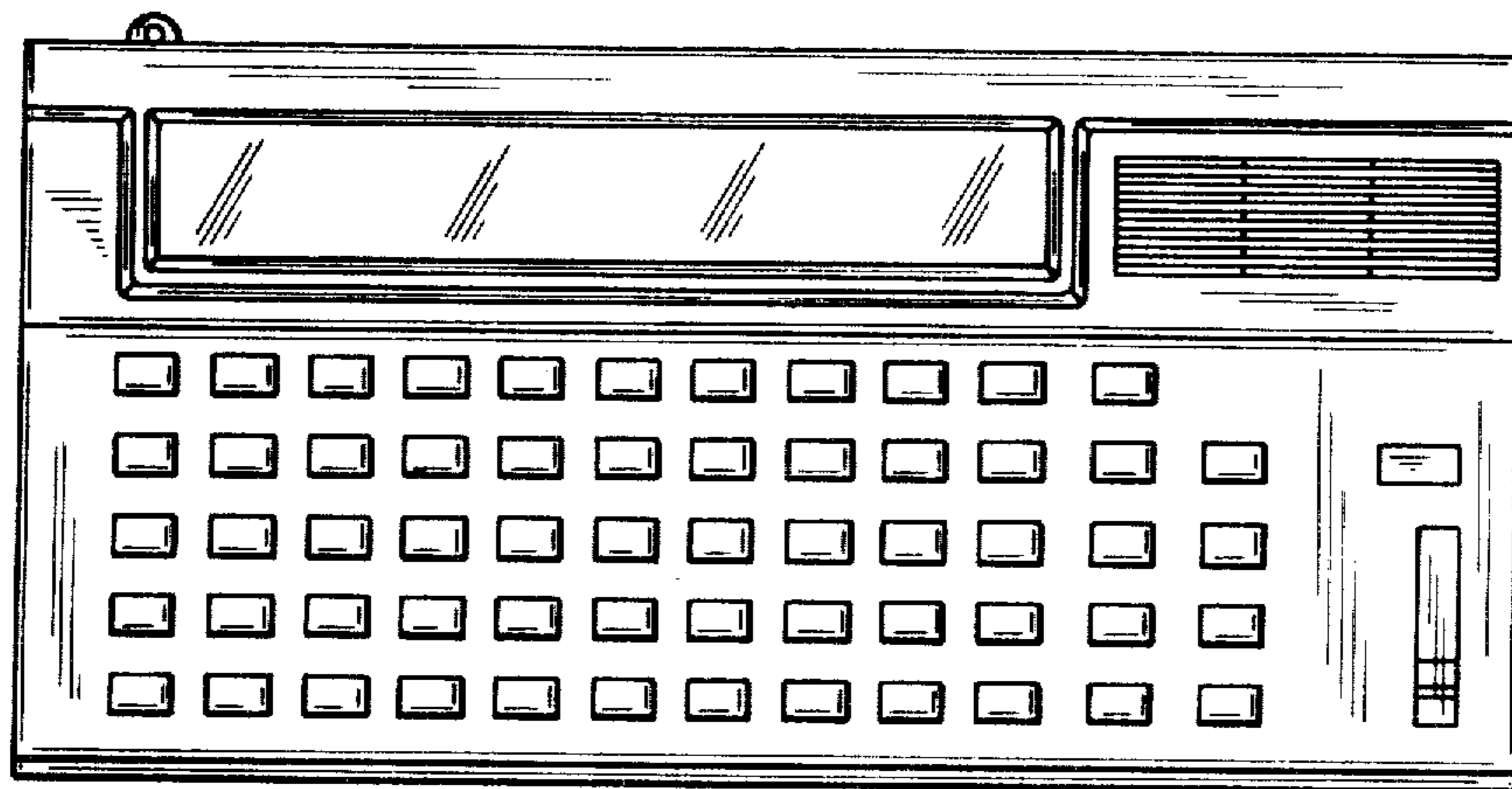
[30] Foreign Application Priority Data

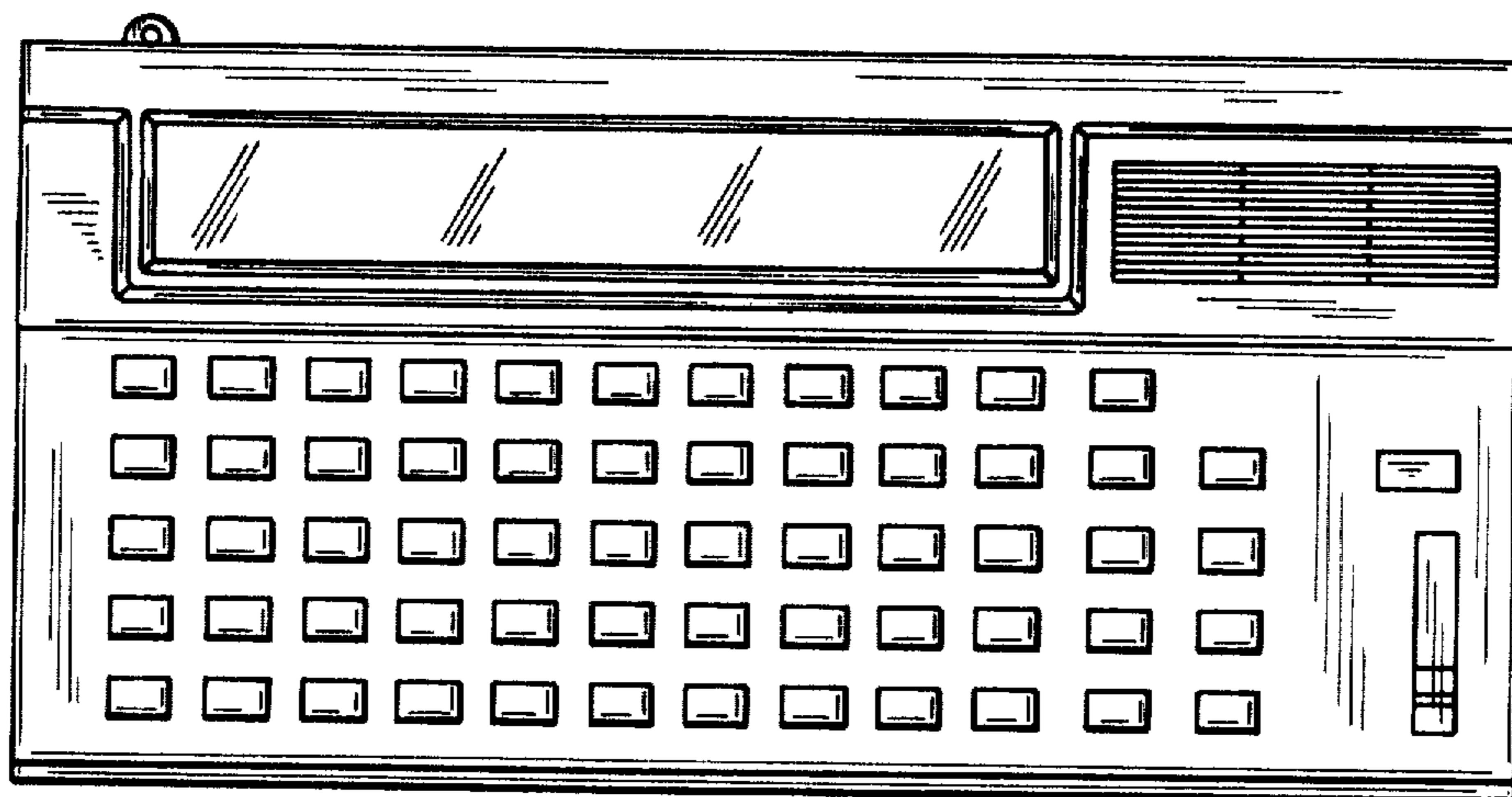
DESCRIPTION

Oct. 31, 1980 [JP] Japan 55-45834

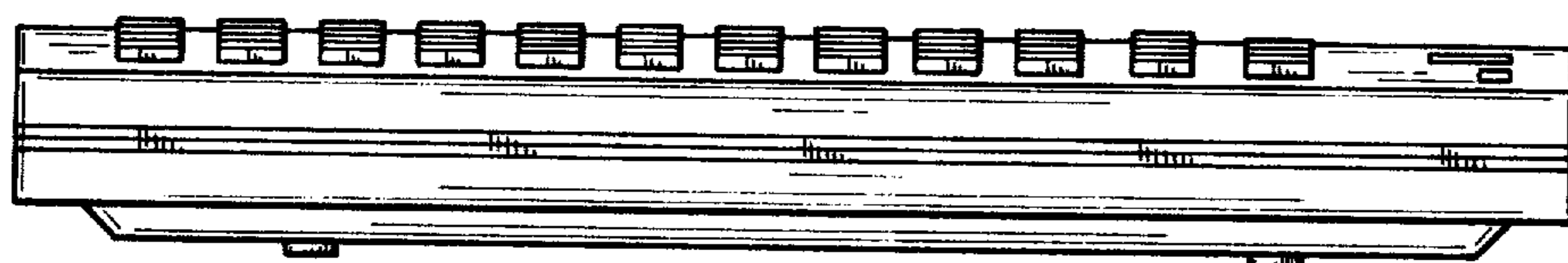
FIG. 1 is a plan view of an electronic translator showing my new design;
FIG. 2 is a bottom view thereof;
FIG. 3 is a front elevational view thereof;
FIG. 4 is a rear elevational view thereof;
FIG. 5 is a right side elevational view thereof; and
FIG. 6 is a left side elevational view thereof.

[51] Int. Cl. D14-02; D18-01
 [52] U.S. Cl. D14/100; D18/7
 [58] Field of Search D18/1, 2, 6-12;
 D14/100, 101, 114, 115; D10/46.1;
 364/706-710; 200/5 A

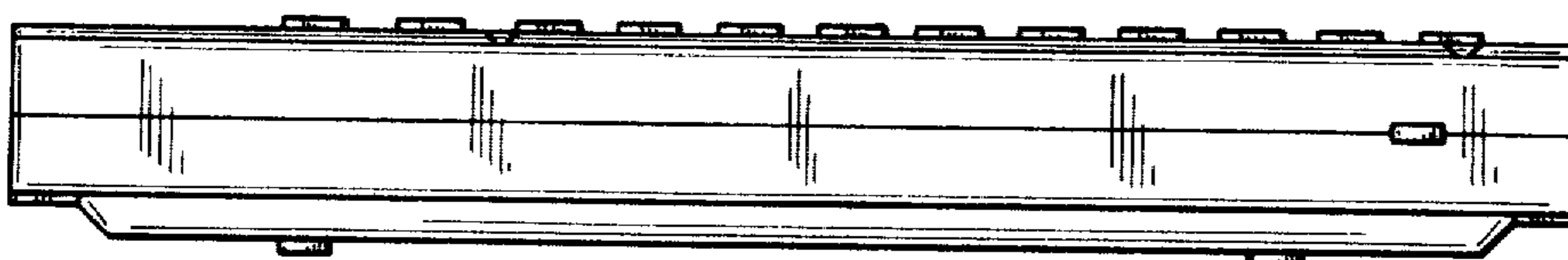




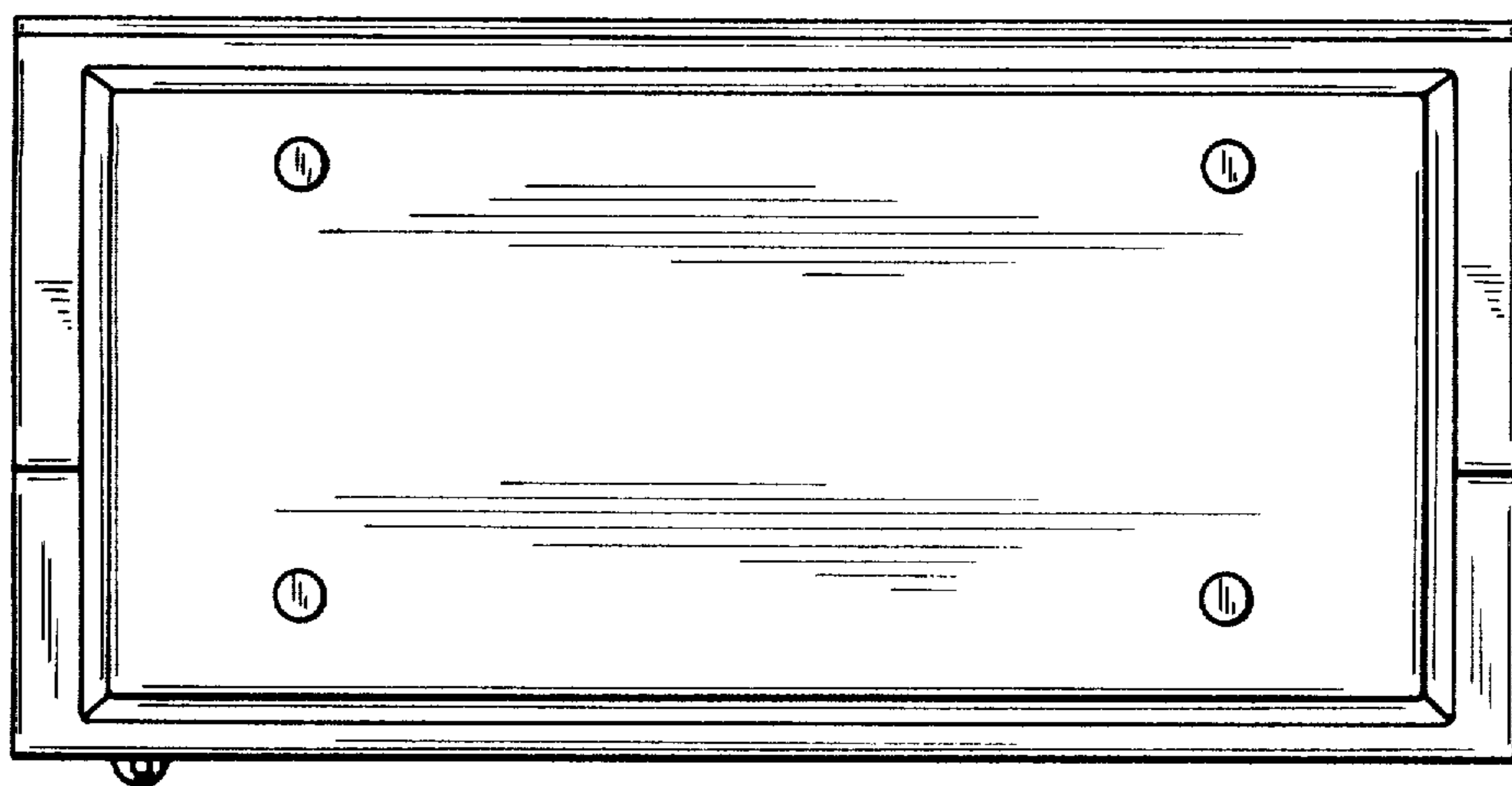
FIG_1



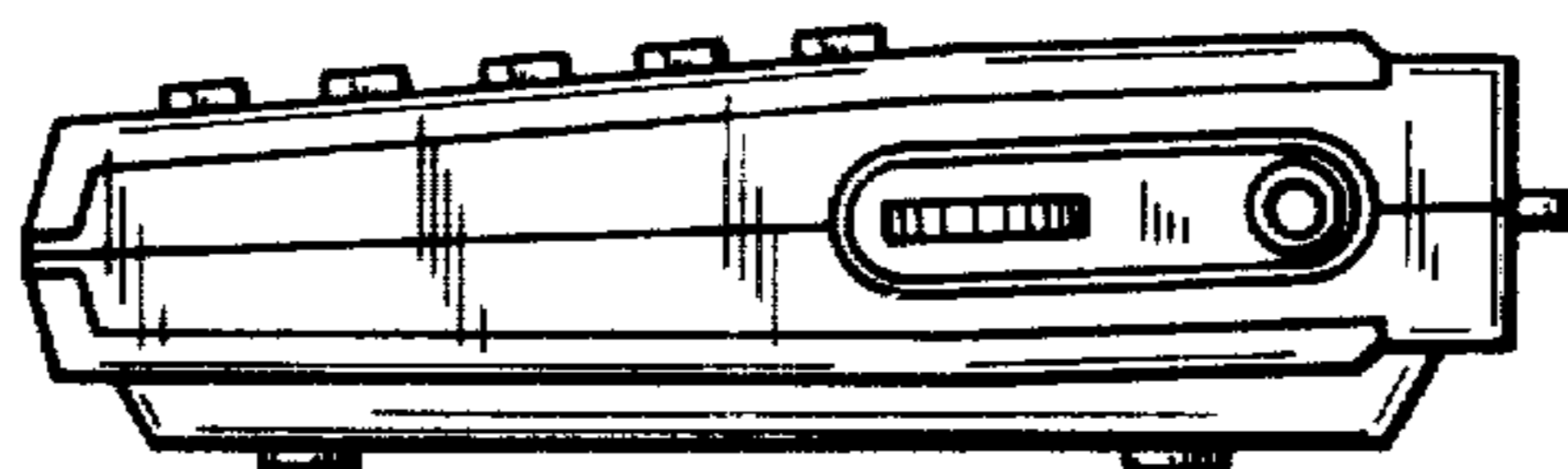
FIG_3



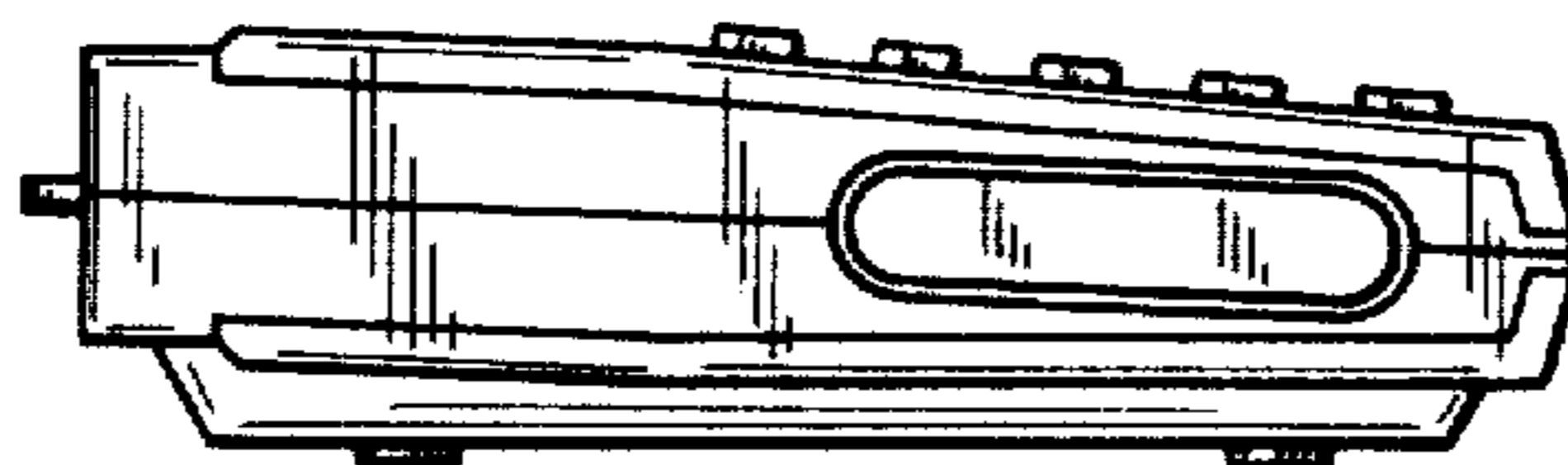
FIG_4



FIG_2



FIG_5



FIG_6