United States Patent [19]

Doyle et al.

[11] Des. 269,427

[45] ** Jun. 21, 1983

[54]	ELECTRO	ELECTRONIC RECEPTACLE	
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[**]	Term:	14 Years	
[21]	Appl. No.:	309,339	
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[56] References Cited			
U.S. PATENT DOCUMENTS			
4,010,432 3/1977 Klein et al 361/332 X			

OTHER PUBLICATIONS

Hubbell, "Designerline" ® Brochure, 1968, p. 2, wall outlet, #3606-I, #3626-I.

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[57]

The ornamental design for an electronic receptacle, as shown and described.

CLAIM

DESCRIPTION

FIG. 1 is a right front perspective view of an electronic receptacle showing our new design;

FIG. 2 is a top plan view thereof on a reduced scale;

FIG. 3 is a front elevational view thereof on a reduced scale;

FIG. 4 is a right side elevational view thereof on a reduced scale;

FIG. 5 is a left side elevational view thereof on a reduced scale;

FIG. 6 is a rear elevational view thereof on a reduced scale;

FIG. 7 is a bottom plan view thereof on a reduced scale.

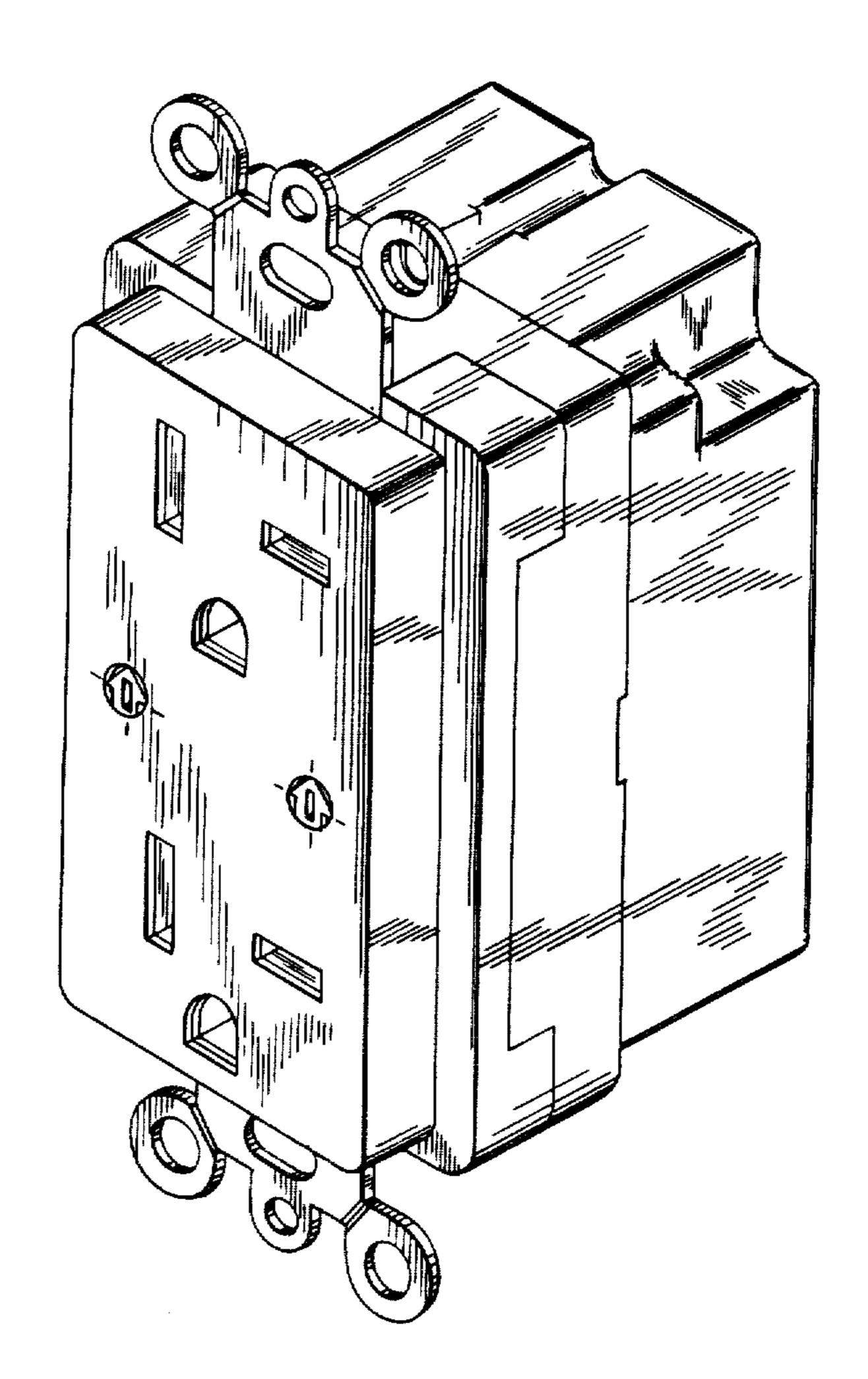
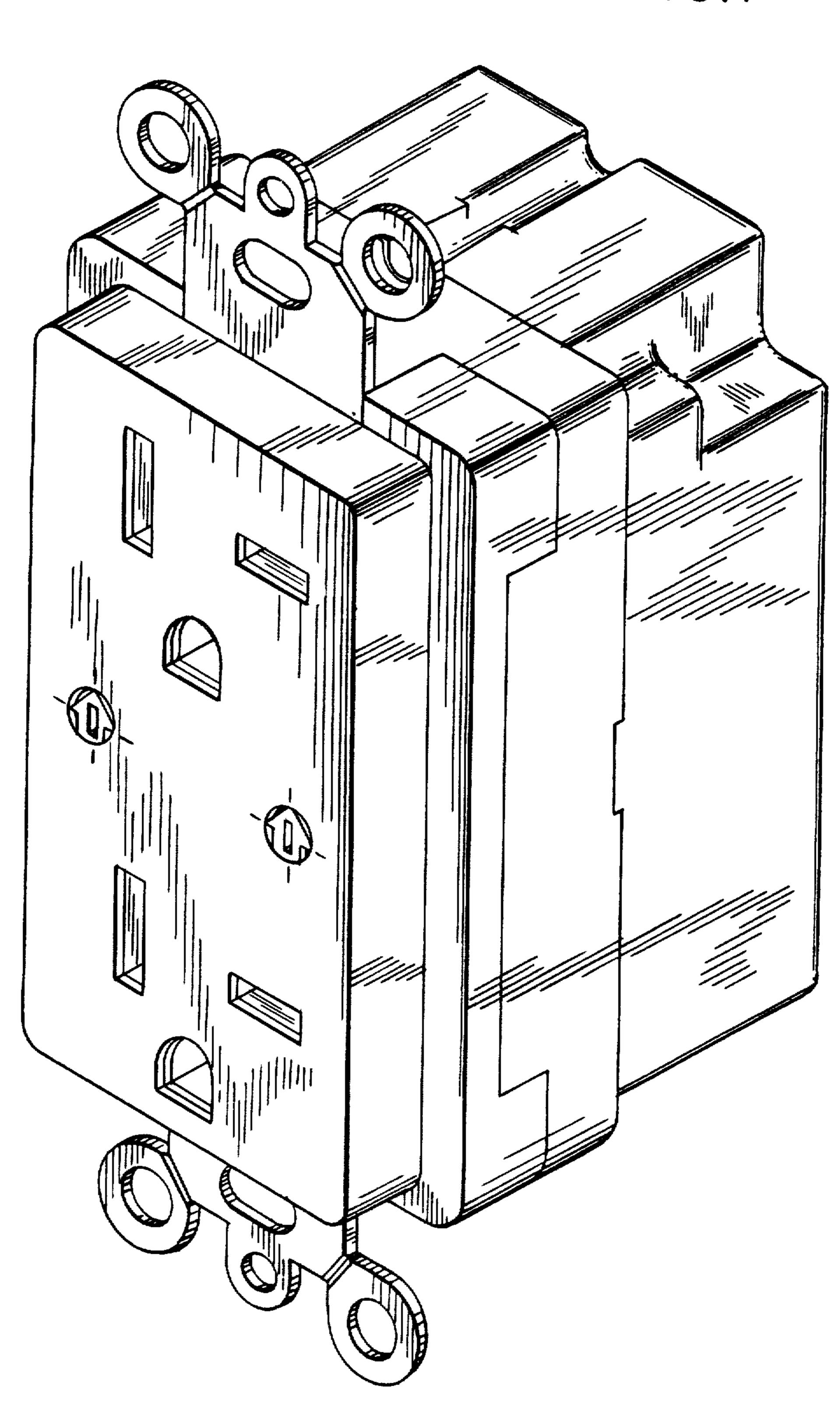
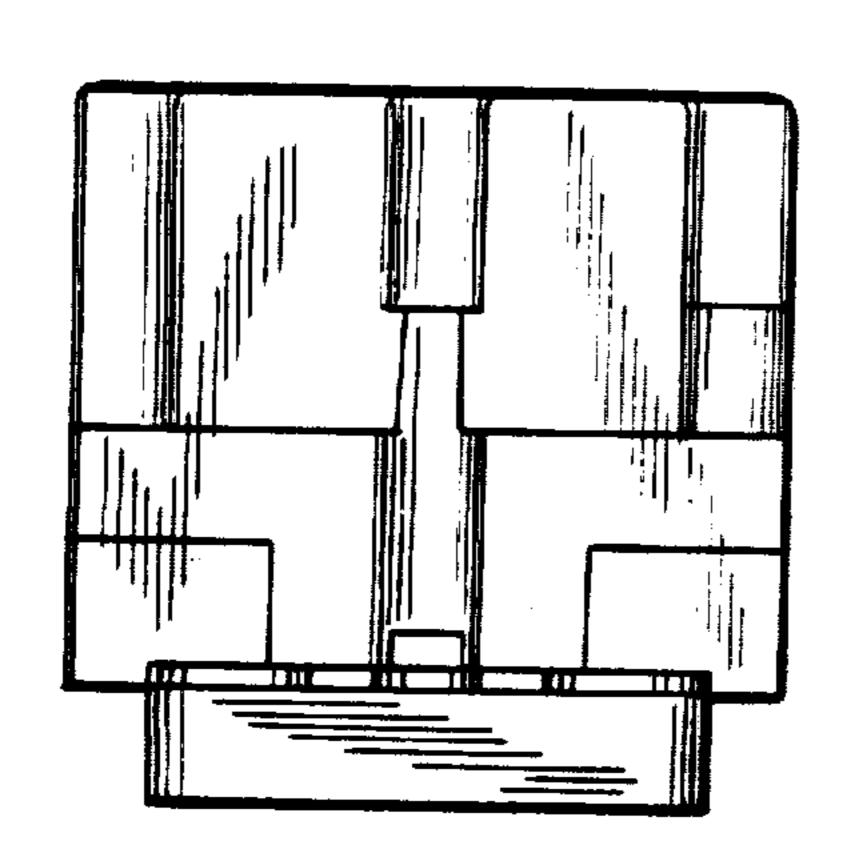


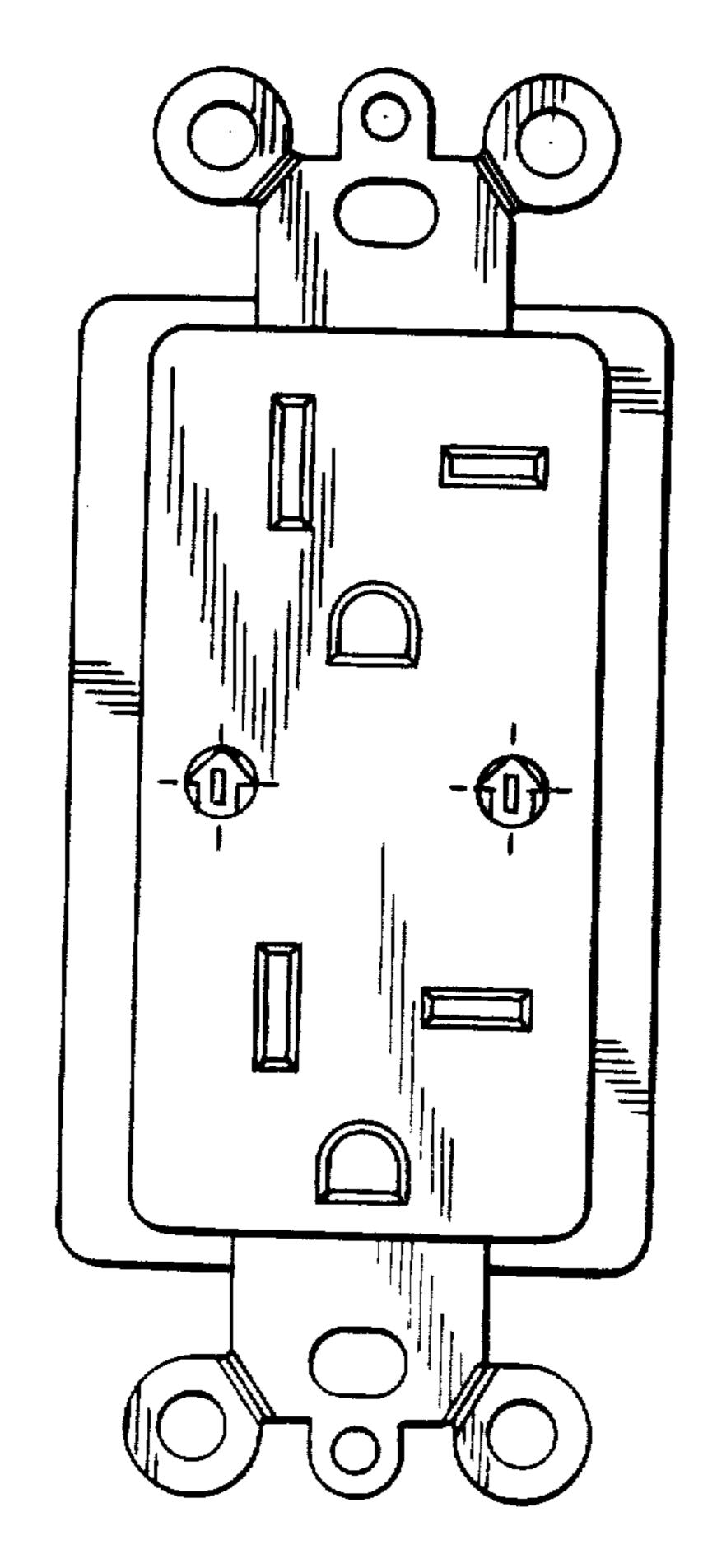
FIG. 1



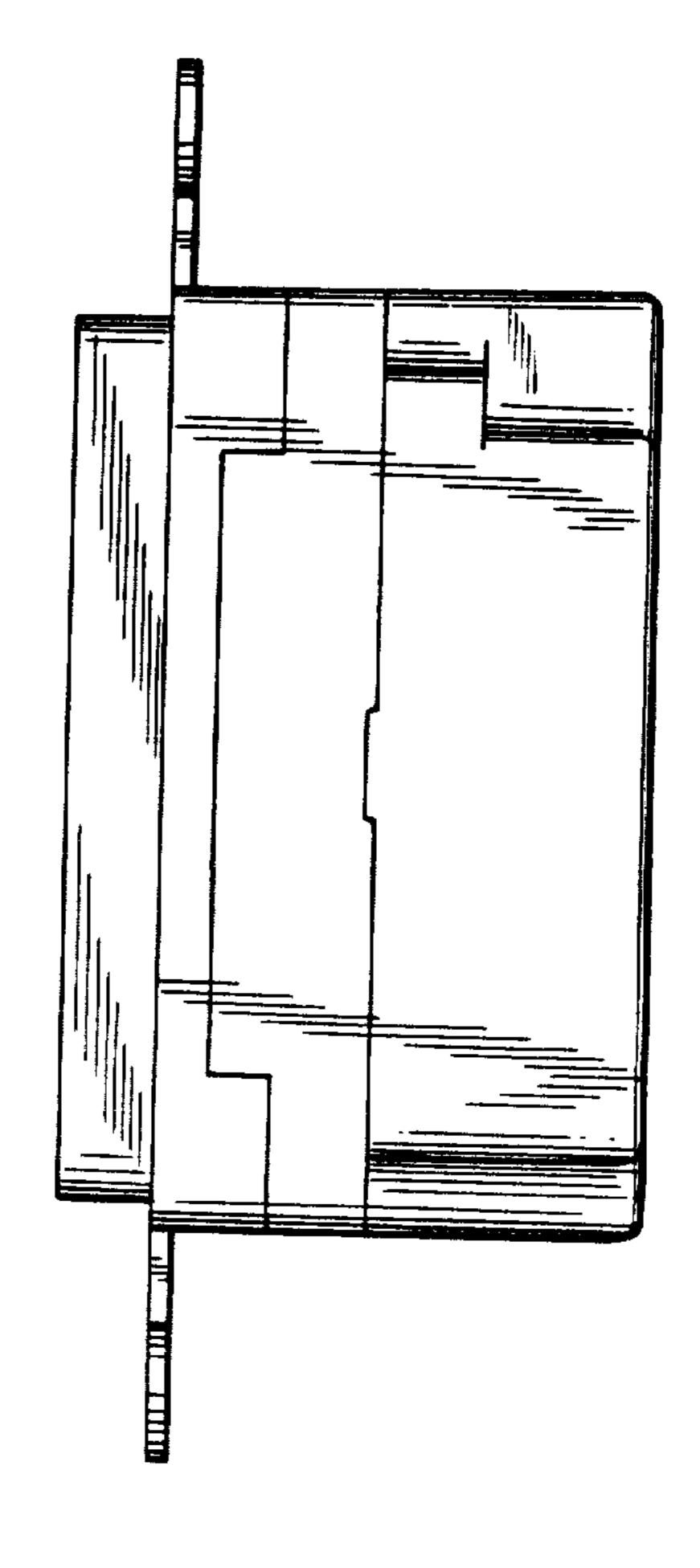
F16.2



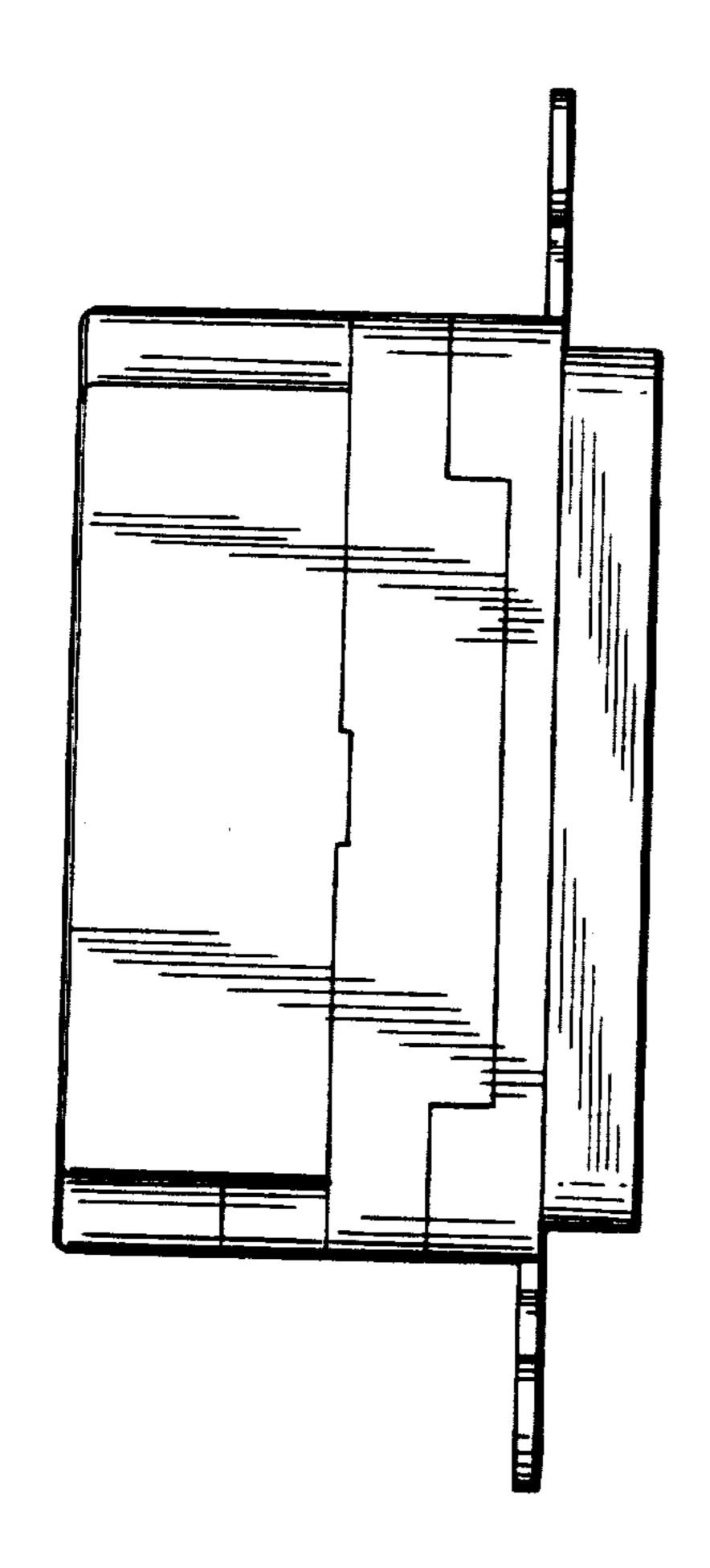
F/G. 3



F1G.4



F/G.5



F/G.6

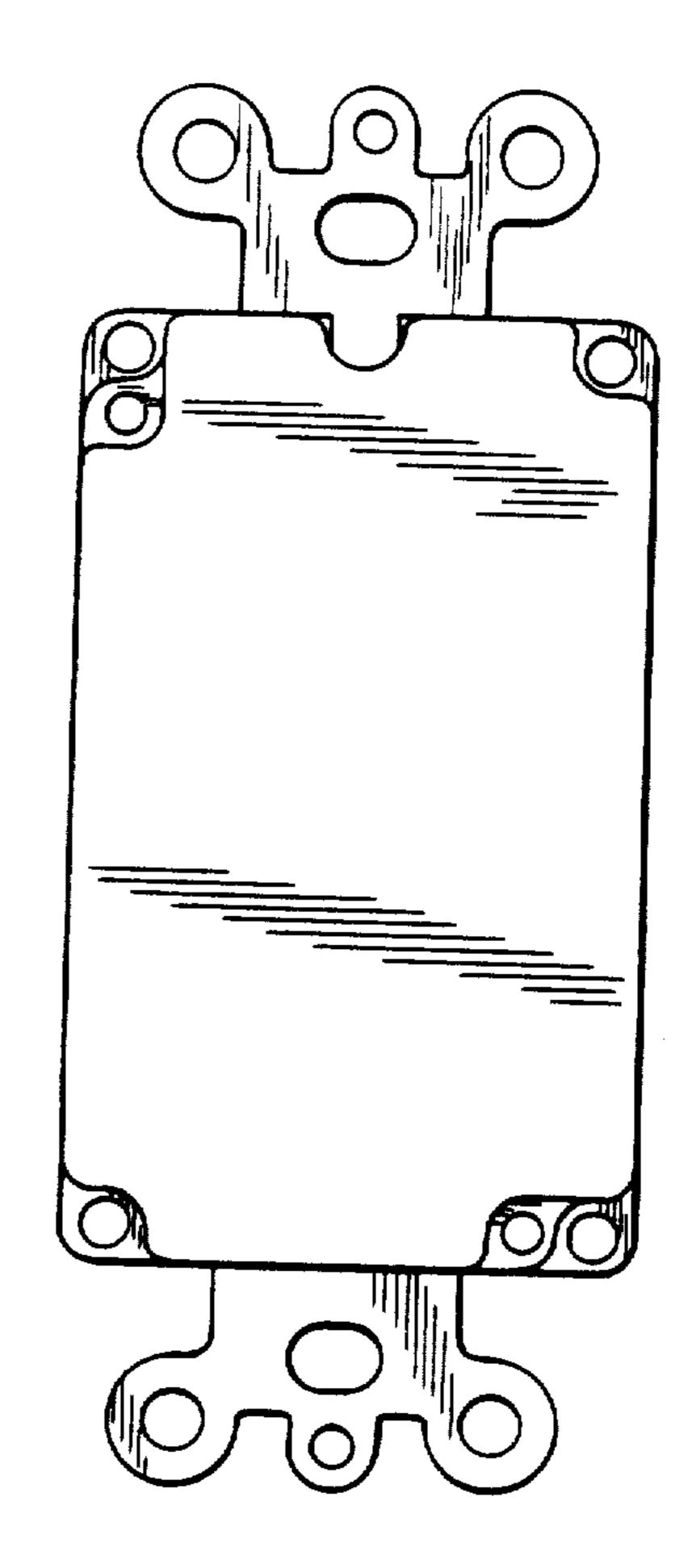


FIG.7

