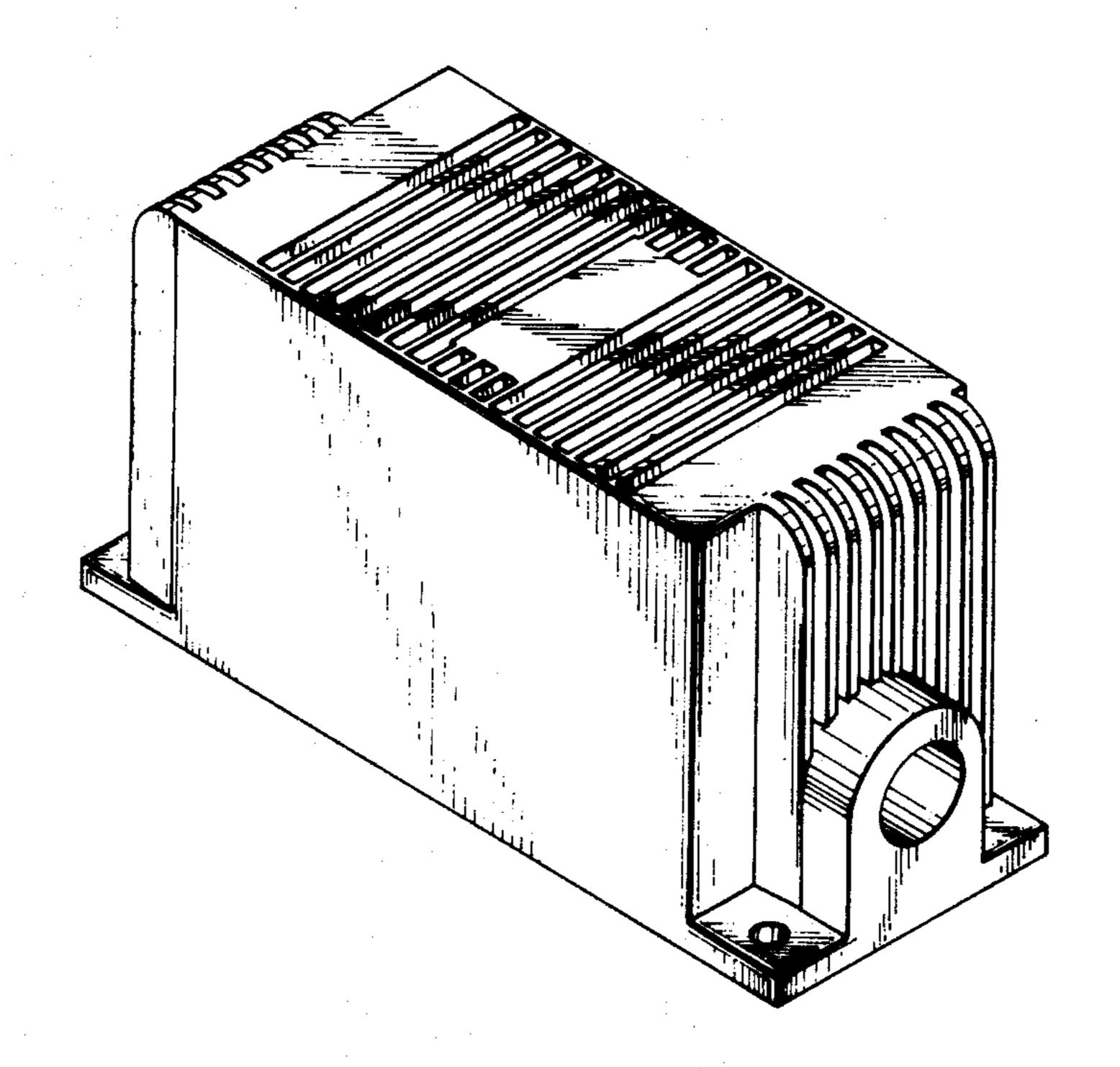
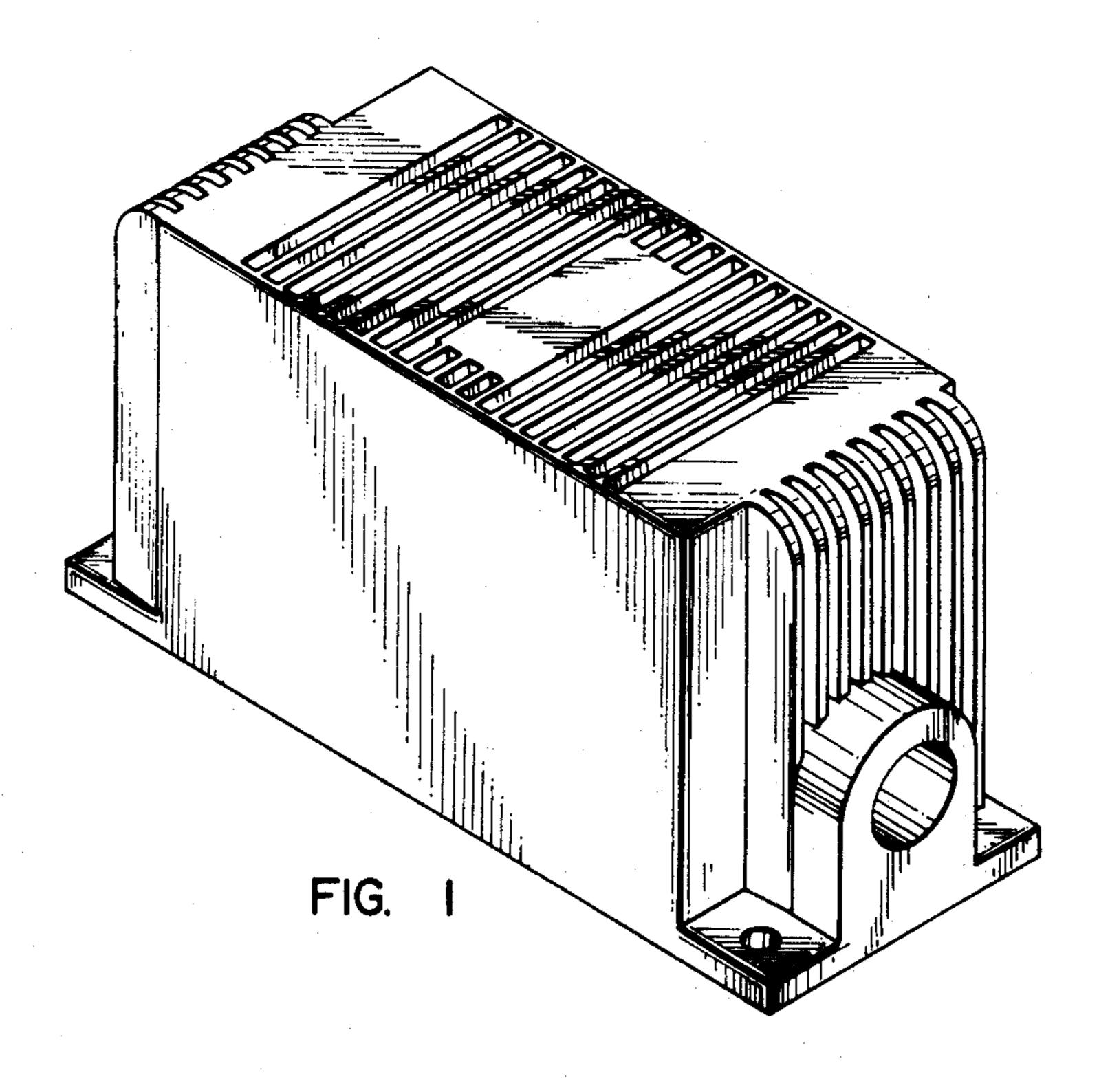
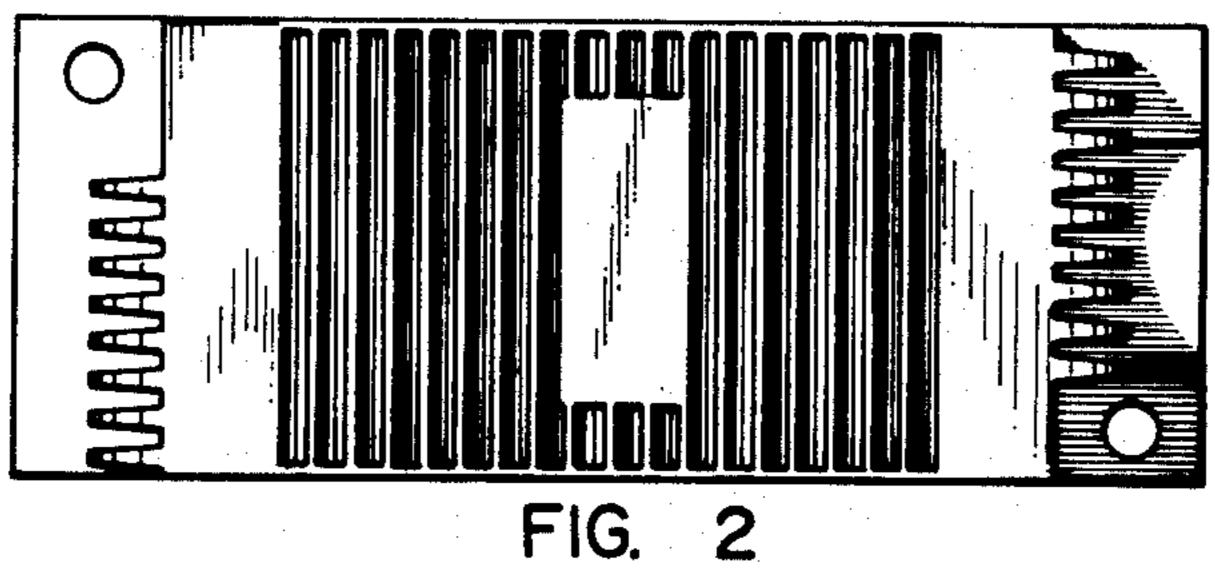
## Comstock

[45] \*\* Aug. 7, 1979

[54]	54] HOUSING FOR ELECTRICAL CIRCUITS		Primary Examiner—Susan J. Lucas	
[76]	Inventor:	Wilford K. Comstock, 8152 S. Danish Rd., Sandy, Utah 84070	Attorney, Agent, or Firm—Calvin E. Thorpe	
			[57] CLAIM	
[**]	Term:	14 Years	The ornamental design for a housing for electrical circuits, substantially as shown and described.  DESCRIPTION	
[21]	Appl. No.:	808,882		
[22]	Filed:	Jun. 22, 1977	FIG. 1 is a perspective view of a housing for electrical	
[51]	Int. Cl		circuits showing my new design;	
	•		FIG. 2 is a top plan view thereof;	
[58] Field of Search			FIG. 3 is a side elevational view thereof, the opposite side being a mirror image;	
:	174/53, 58, 59, 60; 361/331, 356, 377, 392; 220/3.2; D13/12, 40, 41		FIG. 4 is an end, elevational view as seen from the left of FIGS. 2 and 3; FIG. 5 is an opposite, end, elevational view as seen from	
[56]	References Cited			
U.S. PATENT DOCUMENTS			the right of FIGS. 2 and 3; and FIG. 6 is a bottom view thereof.	
D. 2	30,406 2/197	4 Hampton	The side opposite that shown in FIG. 3 is substantially	
•	76,424 3/195		the same as that of FIG. 3 and is therefore not shown	
	51,221 11/197		separately.	







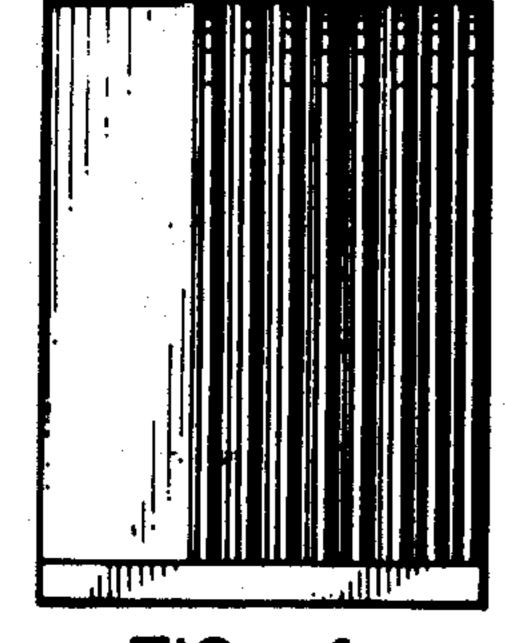


FIG. 4

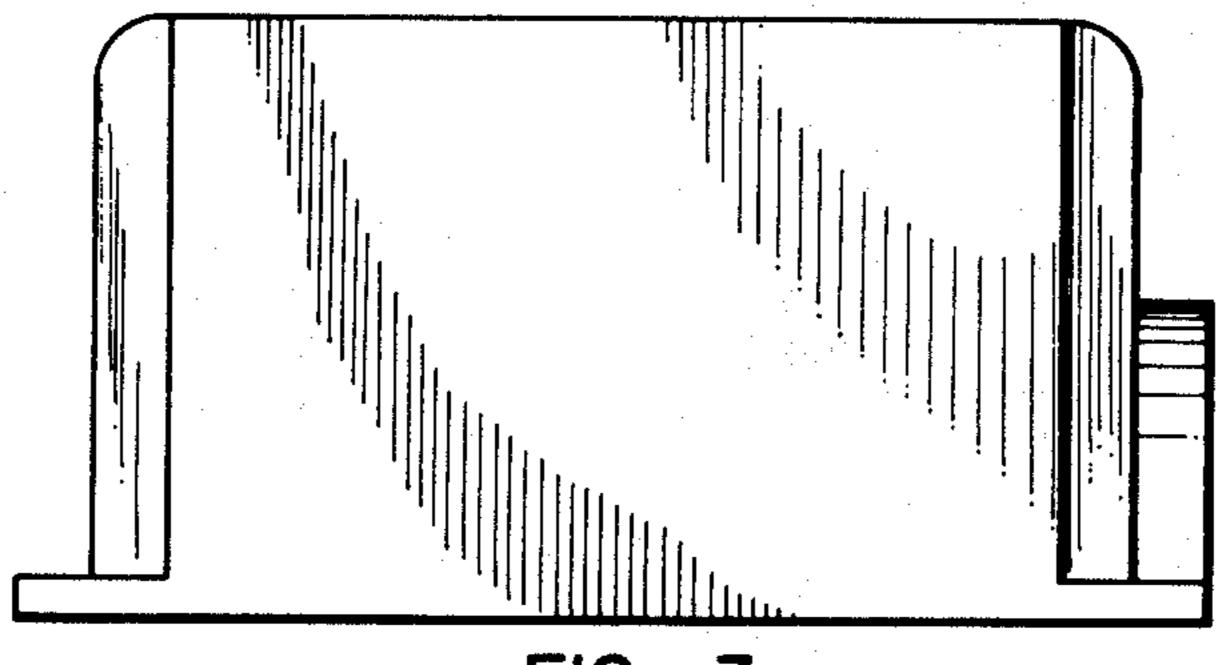


FIG. 3

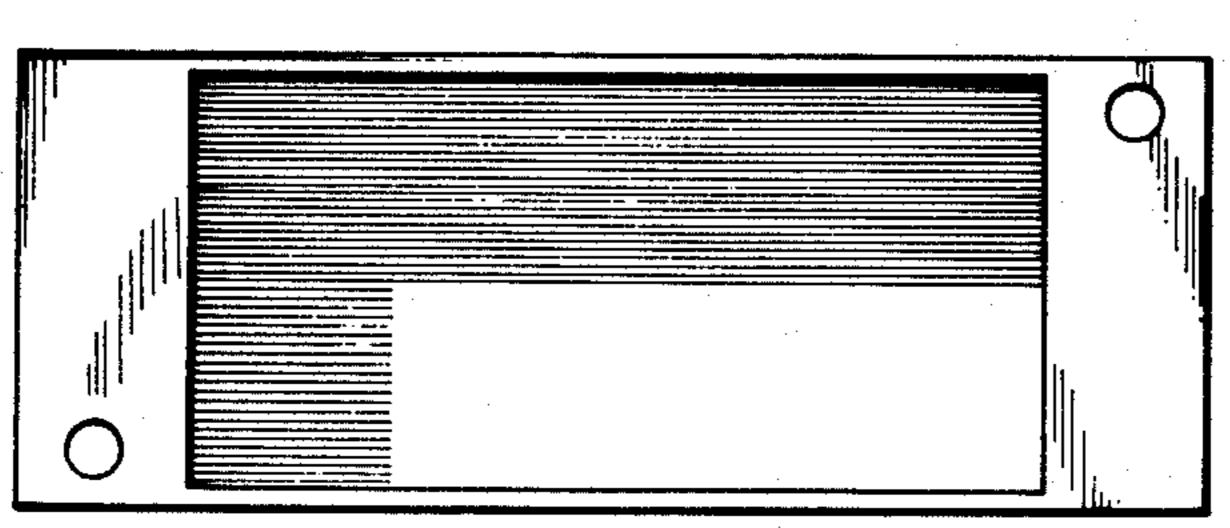


FIG. 6

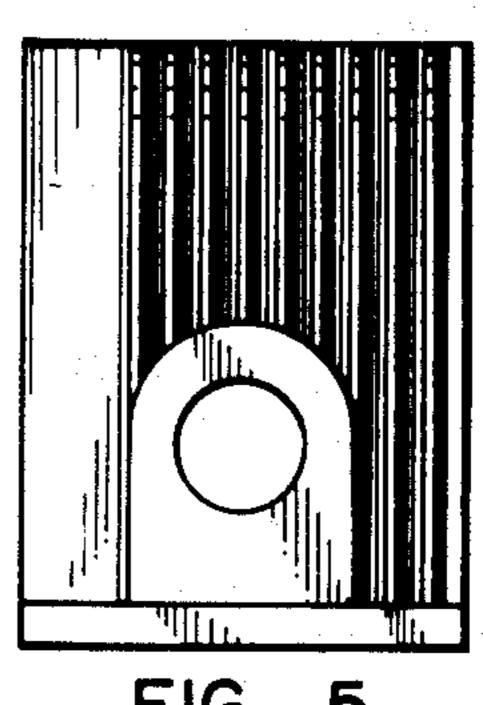


FIG. 5