

[54] DIGITAL CLOCK

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[**] Term: 14 Years

[21] Appl. No.: 835,249

[22] Filed: Sep. 21, 1977

[30] Foreign Application Priority Data

Mar. 3, 1977 [JP] Japan 52-11708

[51] Int. Cl. D10-01

[52] U.S. Cl. D10/15

[58] Field of Search D10/1, 15, 14, 21, 24, D10/27, 25, 26, 40; 58/50 R, 50 A, 152 R, 53-56, 23 R; D14/73

[56] References Cited

U.S. PATENT DOCUMENTS

D. 236,189 8/1975 Daher D10/15
D. 236,445 8/1975 Danz D14/73 X

OTHER PUBLICATIONS

European Jeweler, 5/74, p. 88, dig. clock at top.

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Attorney, Agent, or Firm—Sewall P. Bronstein

[57] CLAIM

The ornamental design for digital clock, as shown.

DESCRIPTION

FIG. 1 is a front perspective view of showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a top plan view thereof;

FIG. 4 is a right side elevational view thereof;

FIG. 5 is a left side elevational view thereof;

FIG. 6 is a rear elevational view thereof; and

FIG. 7 is a bottom plan view thereof.

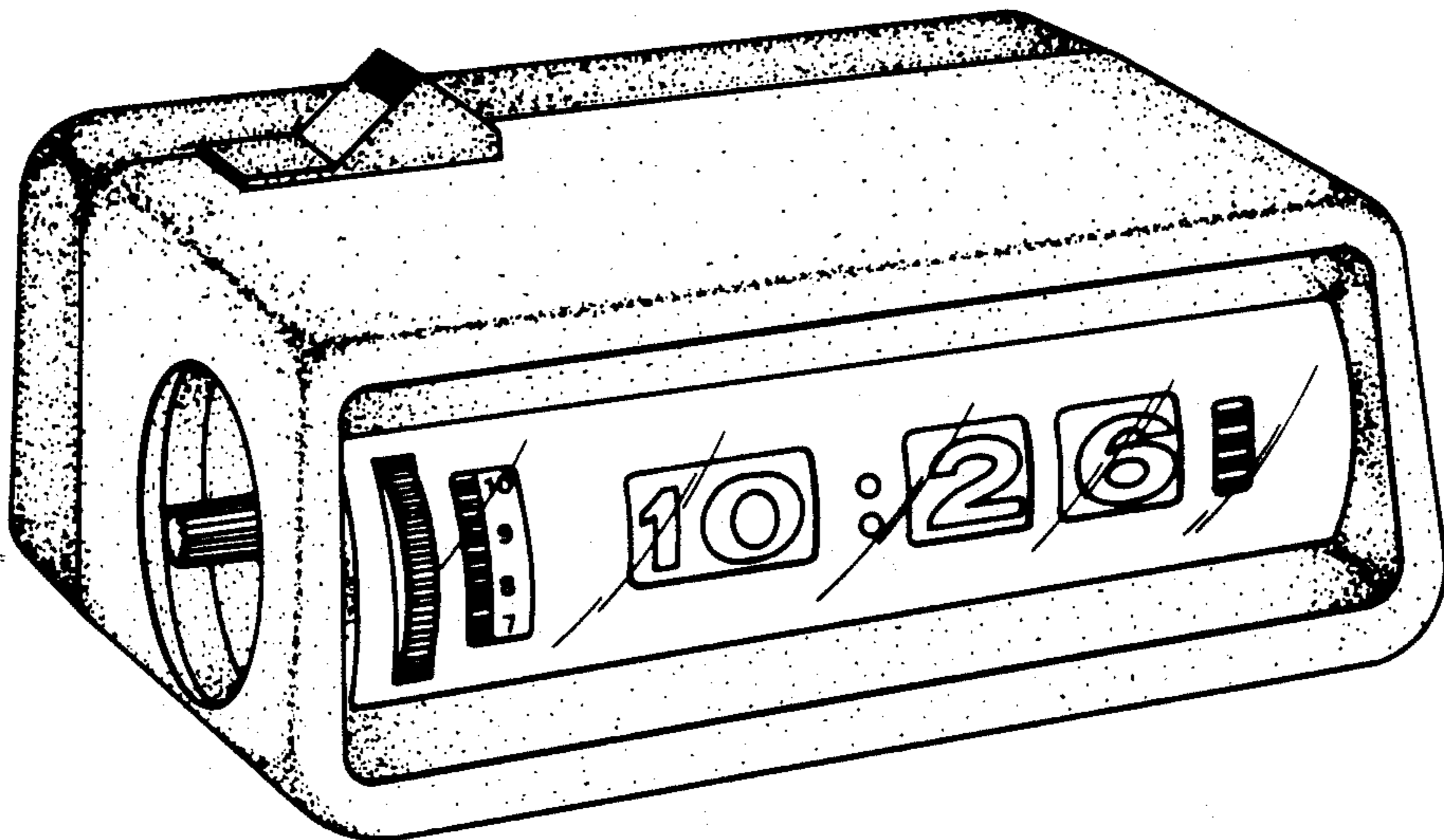


FIG. 1

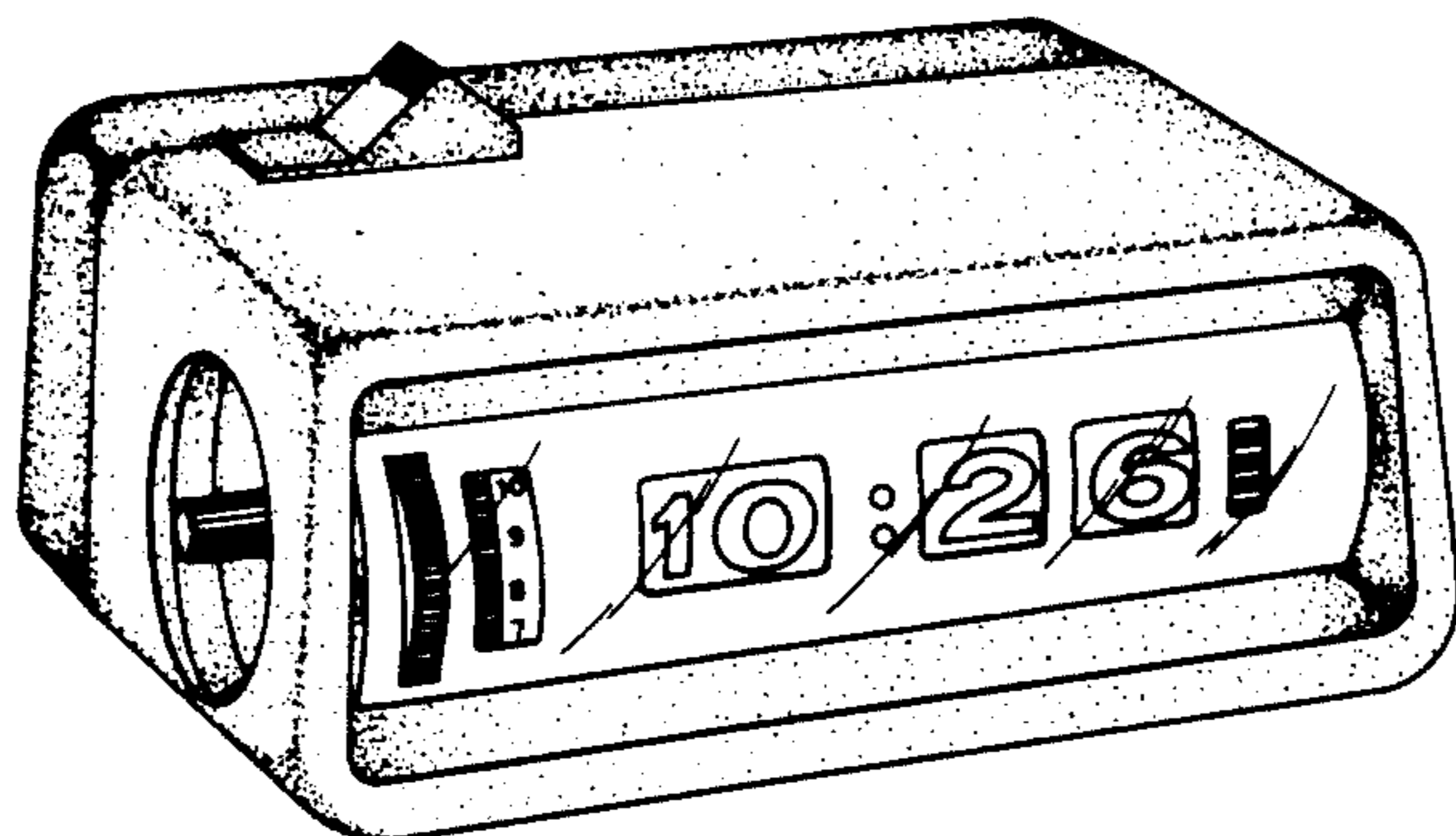


FIG. 2

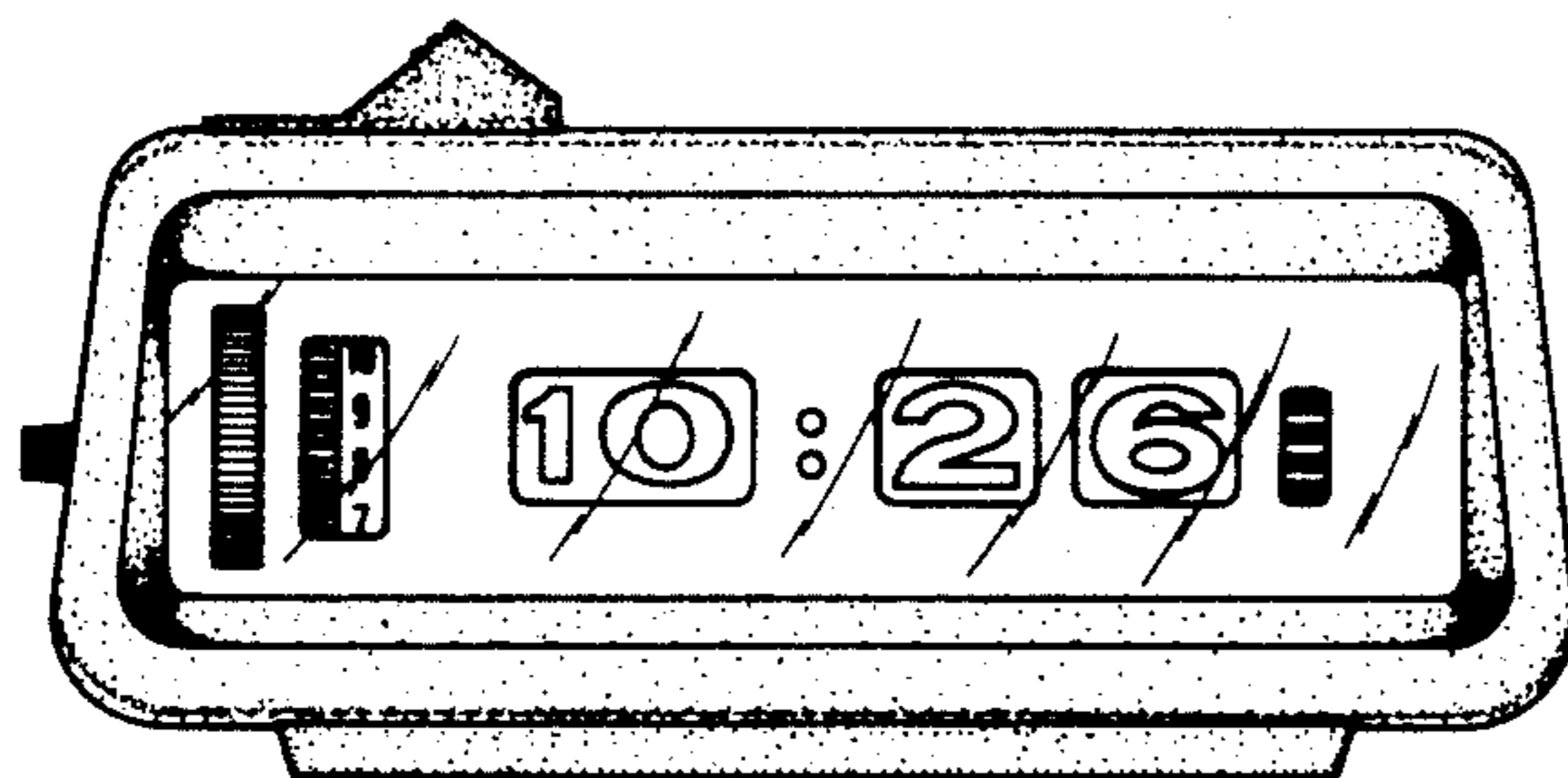


FIG. 3

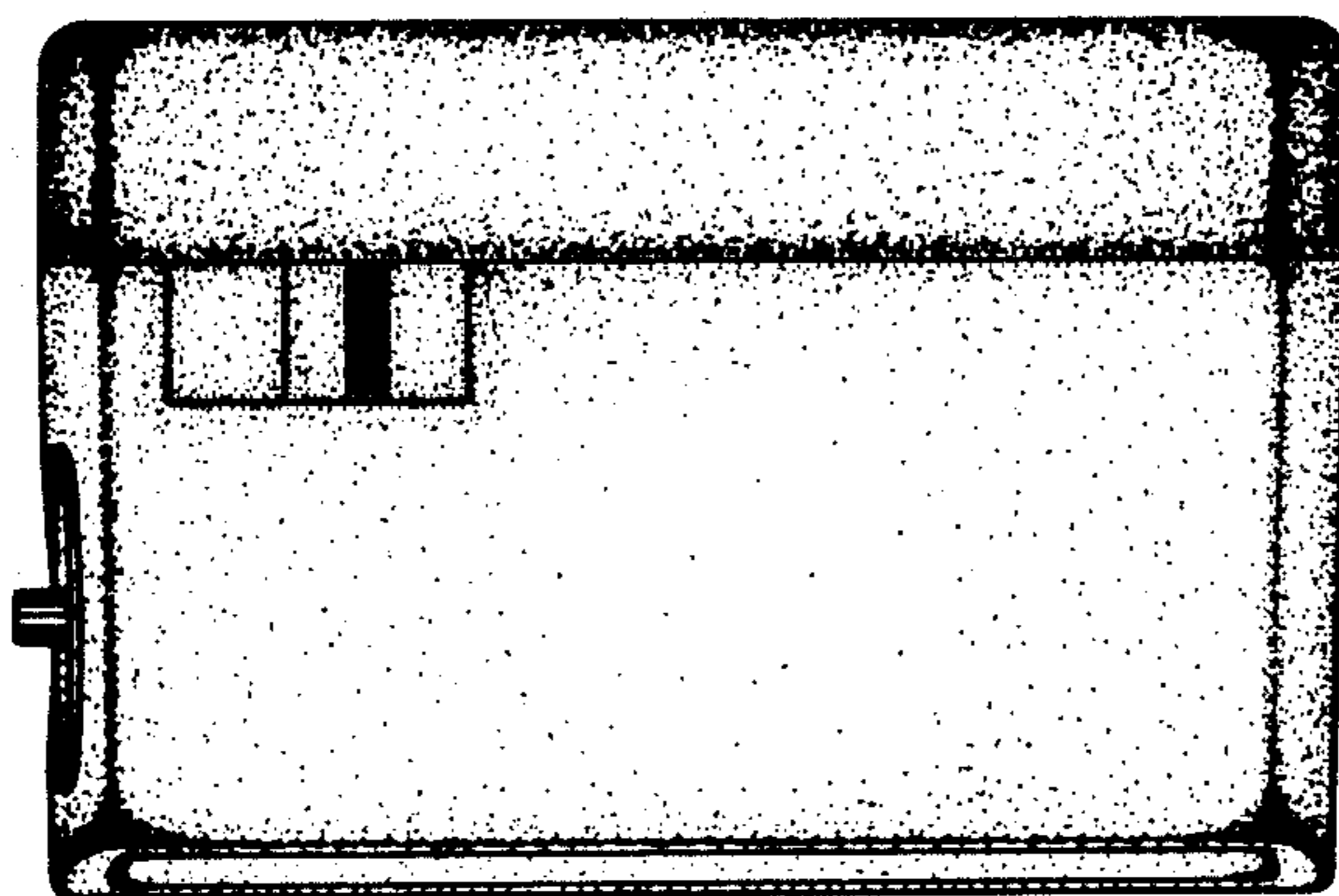


FIG.4

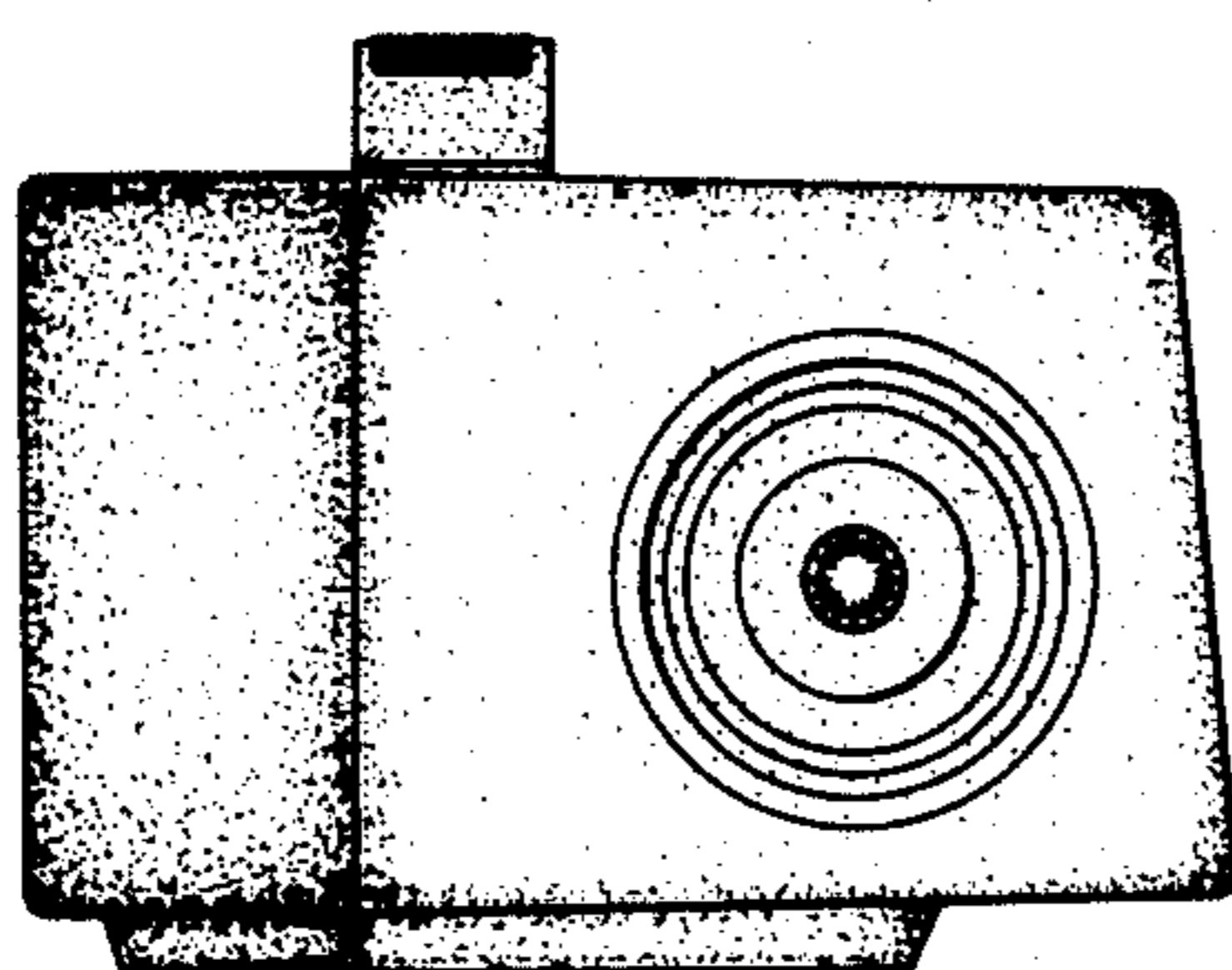


FIG.5

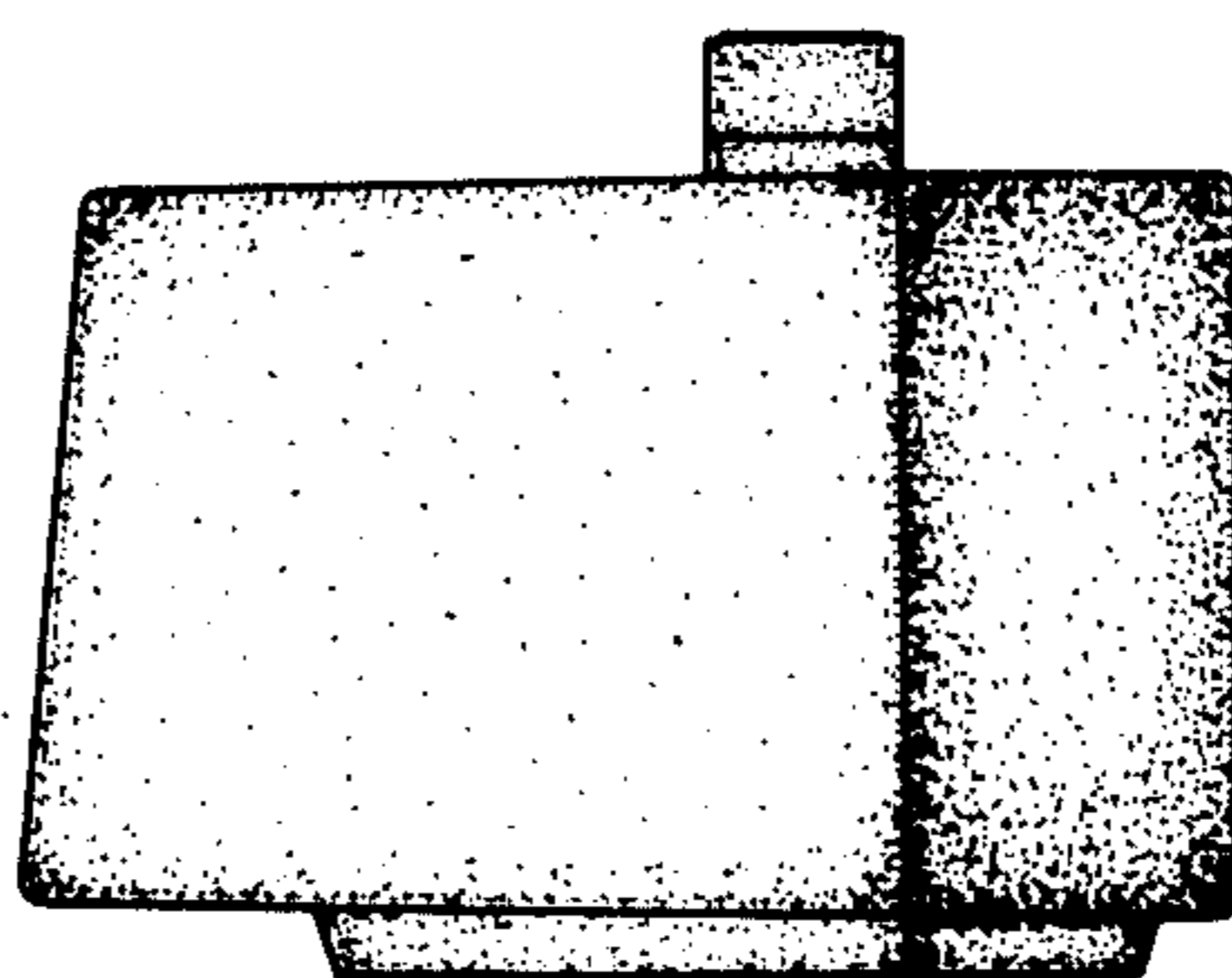


FIG.6

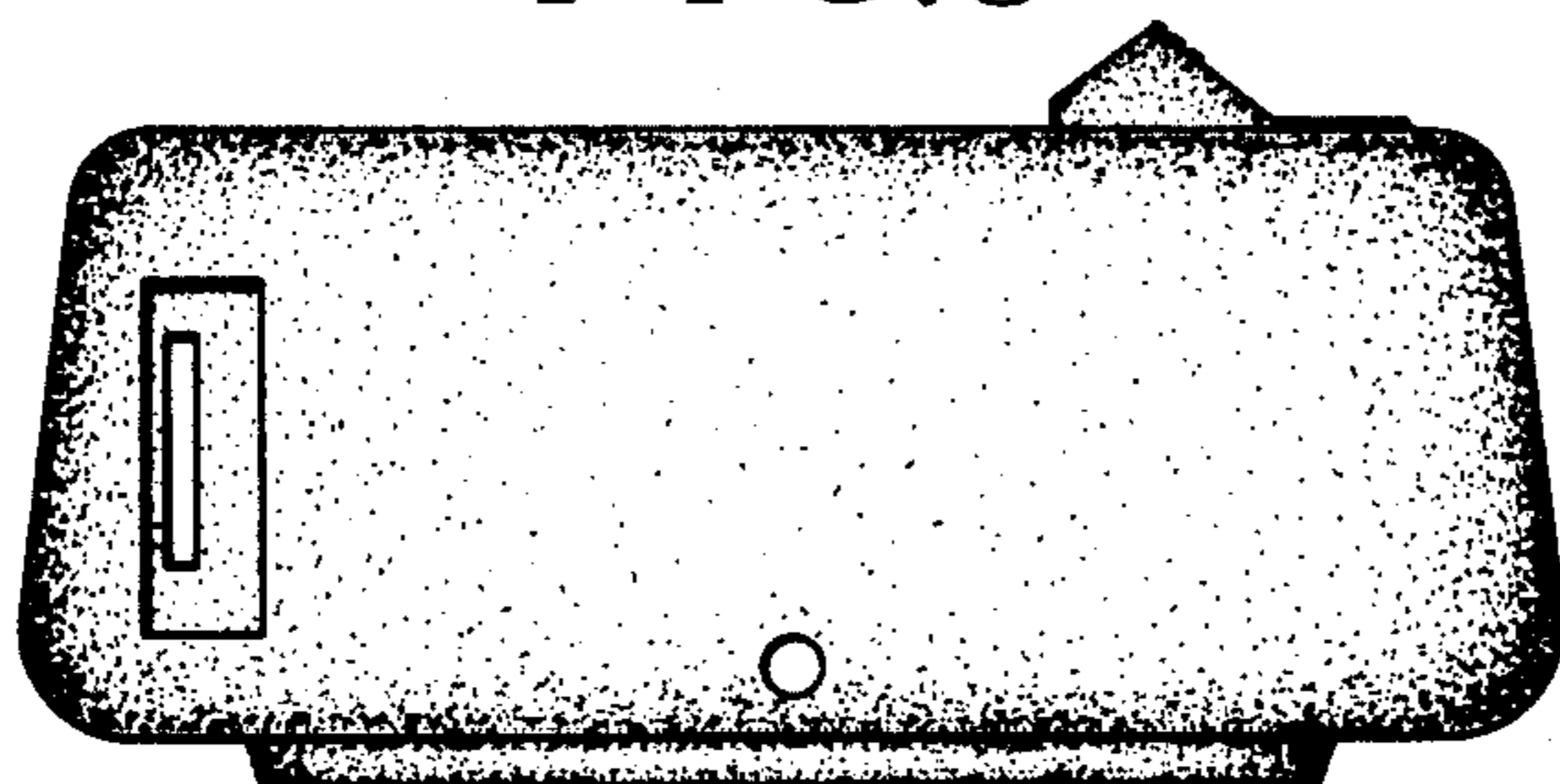
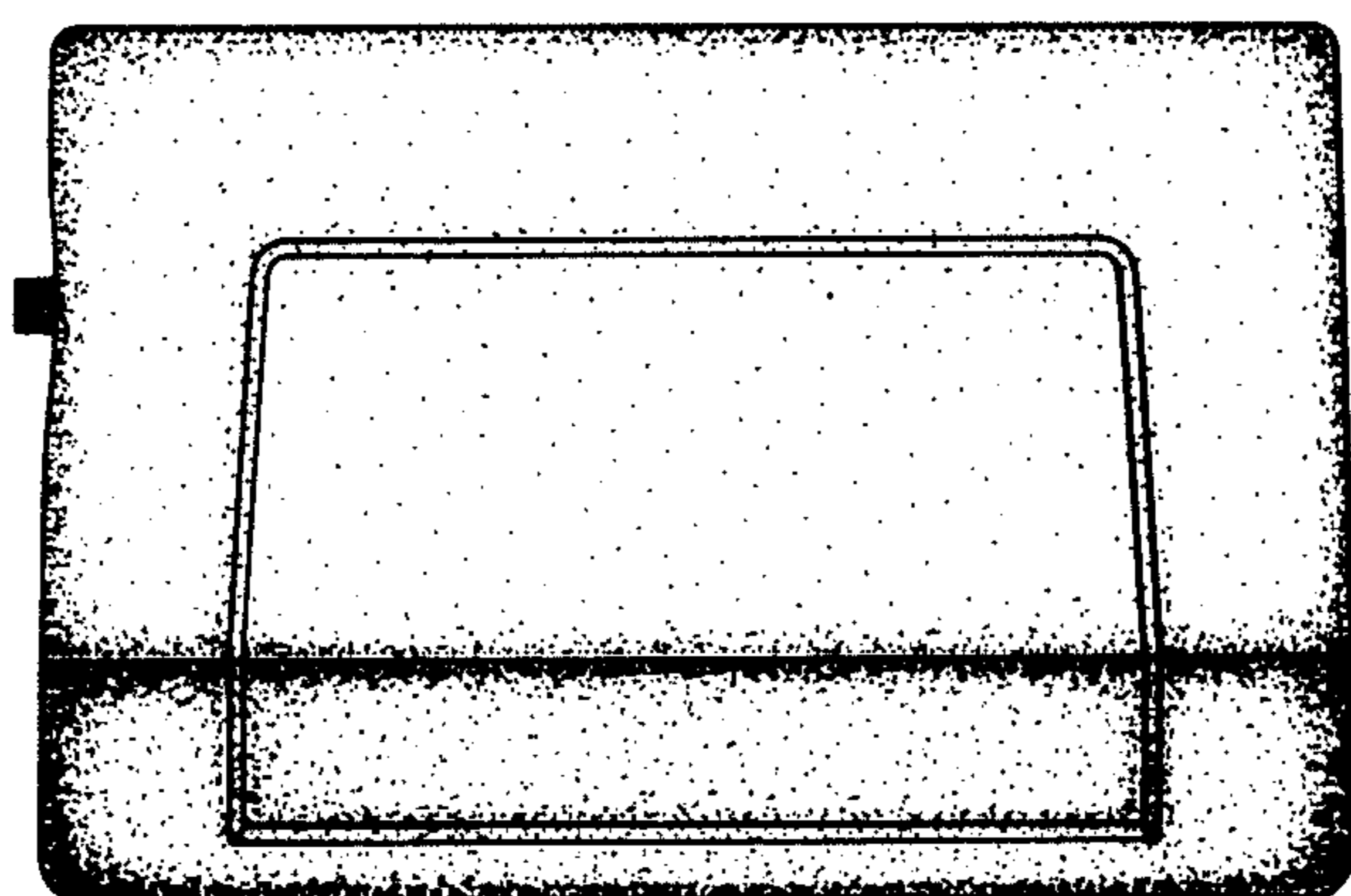


FIG.7



UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : Des. 252,498

DATED : July 31, 1979

INVENTOR(S) : Koji Terada

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

Insert name of Assignee: -- TAMURA ELECTRIC WORKS, LTD. --

Signed and Sealed this

Twenty-seventh Day of November 1979

[SEAL]

Attest:

RUTH C. MASON
Attesting Officer

LUTRELLE F. PARKER
Acting Commissioner of Patents and Trademarks