

[54] **ELECTRONIC PROCESSOR**
[75] Inventor: **Richard R. Calverley**, Chatsworth,
Calif.
[73] Assignee: **Computer Kinetics Corporation**,
Westlake Village, Calif.
[**] Term: **14 Years**
[21] Appl. No.: **736,434**
[22] Filed: **Oct. 28, 1976**
[51] Int. Cl. **D14—02**
[52] U.S. Cl. **D14/42**
[58] Field of Search **D14/40, 41, 42, 43,**
D14/44, 45, 46, 47, 49, 50; D13/40, 41

[56] **References Cited**
U.S. PATENT DOCUMENTS
D. 203,989 3/1966 Wiggerman D14/45

D. 225,647 12/1972 Brooks et al. D14/45
D. 232,126 7/1974 Conway et al. D14/45 X

OTHER PUBLICATIONS

Wang Laboratories Flyer, Programmable Calculator.

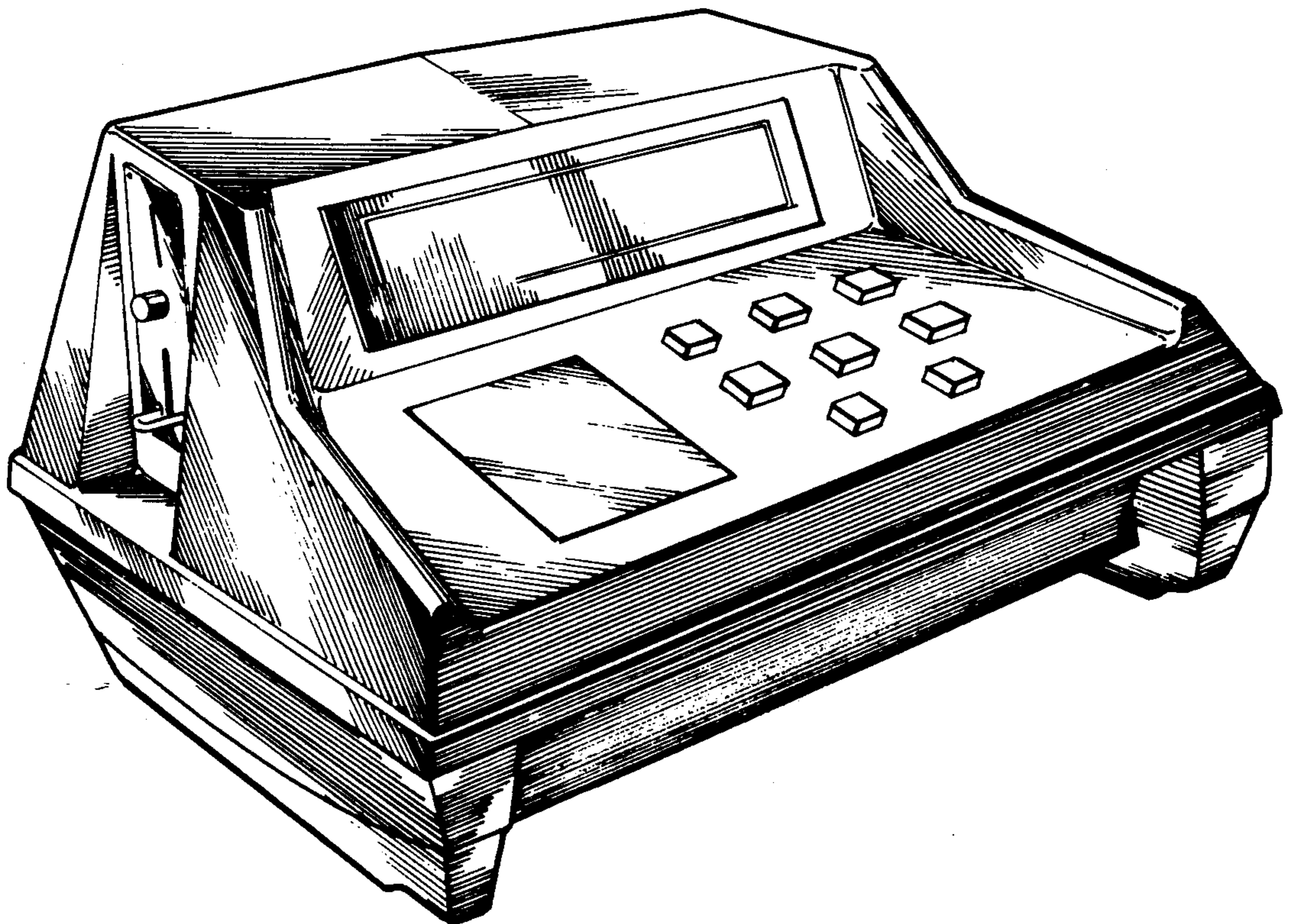
Primary Examiner—Susan J. Lucas

[57] **CLAIM**

The ornamental design for an electronic processor, as shown.

DESCRIPTION

FIG. 1 is a perspective view of an electronic processor showing my new design;
FIG. 2 is a left side elevational view thereof;
FIG. 3 is a right side elevational view thereof;
FIG. 4 is a top plan view thereof;
FIG. 5 is a front elevational view thereof;
FIG. 6 is a bottom view thereof; and
FIG. 7 is a rear elevational view thereof.



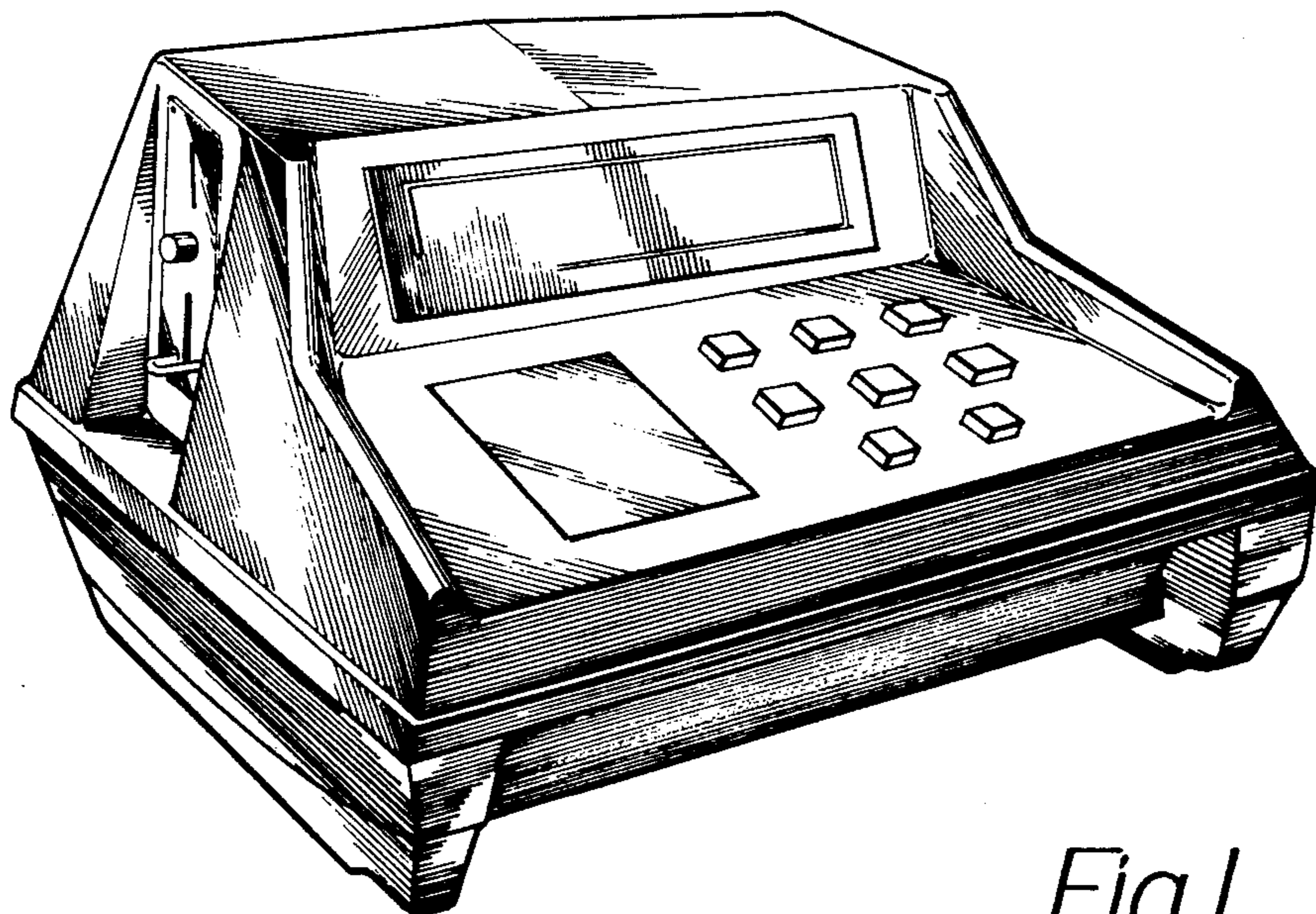


Fig. 1.

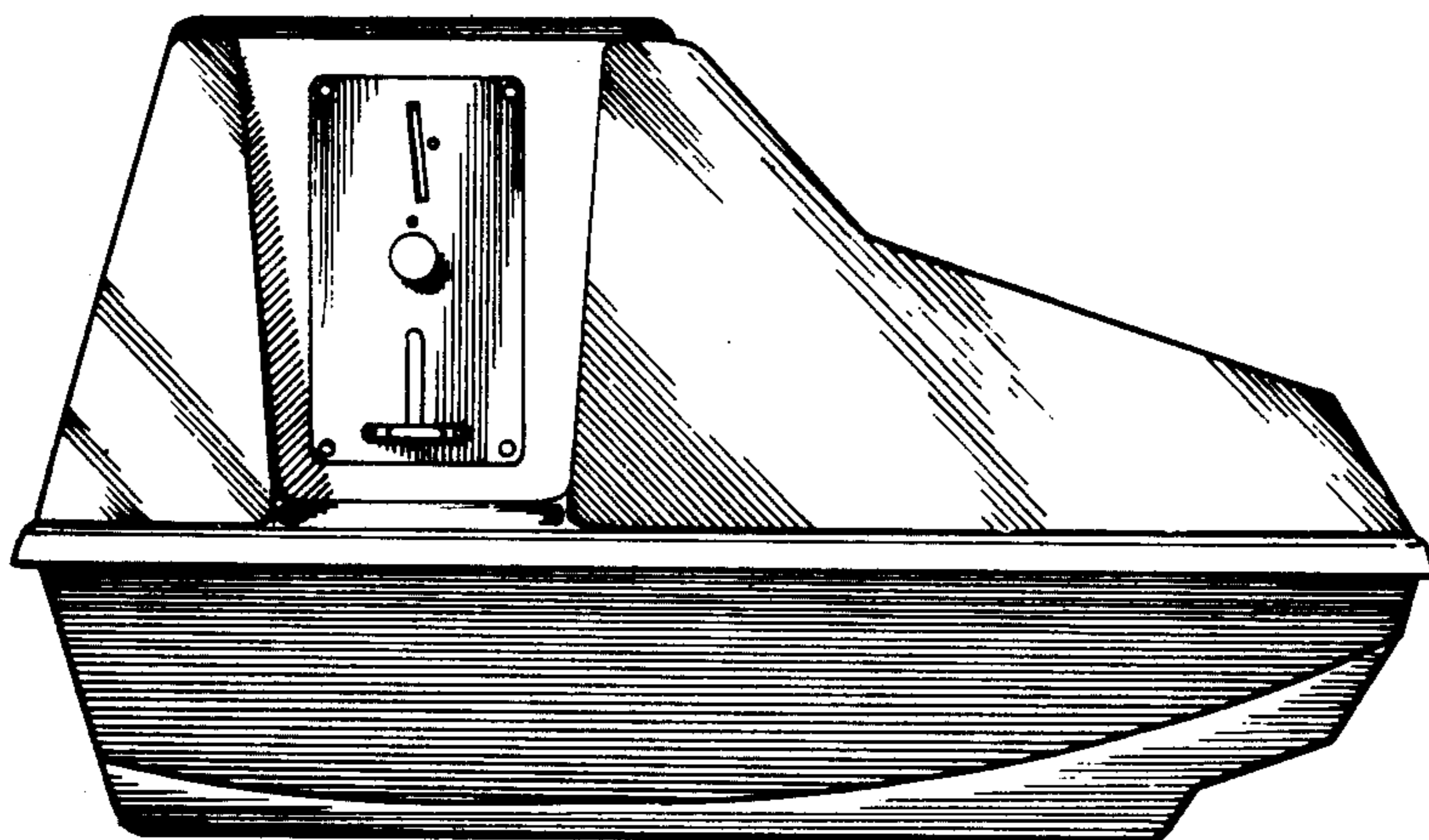
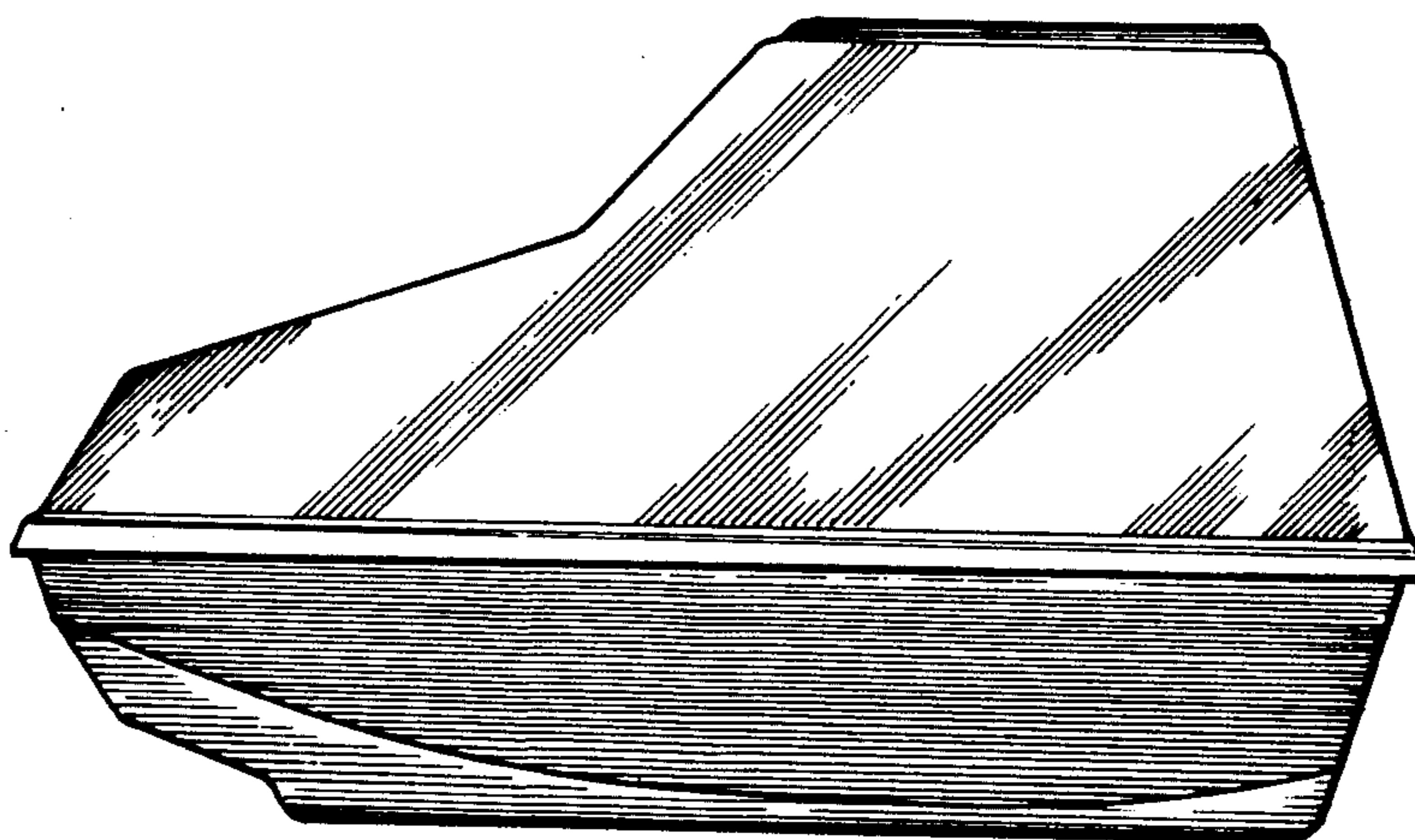


Fig. 2.

Fig. 3.



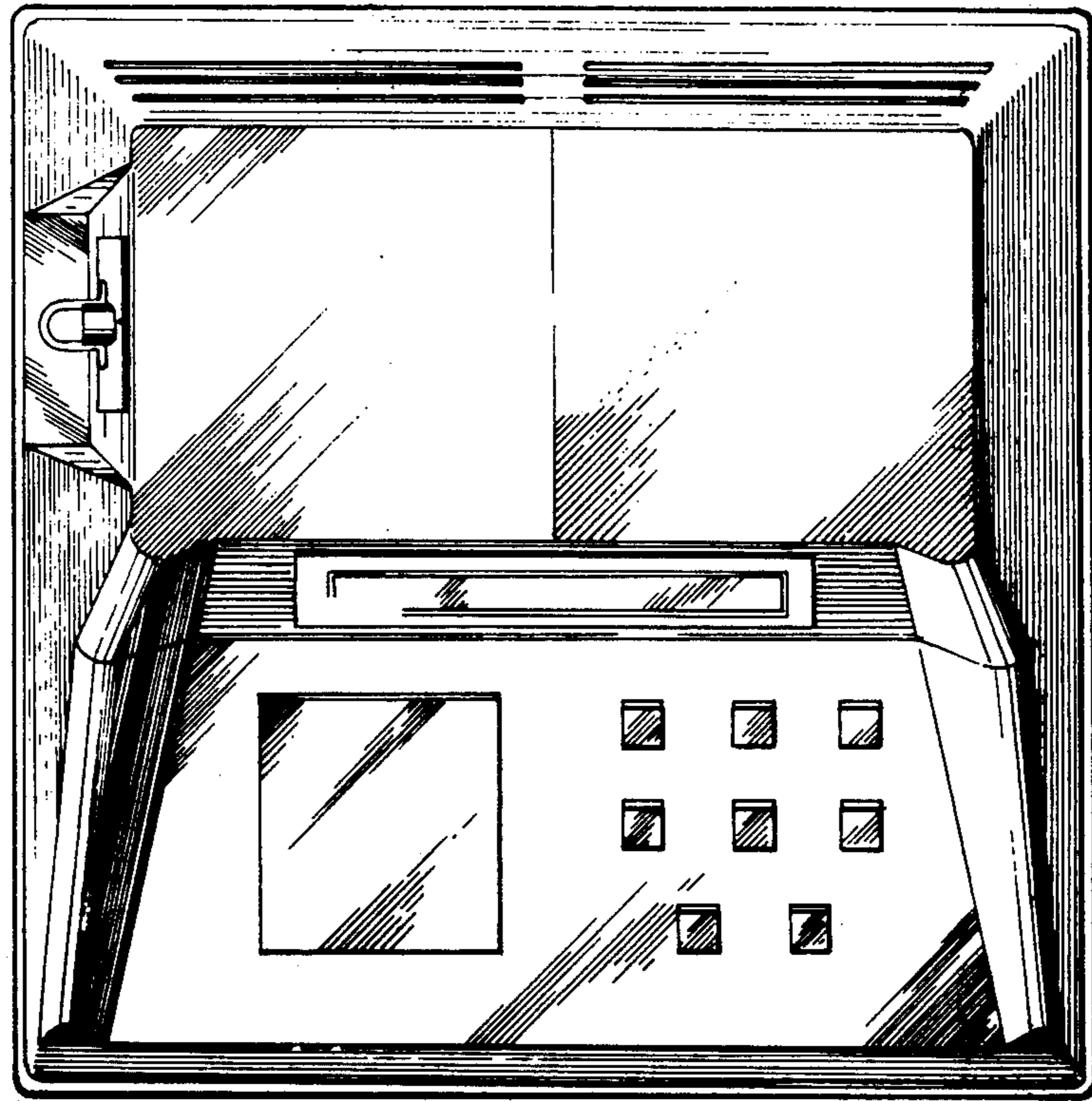


Fig. 4.

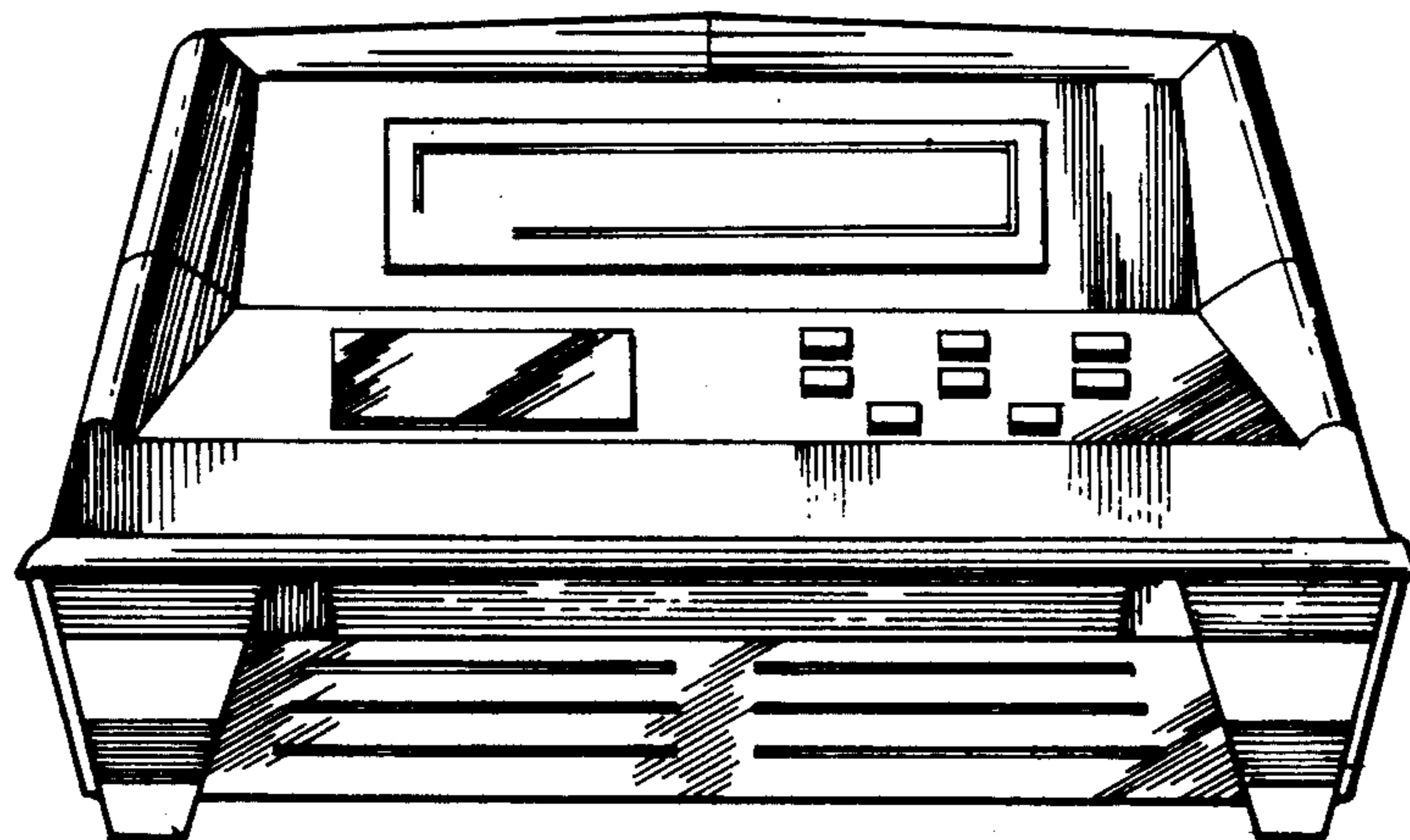


Fig. 5.

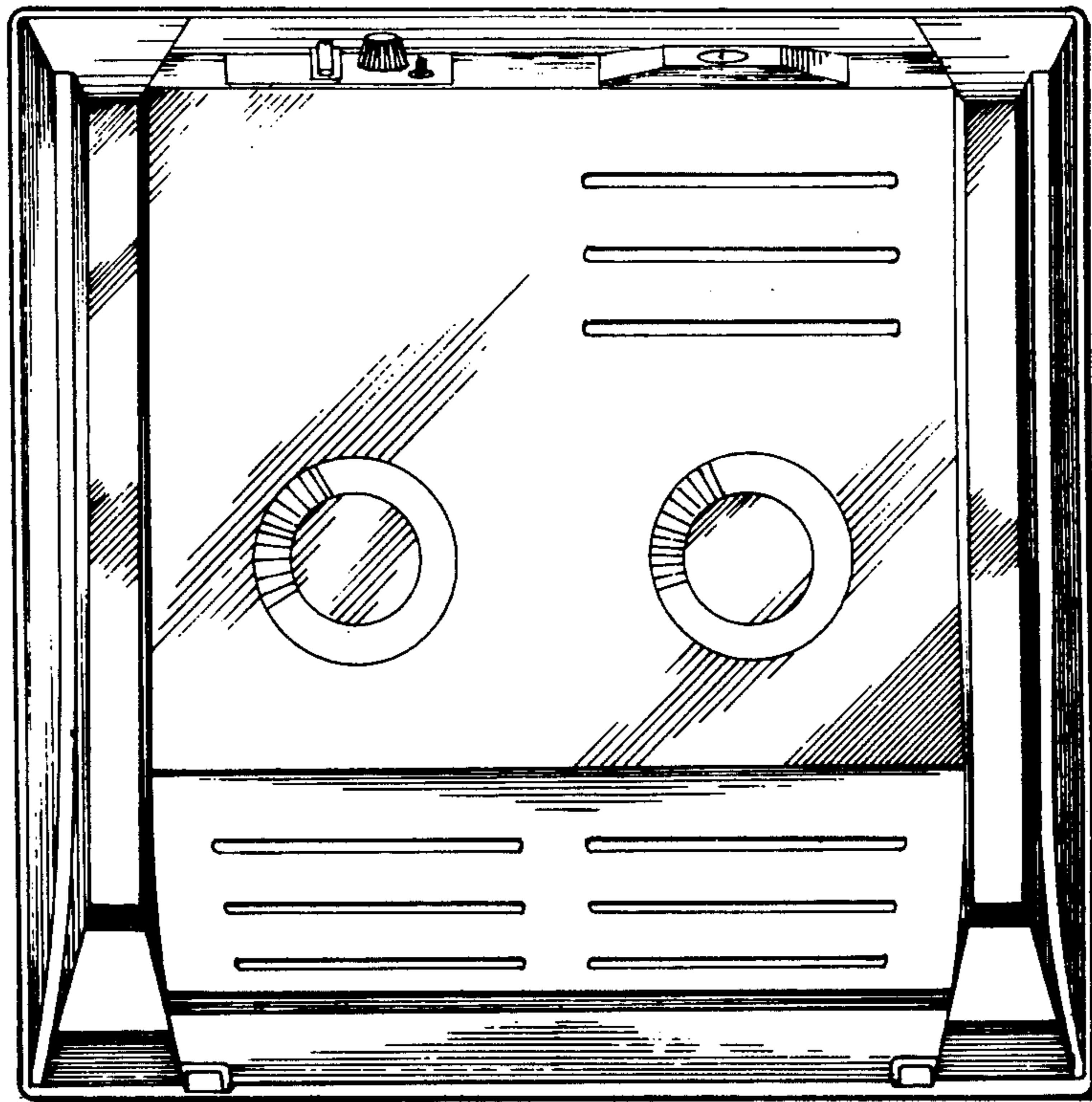


Fig. 6.

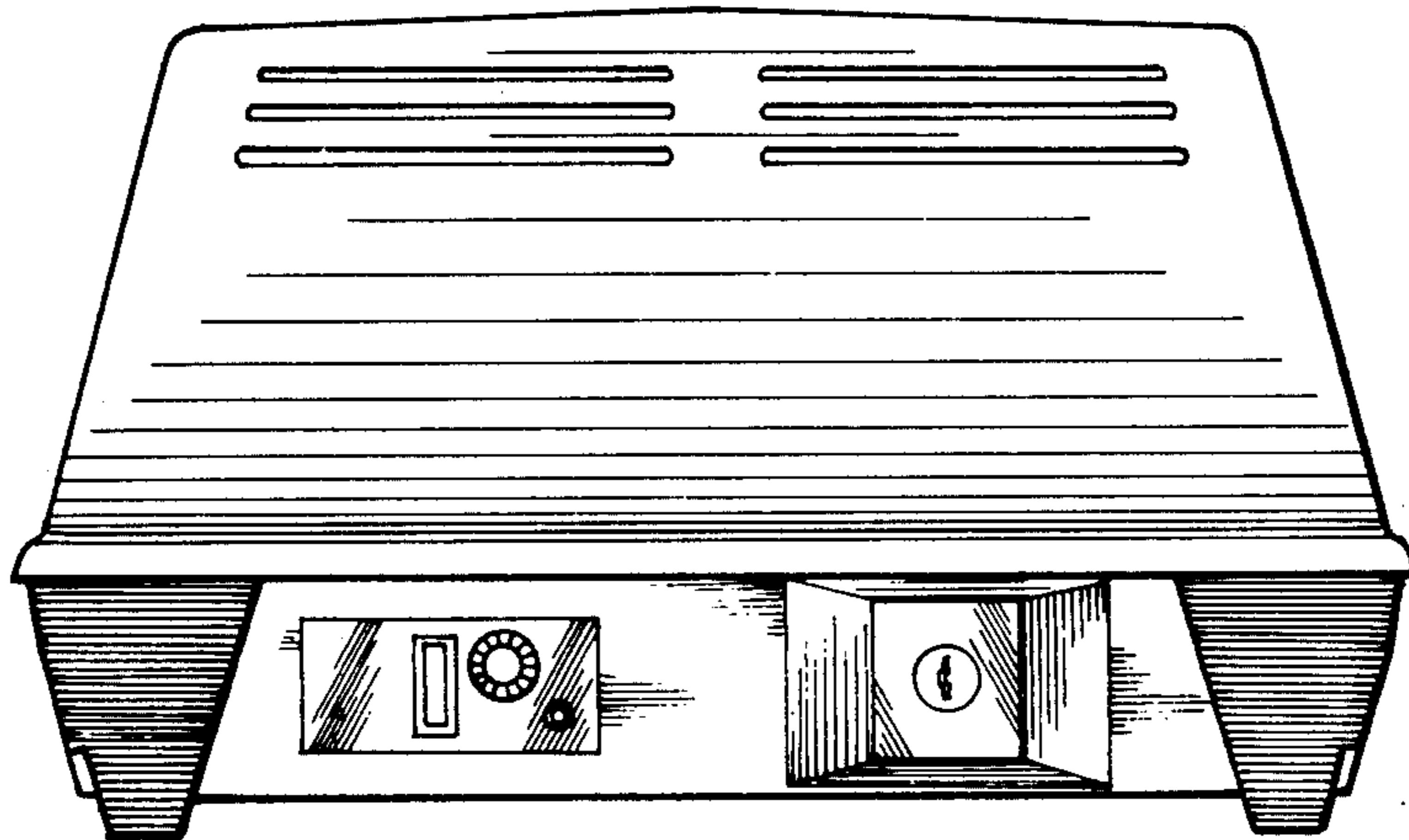


Fig. 7.