

[54] ELECTRICAL CIRCUIT GAME BOARD

[56]

References Cited

[75] Inventor: Teruo Matsumoto, Tokyo, Japan

U.S. PATENT DOCUMENTS

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3,376,041 4/1968 Anderson 273/130 AB

[73] Assignee: Epoch Company, Ltd., Tokyo, Japan

Primary Examiner—Melvin B. Feifer
Attorney, Agent, or Firm—James D. Halsey, Jr.

[**] Term: 14 Years

[57]

CLAIM

[21] Appl. No.: 677,125

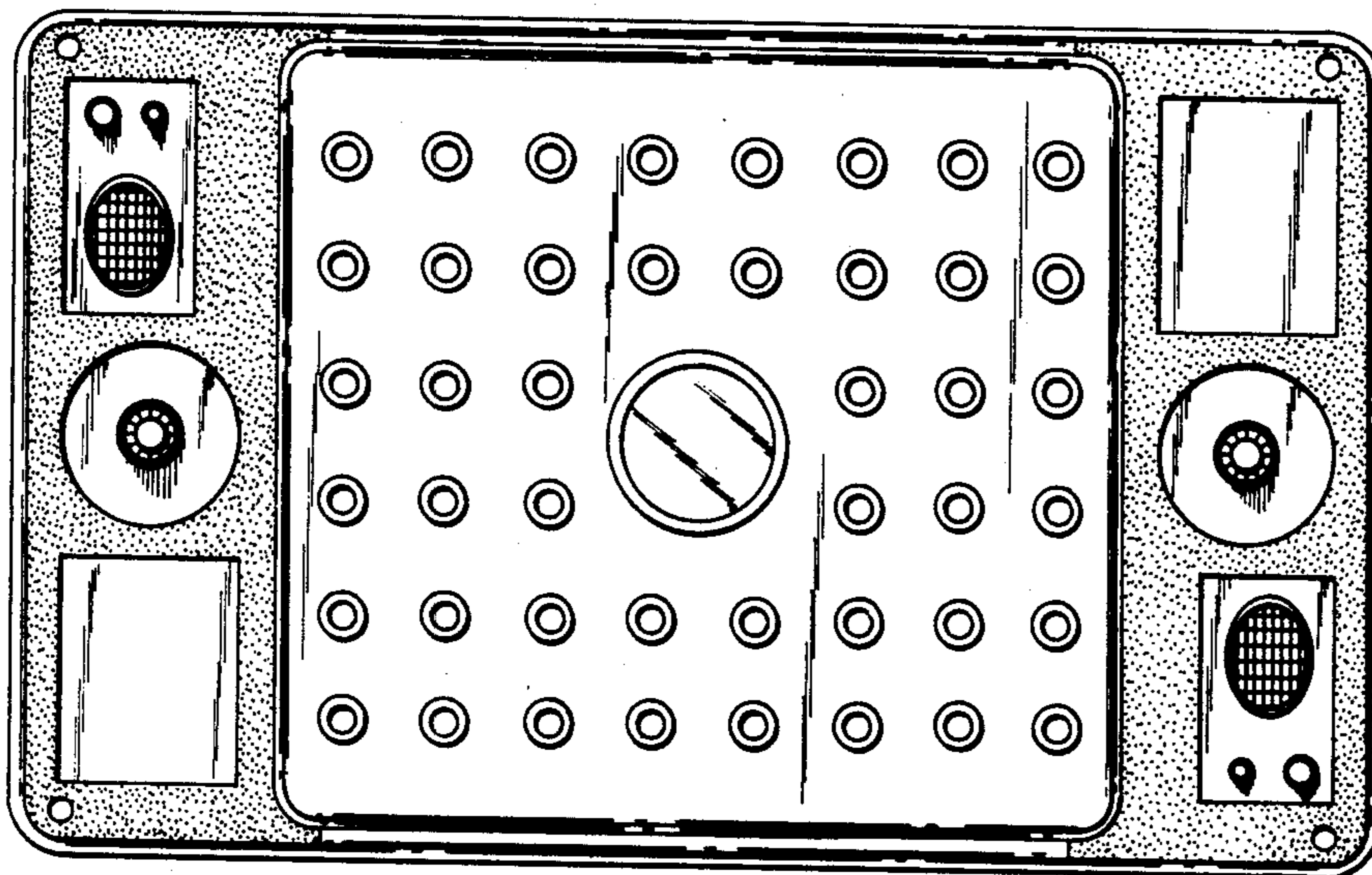
The ornamental design for an electrical circuit game board, as shown.

[22] Filed: Apr. 15, 1976

DESCRIPTION

[51] Int. Cl. D21-01
[52] U.S. Cl. D34/5 SS
[58] Field of Search D34/5 SS; 273/130 AB,
273/131 A, 134 A, 135 A, 136 A; 35/9 B, 10

FIG. 1 is a top plan view of an electrical circuit game board, embodying my new design;
FIG. 2 is a bottom plan view thereof;
FIG. 3 is a front elevational view thereof;
FIG. 4 is a rear elevational view thereof;
FIG. 5 is a right side elevational view thereof; and
FIG. 6 is a left side elevational view thereof.



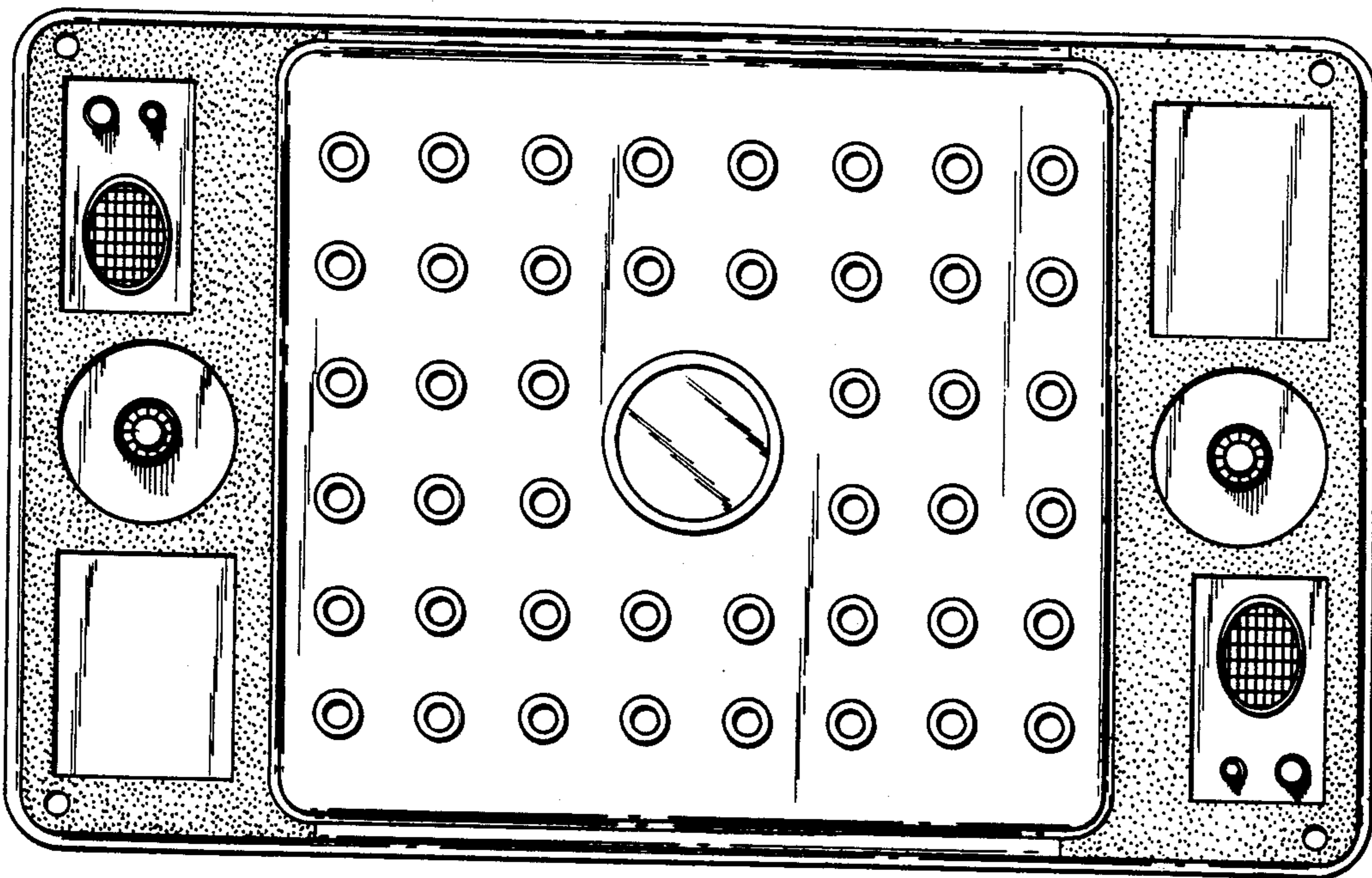


FIG. 1

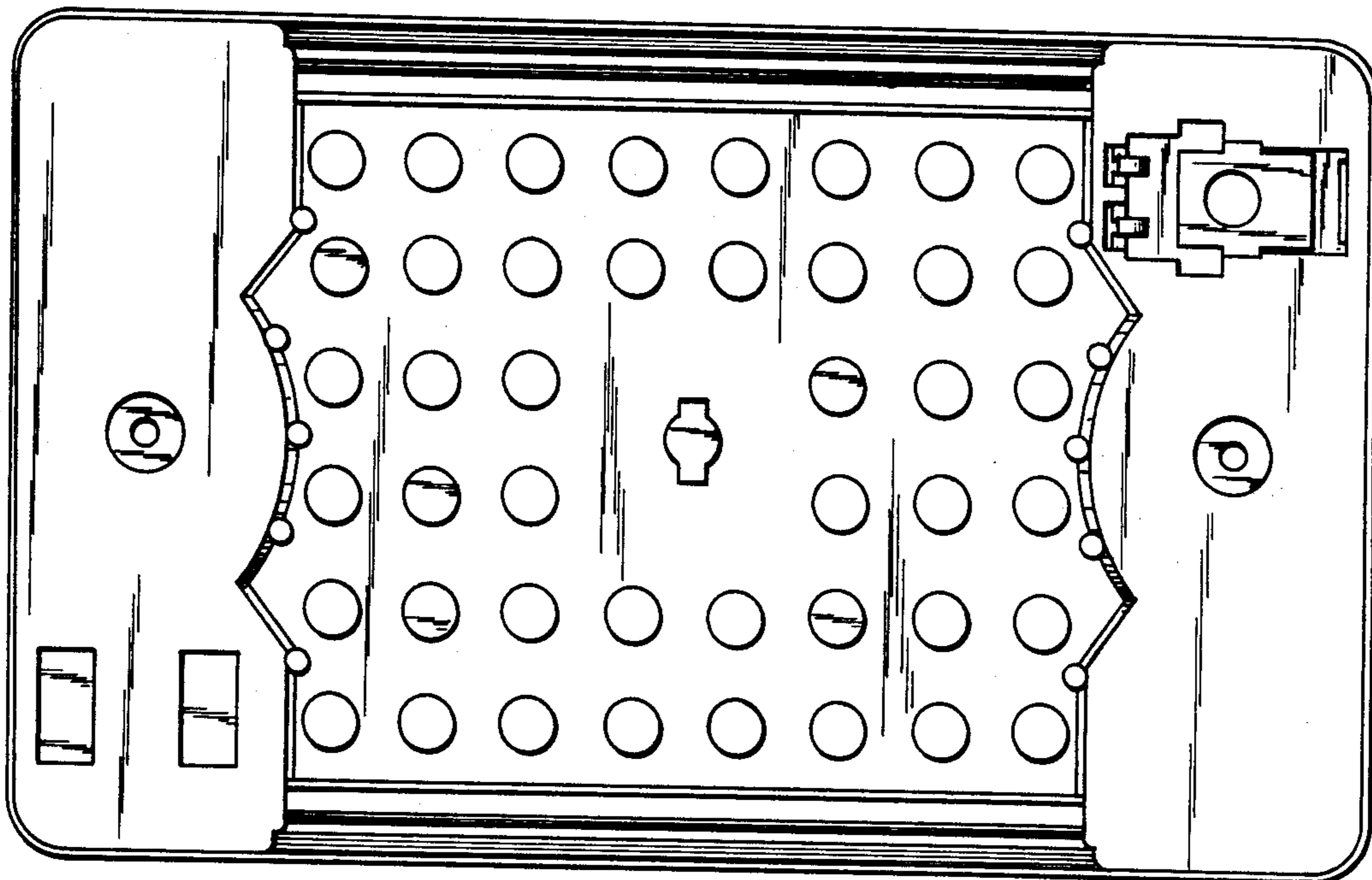


FIG. 2

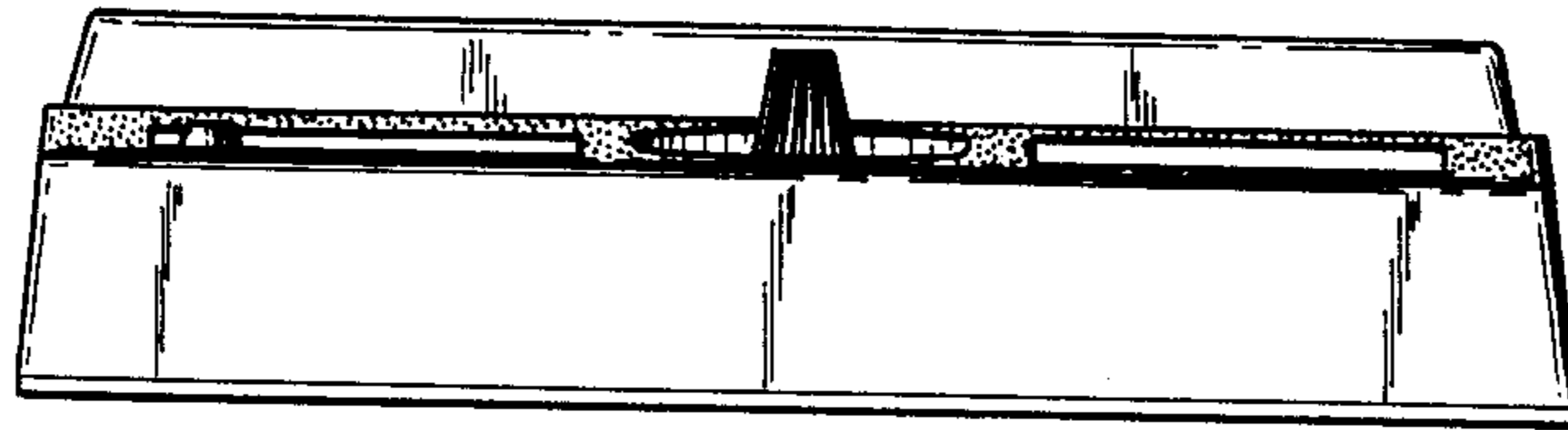


FIG. 3

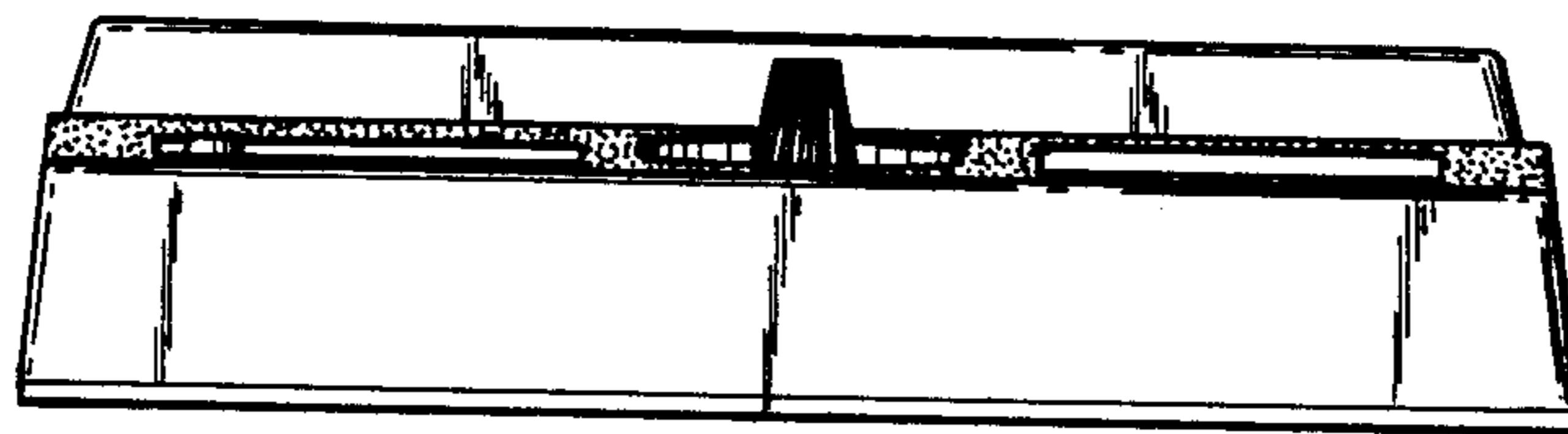


FIG. 4

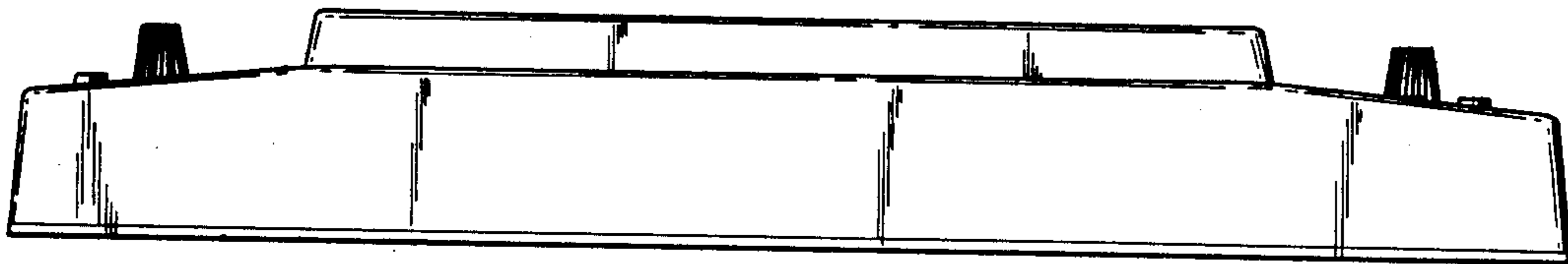


FIG. 5

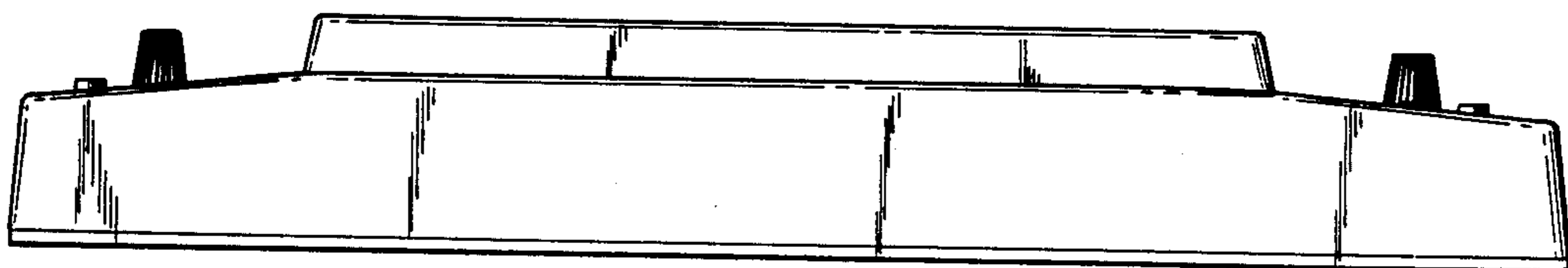


FIG. 6