## United States Patent [19]

## Fine

Des. 245,223 [11]

\*\* Aug. 2, 1977 [45]

[54]	WALLPLATE-EDGE CLUSTERS	
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[73]	Assignee:	General Electric Company
[**]	Term:	14 Years
[21]	Appl. No.:	613,825
[22]	Filed:	Sept. 16, 1975
[52]	Int. Cl	
[56] References Cited		
U.S. PATENT DOCUMENTS		
D. 21	79,512 10/192 13,879 4/196 33,650 11/197	59 Algoren D8/184
Primary Examiner-William N. Wertman		

## [57] **CLAIM**

Attorney, Agent, or Firm-Paul E. Rochford

The ornamental design for a wallplate-edge clusters, as shown and described.

## DESCRIPTION

FIG. 1 is a perspective view of a wall plate for a switch illustrating my new design.

FIG. 2 is a front elevational view of the wall plate of FIG. 1.

FIG. 3 is a rear elevational view of the wall plate of

FIG. 1. FIG. 4 is a side elevational view of the wall plate of

**FIG. 1.** FIG. 5 is a vertical sectional view of the wall plate as in

FIG. 2 taken along the line 5—5 of FIG. 2.

FIG. 6 is an end plan view of the wall plate of FIG. 2.

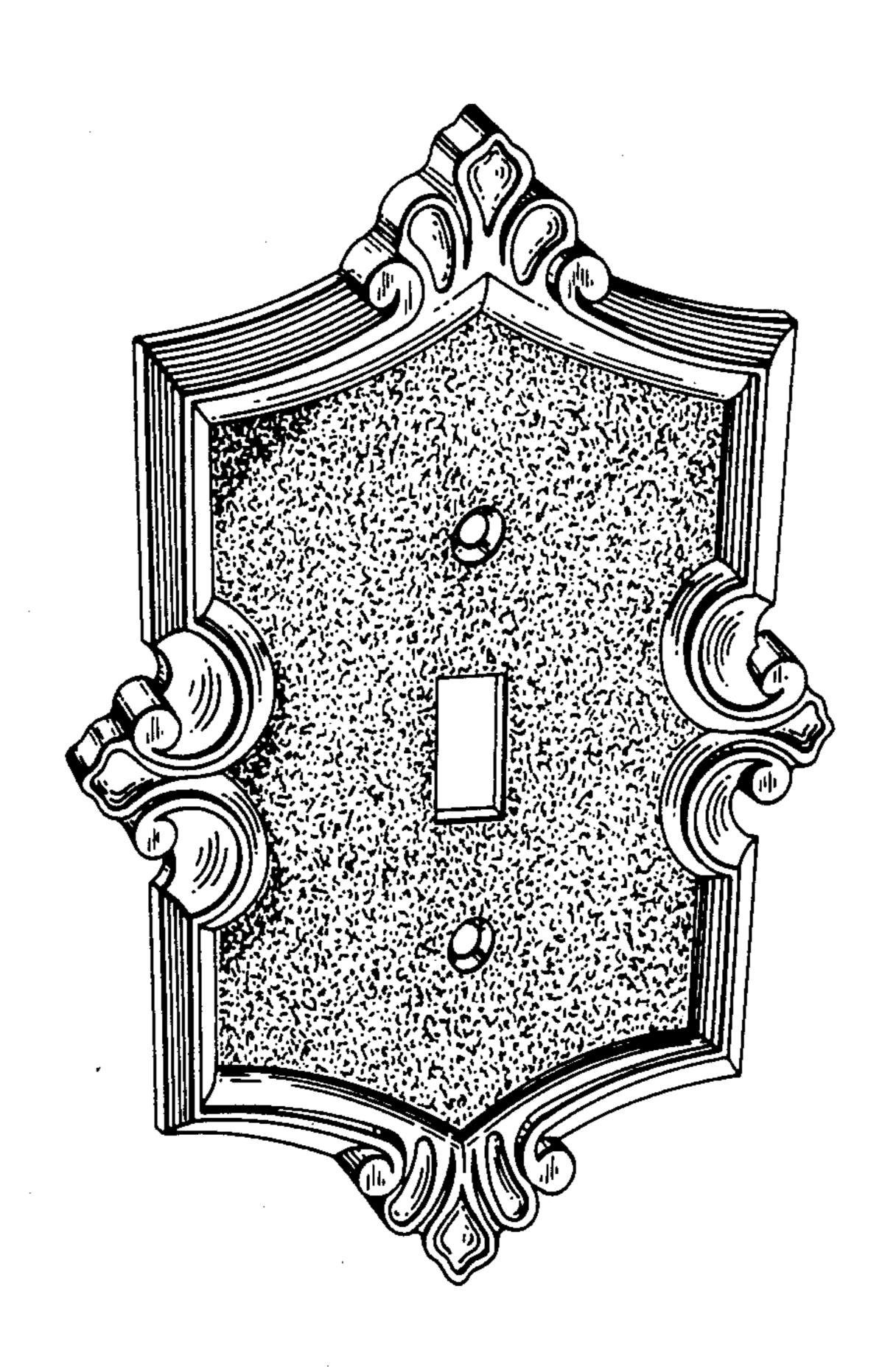
FIG. 7 is a detailed sectional view of the wall plate of

FIG. 3 taken along the line 7—7 of FIG. 3.

FIG. 8 is a vertical elevational view of a wall plate for two side-by-side switches illustrating my new design, the surface decoration being shown only in patches for ease of illustration.

FIG. 9 is a vertical elevational view of a wall plate for a wall receptacle illustrating my new design, the surface decoration being shown in patches as in FIG. 8.

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Aug. 2, 1977

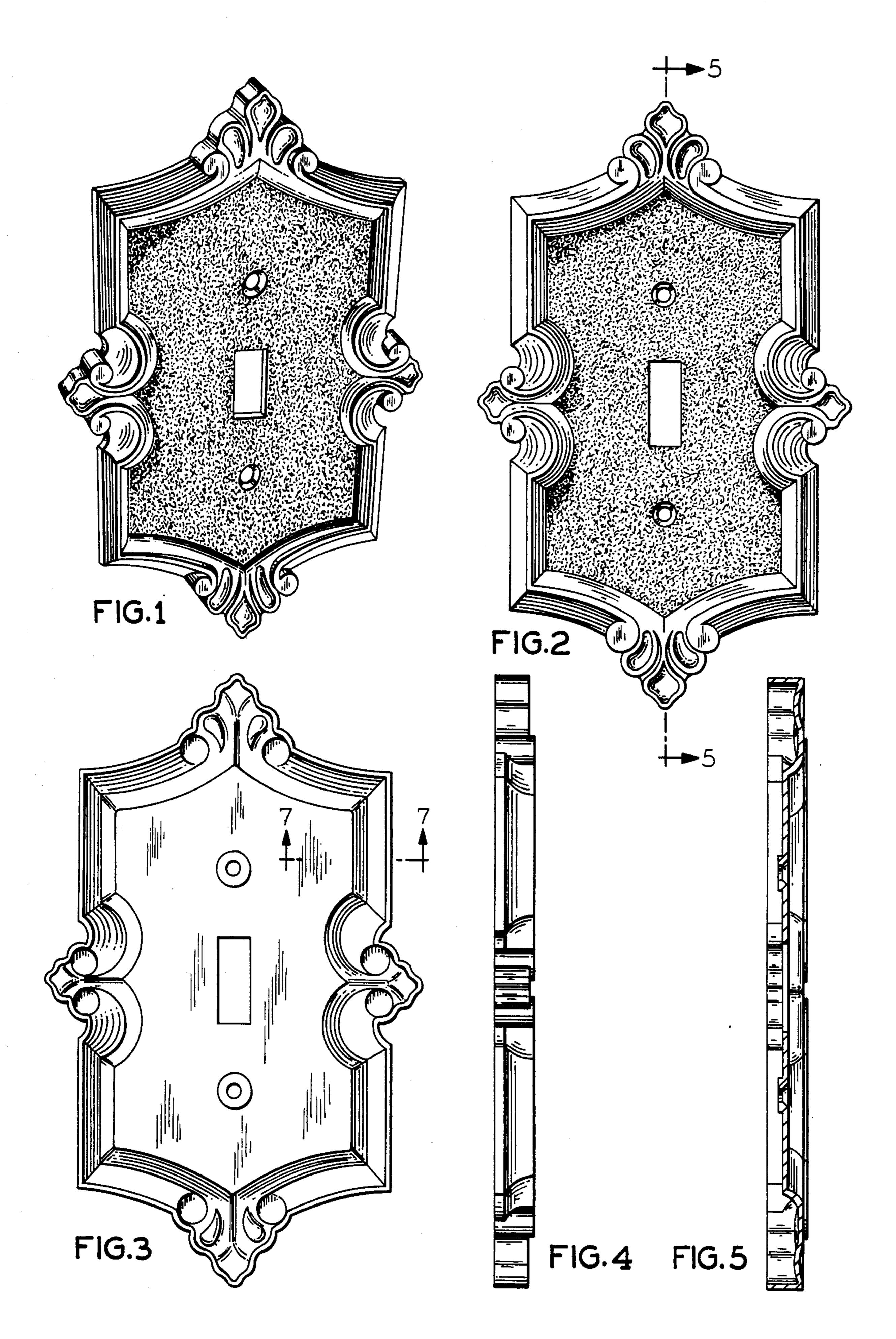




FIG.6



F1G.7

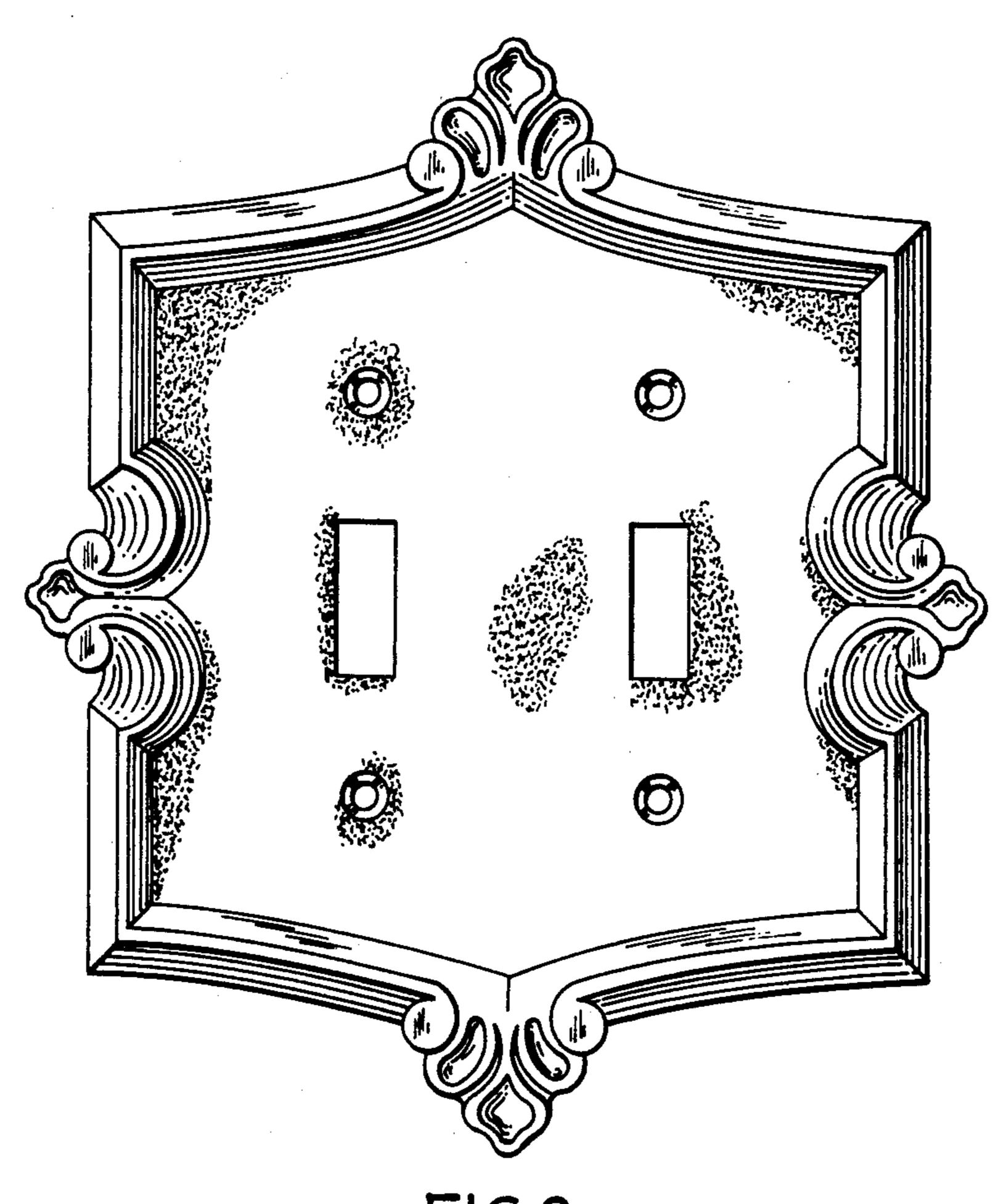


FIG.8

