United States Patent [19]

Jinbo

[11] Des. 243,958

[45] ** Apr. 5, 1977

[54]	ELECTRON	IC CALCULATING MACHINE
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[73]	Assignee: S	harp Kabushiki Kaisha, Japan
[**]	Term: 1	4 Years
[21]	Appl. No.: 5	96,416
[22]	Filed: J	uly 16, 1975
[30]	Foreign Application Priority Data	
Jan. 18, 1975 Japan 50-2673		
[51]		D18—01
[52]	U.S. Cl	D64/11 B
[58] Field of Search D26/5 R, 5 C; D64/11 R,		
D64/11 A, 11 B, 11 C; 235/145 R		
[56]		References Cited
U.S. PATENT DOCUMENTS		
D. 222	,542 11/1971	Matsumoto D64/11 B X
D. 226	,485 3/1973	Kitai D26/5 C
D. 228	,291 9/1973	Maeda et al
D. 235	,349 6/1975	Kurozumi
	,195 10/1975	Kitai D26/5 C

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[57]

CLAIM

The ornamental design for an electronic calculating machine, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of an electronic calculating machine showing my new design;

FIG. 2 is a right side elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a front elevational view thereof;

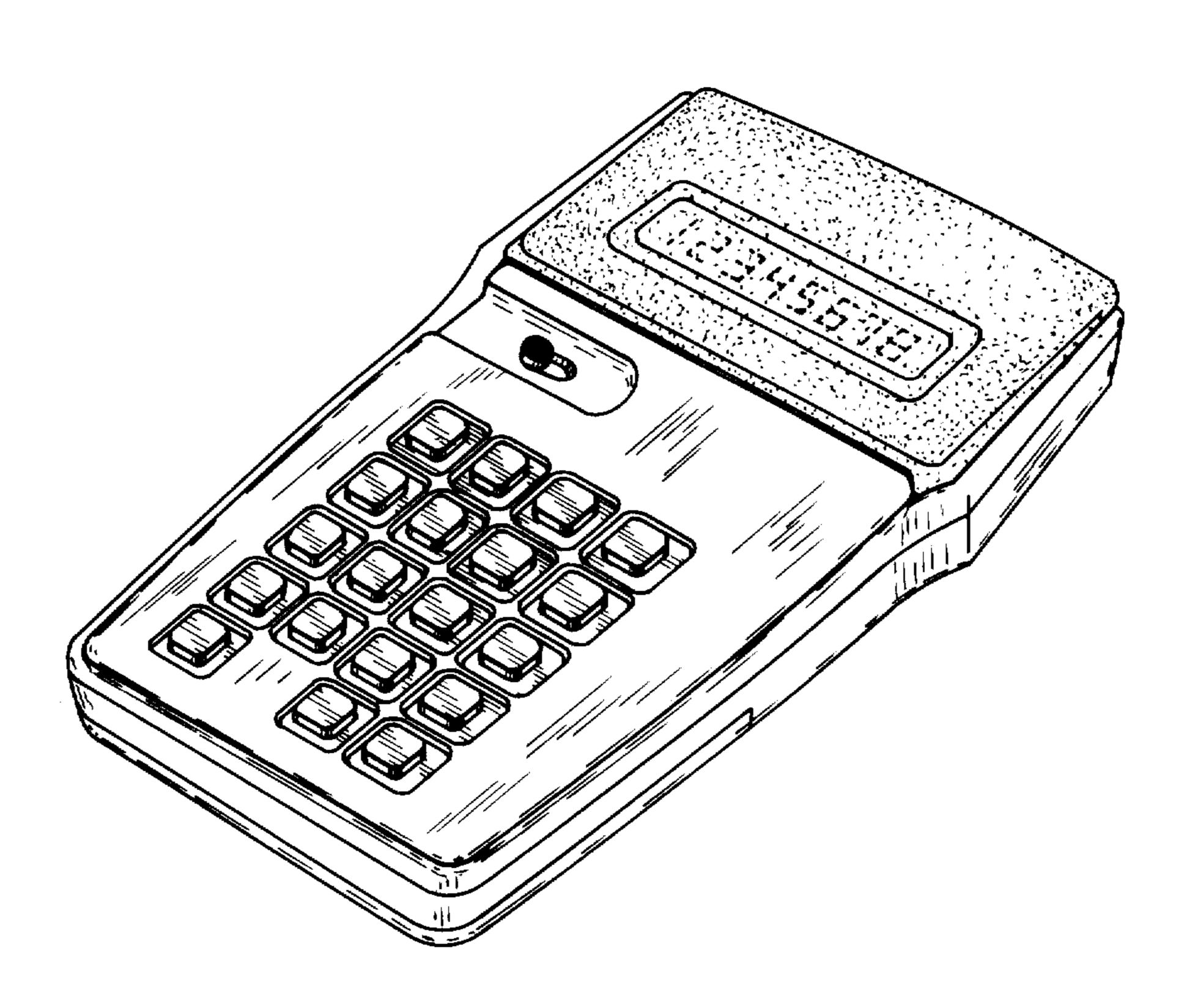
FIG. 5 is a sectional elevational view thereof; taken along the line 5—5 of FIG. 4

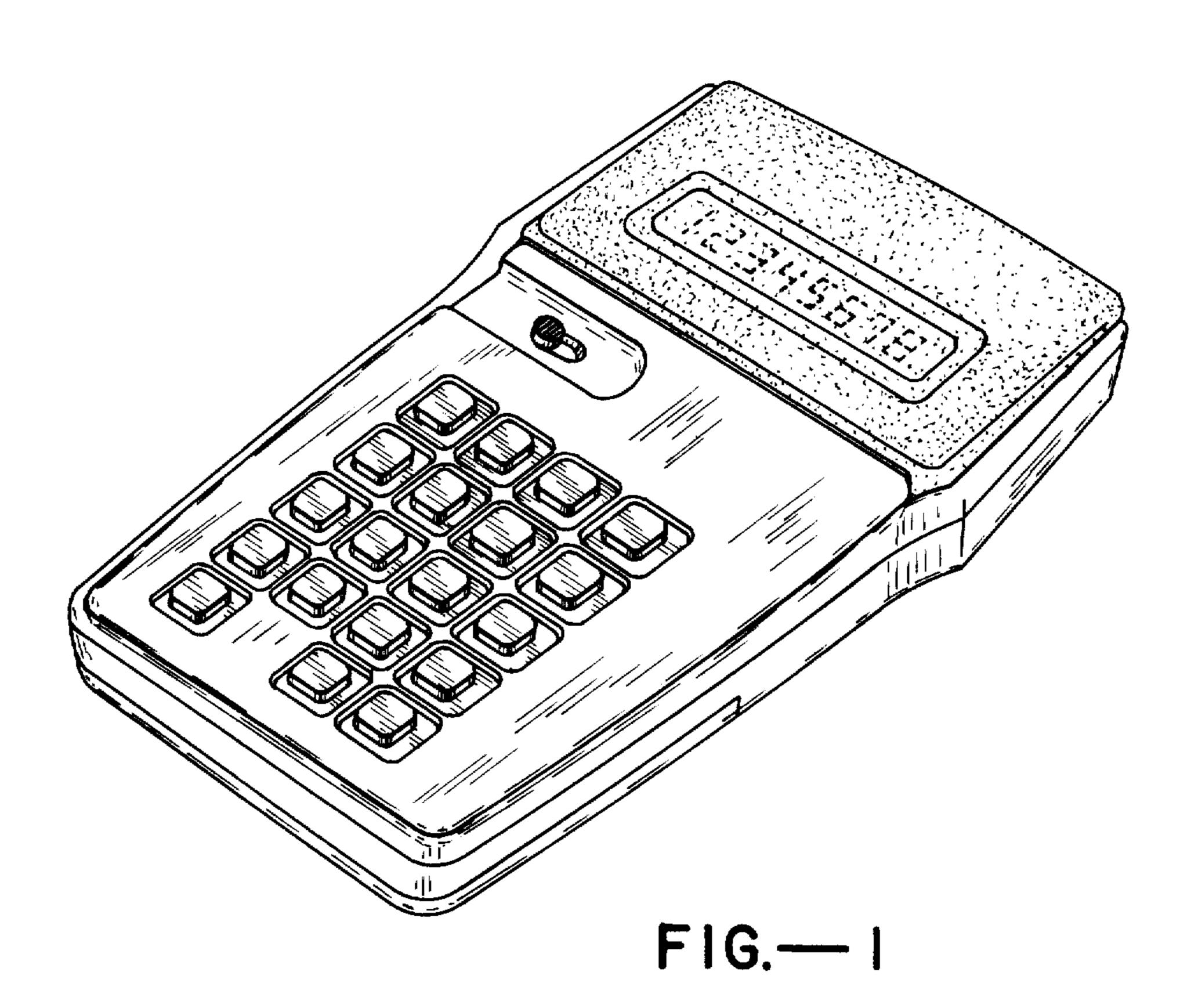
FIG. 6 is a side elevational view thereof;

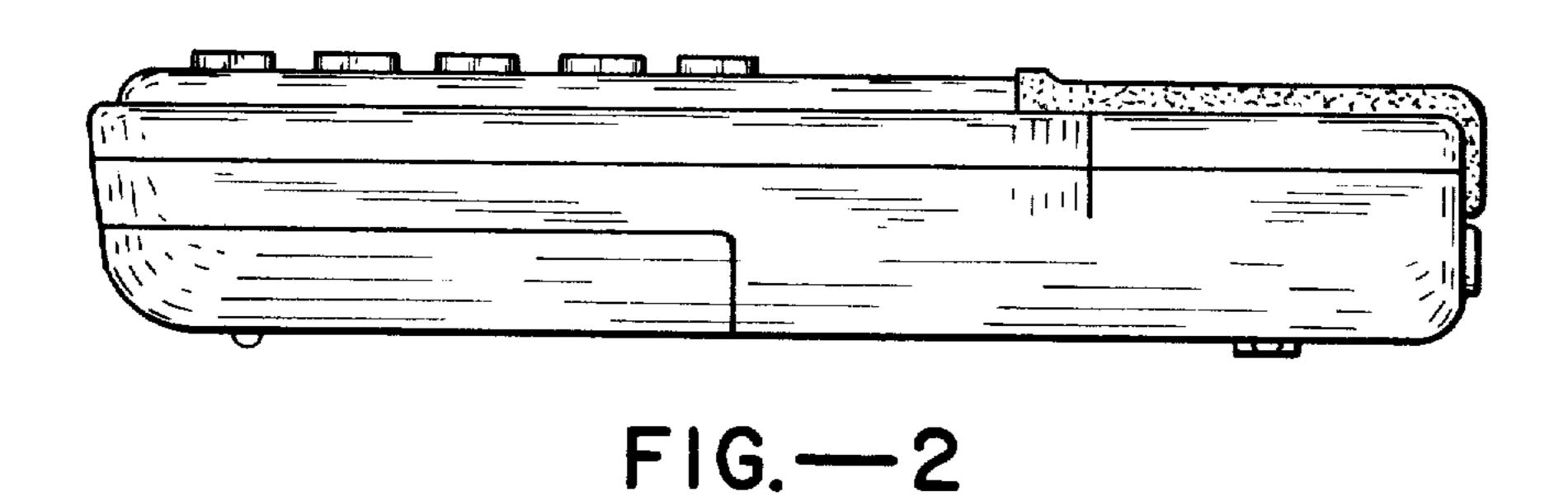
FIG. 7 is a bottom view thereof; and

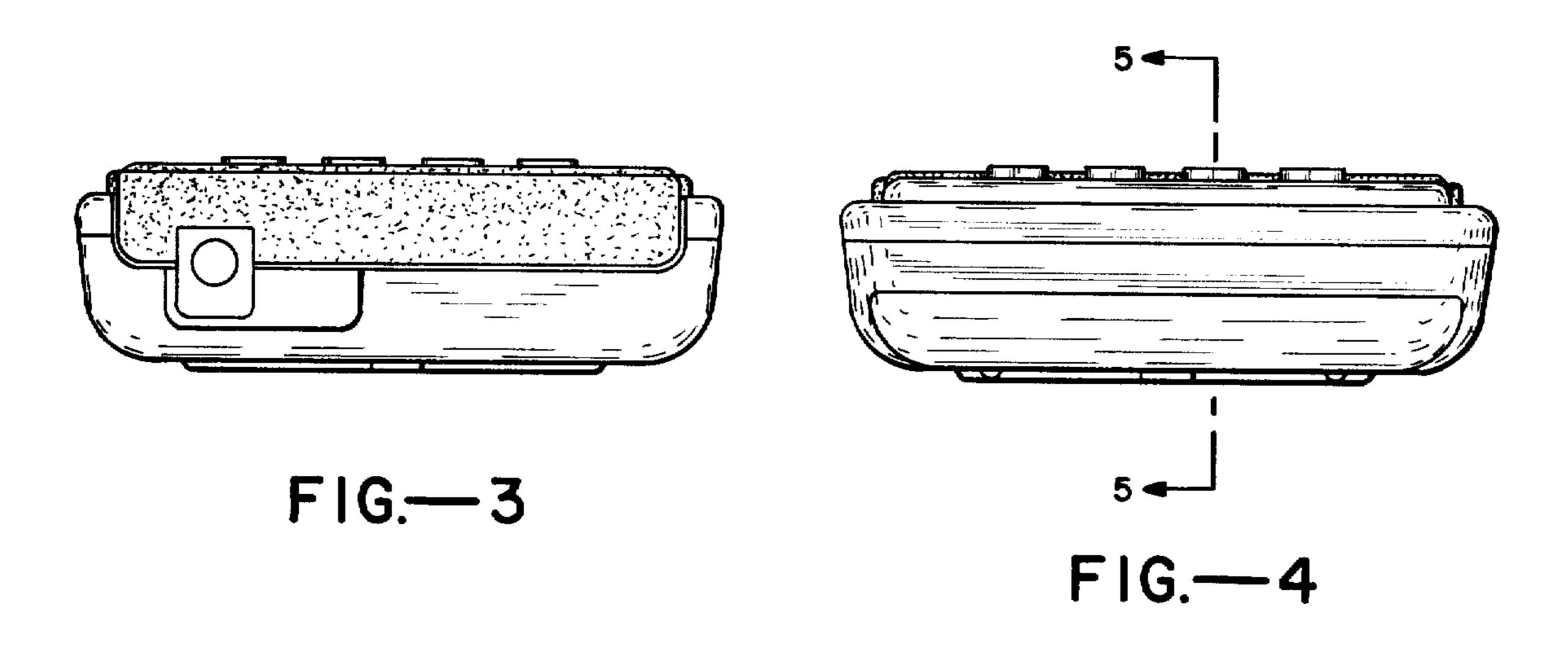
FIG. 8 is a plan view thereof shown in a non-operative state.

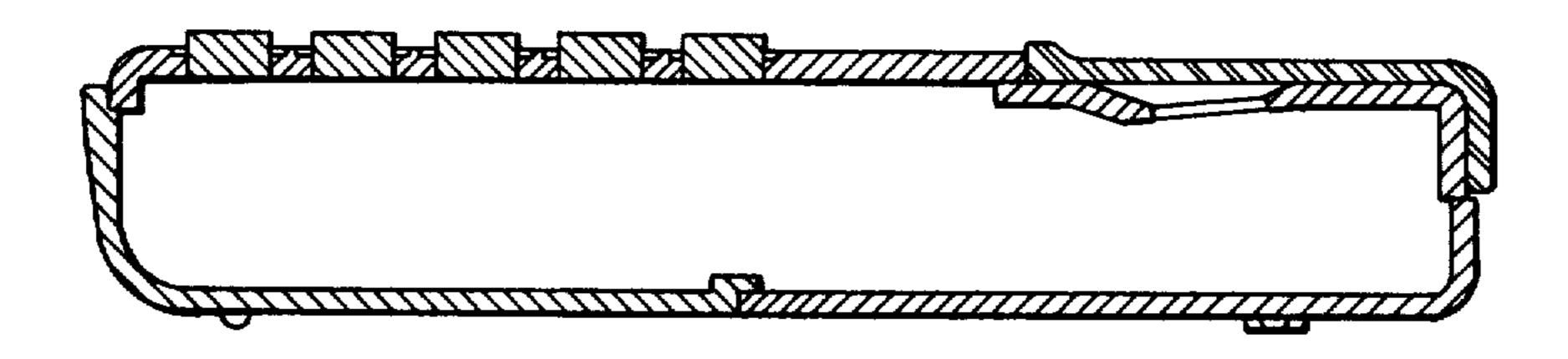
The broken line disclosure of the display numerals in FIG. 1 is for illustrative purposes only and forms no part of the claimed design.



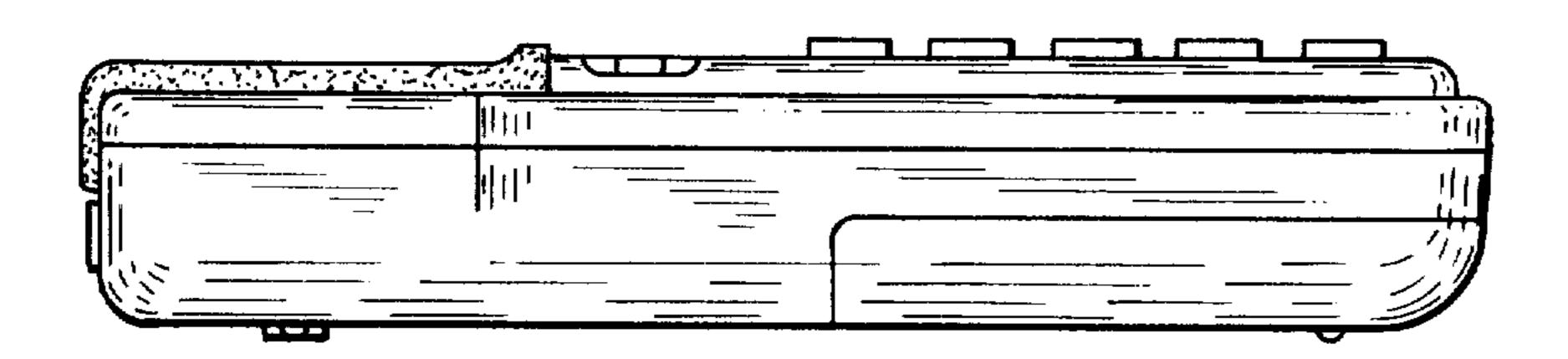








F1G.—5



F1G.—6

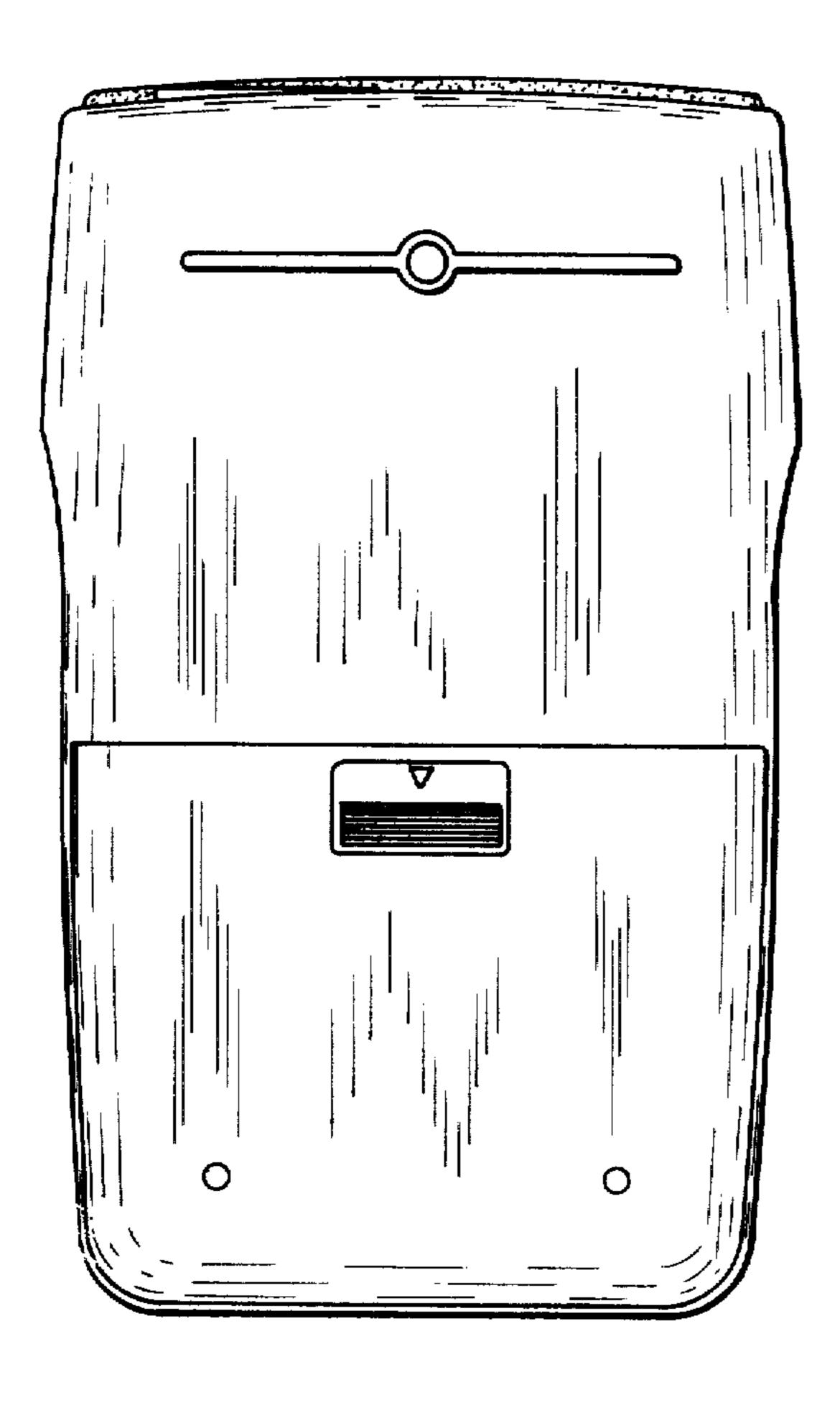
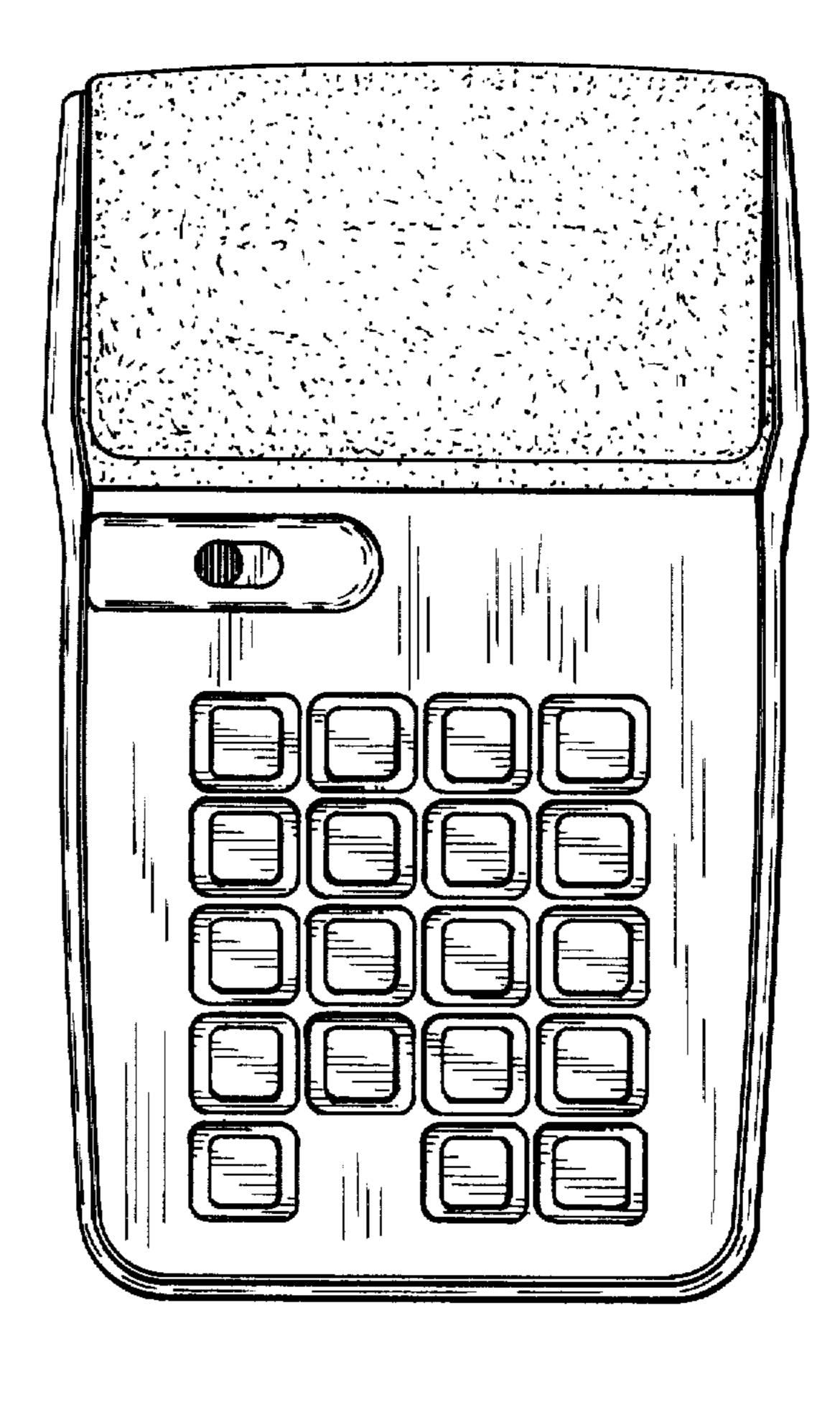


FIG.— 7



F1G.—8