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(12) **United States Design Patent**
Nakano et al.

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(54) **CIRCUIT BOARD FOR COMPUTER PRINTERS**

H05K 1/00; H05K 7/1417; H05K 7/02;
H05K 7/12; H05K 7/06; H01L 2924/14
See application file for complete search history.

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(56) **References Cited**

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Suwa (JP); **Yasuhiko Kosugi,** Suwa
(JP)

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(73) Assignee: **SEIKO EPSON CORPORATION,**
Tokyo (JP)

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(**) Term: **15 Years**

(Continued)

(21) Appl. No.: **35/518,465**

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(80) **Hague Agreement Data**

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(Continued)

(30) **Foreign Application Priority Data**

Primary Examiner — Elizabeth J Oswecki

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(51) **LOC (14) Cl.** **13-03**

(57) **CLAIM**

(52) **U.S. Cl.**
USPC **D13/182**

The ornamental design for a circuit board for computer printers as shown and described.

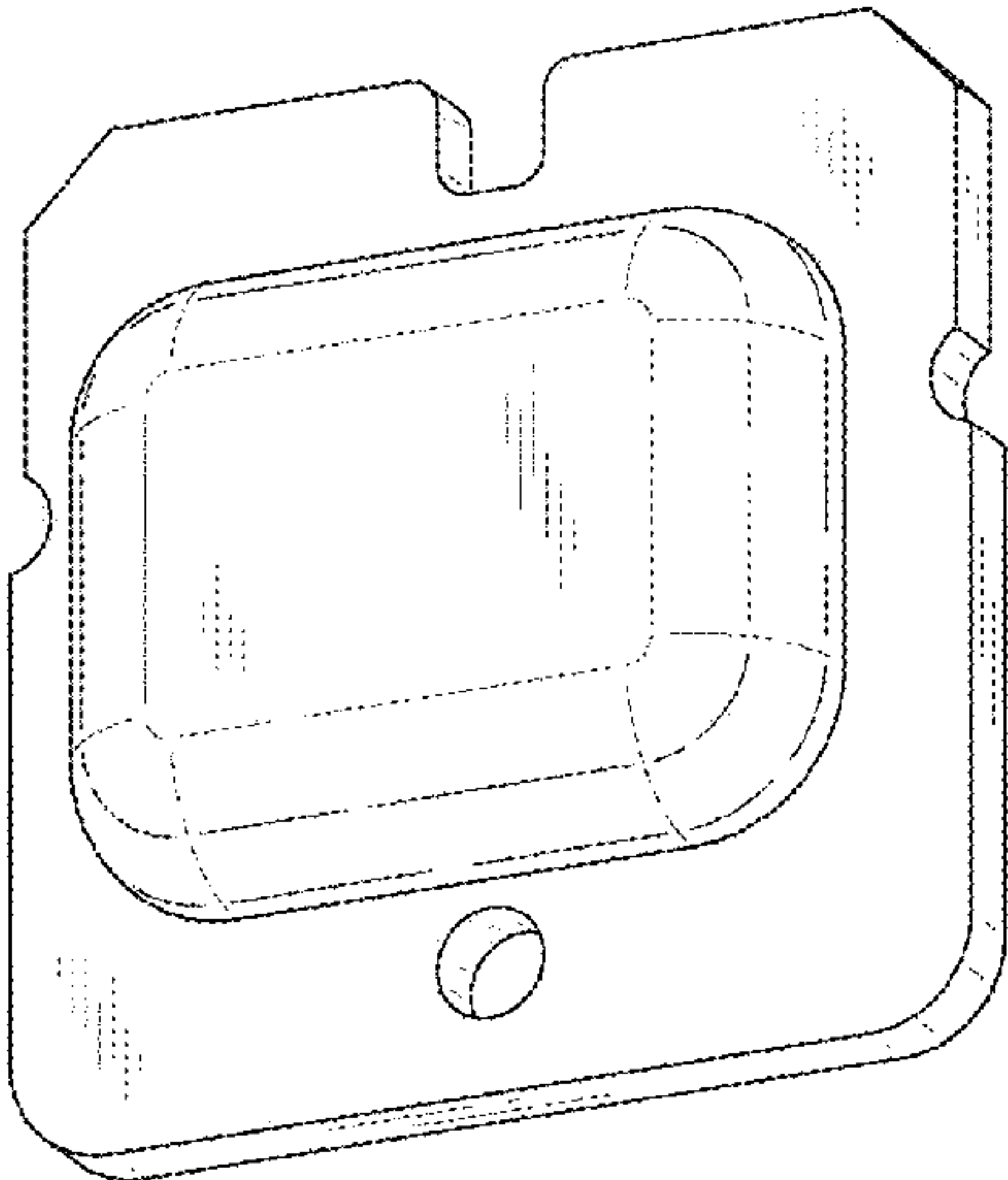
(58) **Field of Classification Search**

DESCRIPTION

USPC D13/182; 361/748, 752, 760, 761, 807,
361/808, 736, 719, 720; 174/250, 253,
174/255, 68.1; 363/144, 147; D14/164,
D14/263, 435, 481; 112/453, 457;
318/568.1, 567; 439/540.1, 945, 946,
439/76.1
CPC H05K 3/30; H05K 3/301; H05K 3/284;
H05K 3/303; H05K 1/18; H05K 1/181;
H05K 1/182; H05K 1/183; H05K 1/185;

1. Circuit board for computer printers
- 1.1 : Perspective
- 1.2 : Perspective
- 1.3 : Front
- 1.4 : Back

(Continued)



1.5 : Top
1.6 : Bottom
1.7 : Right
1.8 : Left

1 Claim, 8 Drawing Sheets

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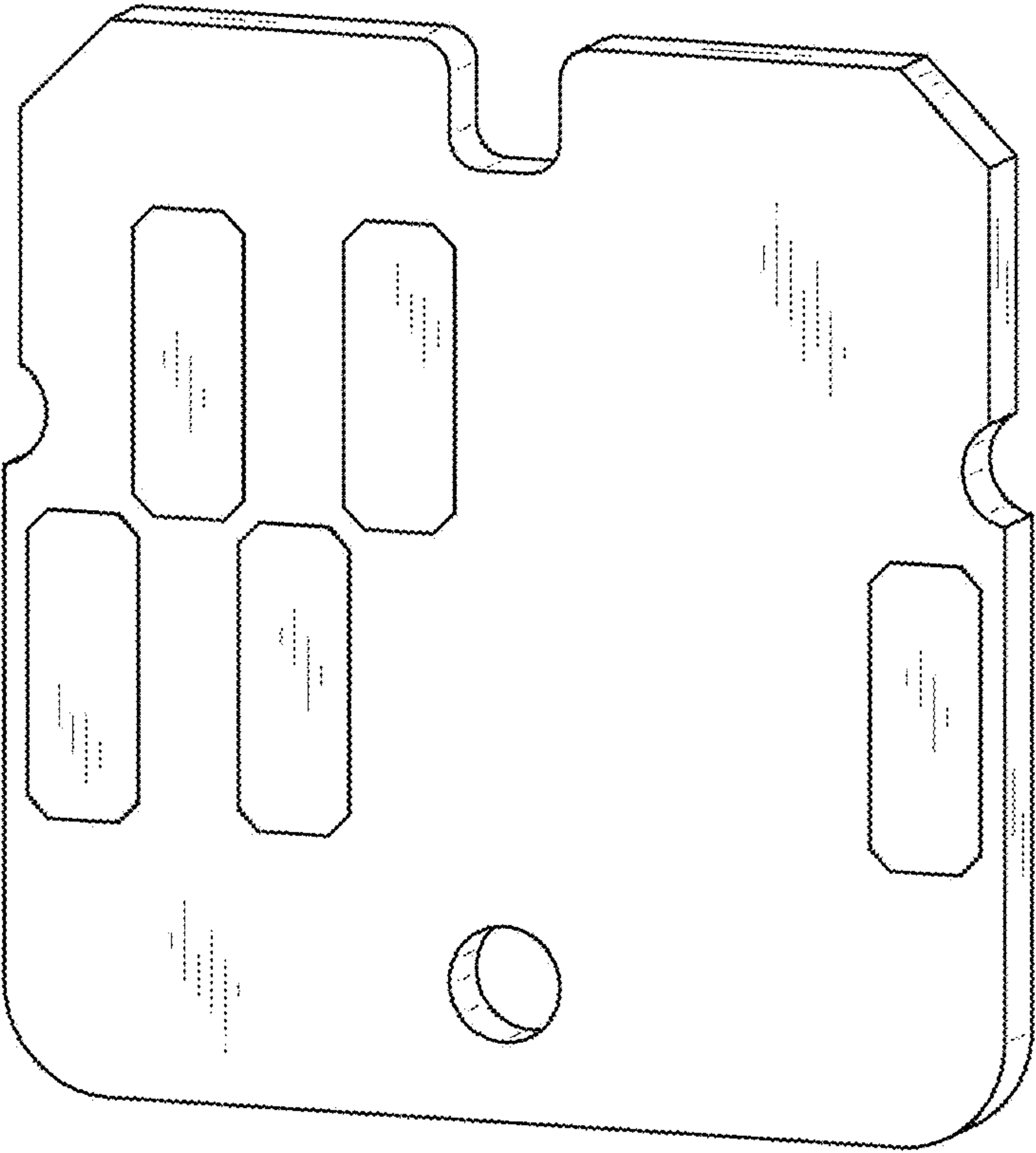
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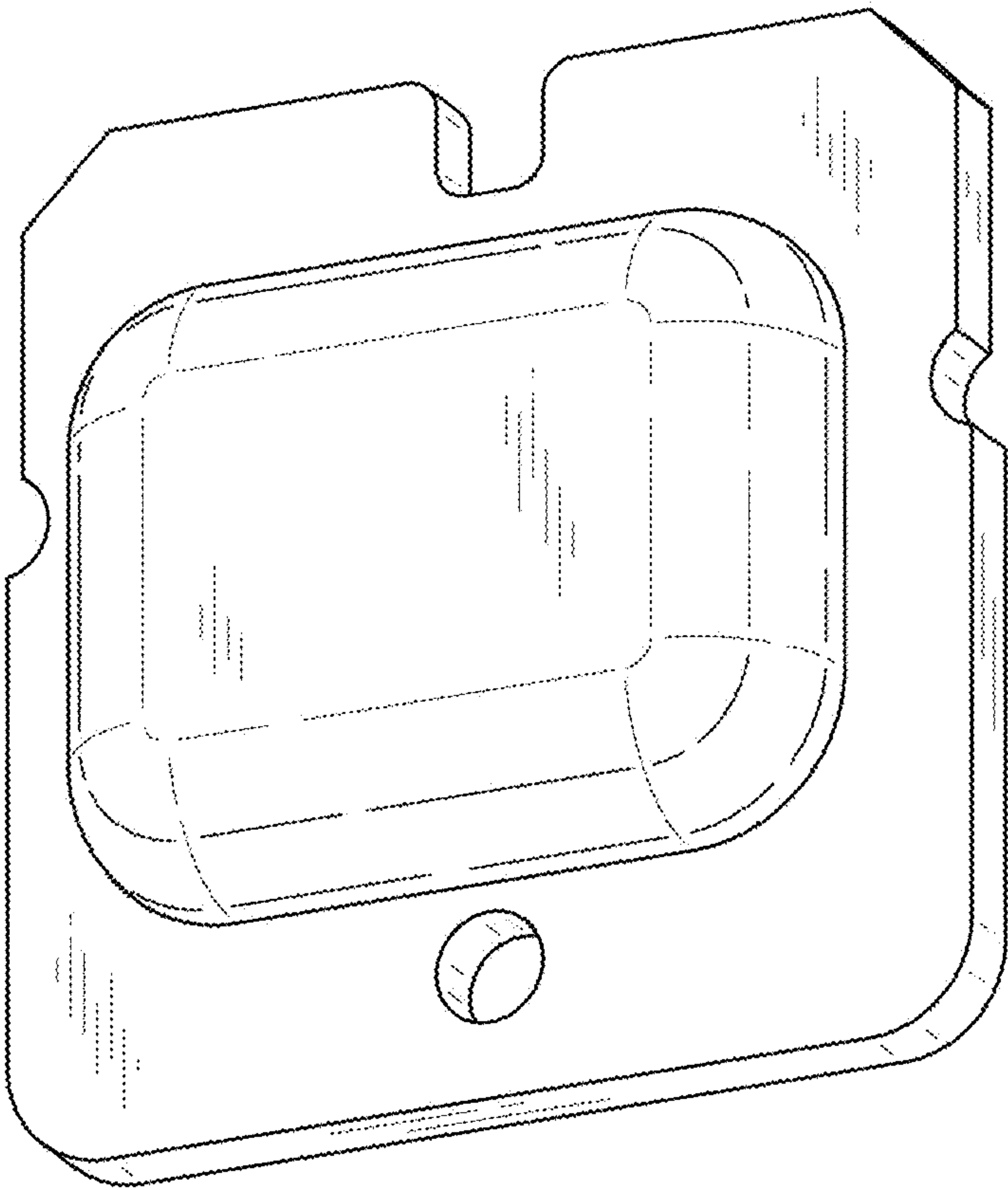
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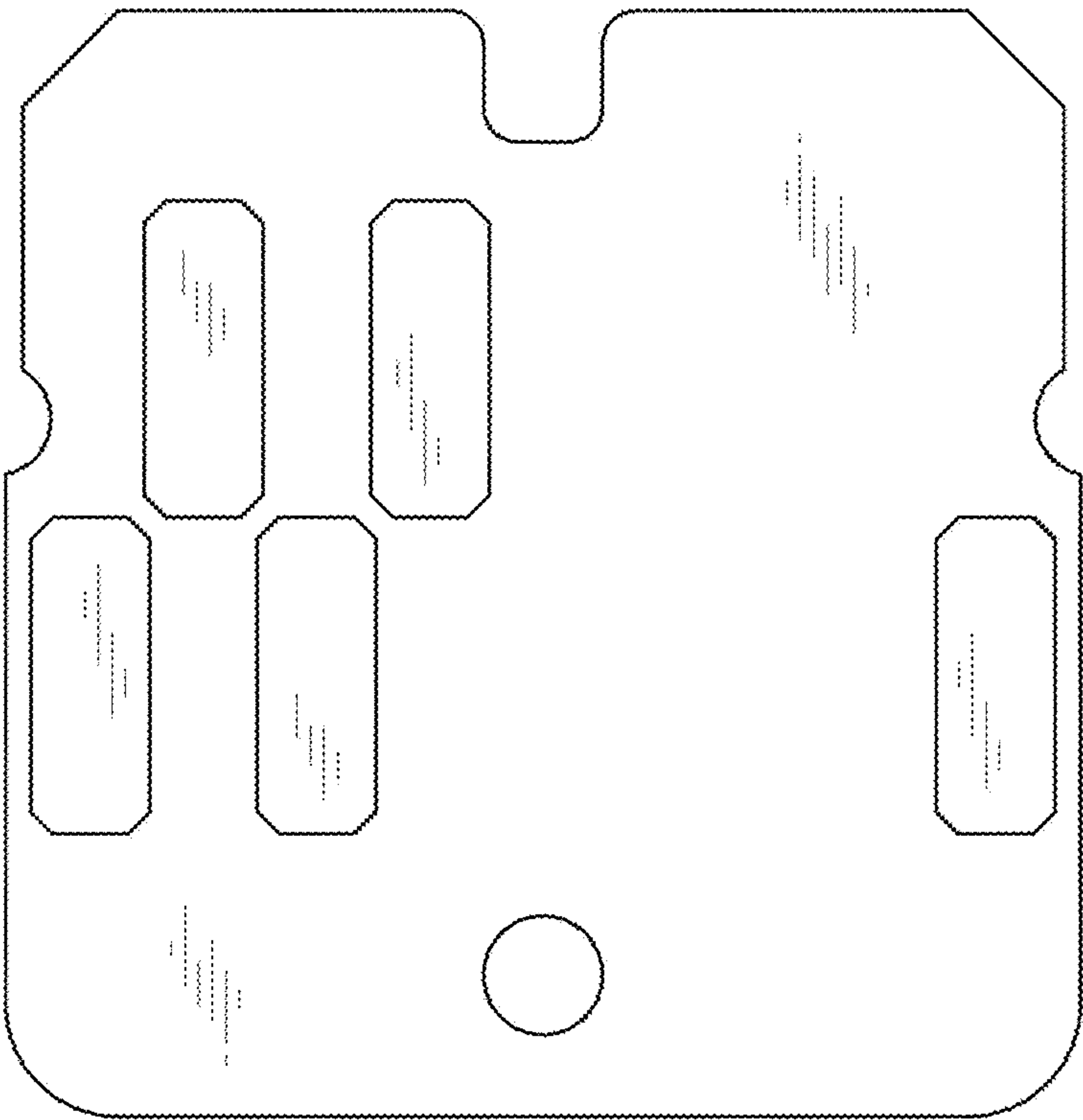
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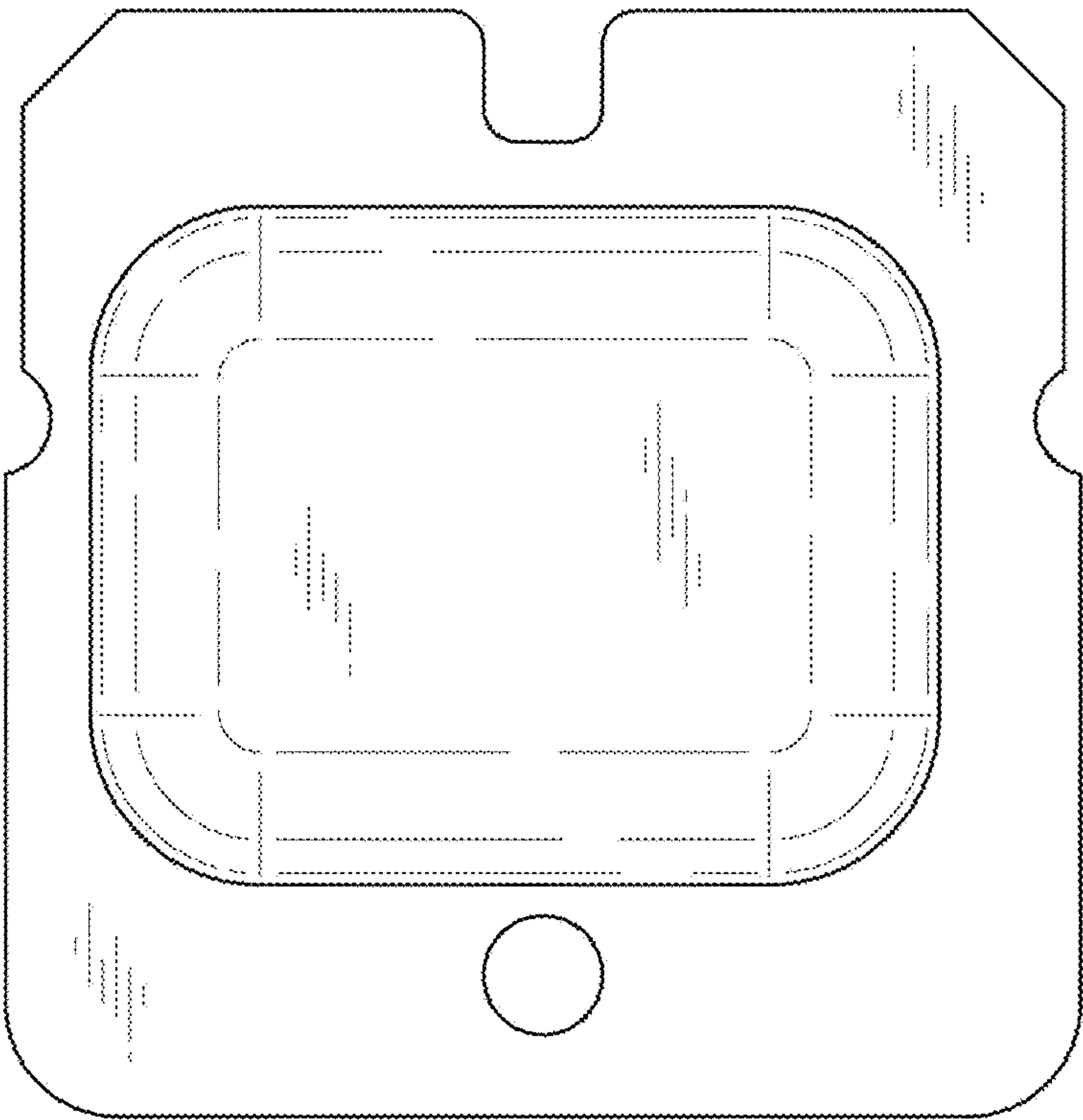
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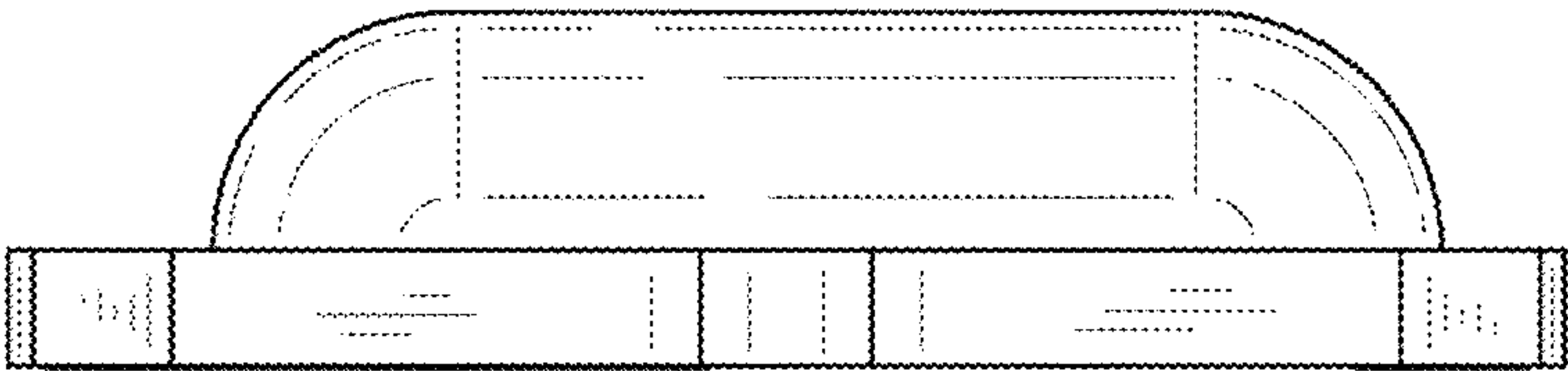
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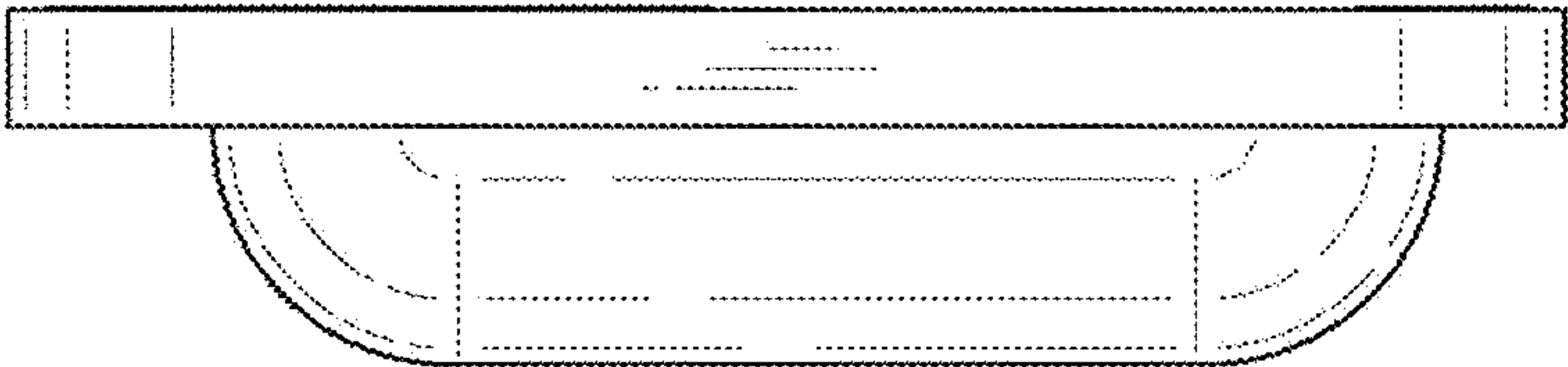
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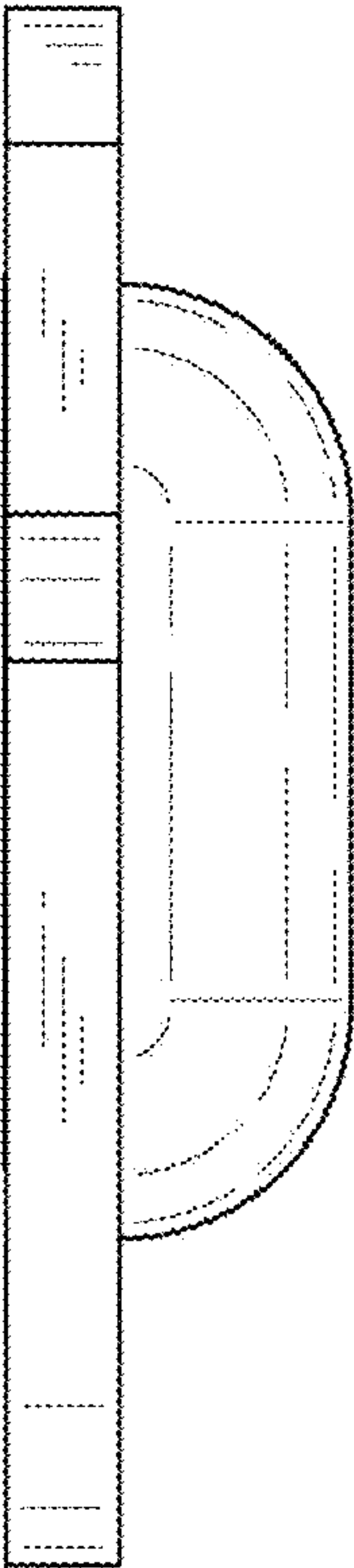
1.5



1.6



1.7



1.8

