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(12) **United States Design Patent** (10) **Patent No.:** **US D1,026,928 S**
Wengreen (45) **Date of Patent:** **** May 14, 2024**

(54) **MOUNT FOR ELECTRONIC DEVICES**

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(**) Term: **15 Years**

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(51) **LOC (14) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/447**

(58) **Field of Classification Search**
USPC D14/447, 432, 434, 439, 440, 451, 452, D14/457, 239, 251, 253, 341; D8/363, D8/373, 380; D6/406.3, 406.4, 406.5, D6/406.6, 700, 313; D12/415; D19/91, D19/86, 90; D3/218
CPC A47B 21/04; A47B 2097/006; A47B 2097/005; A47B 2023/049; A45C 2011/002; A45C 2011/003; F16M 2200/00; F16M 13/00; A45F 2200/0516; A45F 2200/0525
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D306,299 S * 2/1990 Soren D14/251
D533,175 S * 12/2006 Richter D14/447
D586,795 S * 2/2009 Richter D14/434
D710,340 S 8/2014 Wengreen
D710,363 S 8/2014 Wengreen
8,848,113 B2 9/2014 Wengreen
8,896,768 B1 11/2014 Wengreen
D722,043 S * 2/2015 Requa D14/251
8,988,616 B2 3/2015 Wengreen

9,131,195 B2 9/2015 Wengreen
D749,597 S * 2/2016 Wengreen D14/447
D751,563 S * 3/2016 Wengreen D14/447
D763,835 S 8/2016 Wengreen
D765,063 S 8/2016 Wengreen
D765,064 S 8/2016 Wengreen
D777,167 S * 1/2017 Wengreen D14/447
D798,305 S 9/2017 Wengreen
D798,306 S 9/2017 Wengreen
D812,064 S * 3/2018 Wengreen D14/238.1
D813,241 S * 3/2018 Wengreen D14/496
D824,399 S * 7/2018 Wengreen D14/447
D829,723 S * 10/2018 Kanter D14/447
D832,274 S 10/2018 Wengreen
D832,858 S * 11/2018 Wengreen D14/447
D834,029 S 11/2018 Wengreen
D845,310 S * 4/2019 Wengreen D14/447
D845,959 S * 4/2019 Wengreen D14/447

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Primary Examiner — Angela J Lee

(57) **CLAIM**

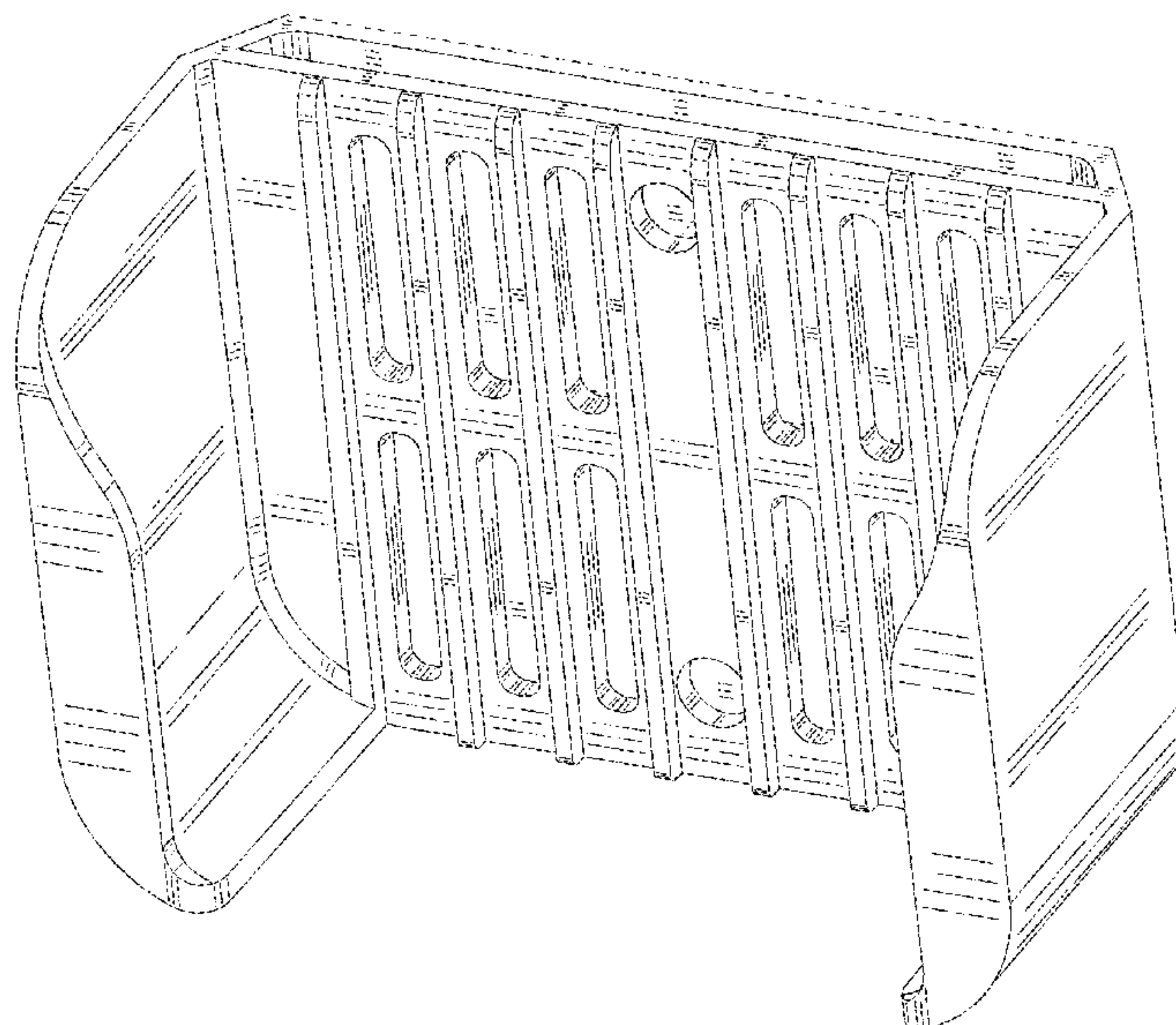
The ornamental design for a mount for electronic devices, as shown and described.

DESCRIPTION

FIG. 1 is a front, first side, and top perspective view of a mount for electronic devices embodying the new design; FIG. 2 is a front and bottom perspective view thereof; FIG. 3 is a back, second side, and bottom perspective view thereof; FIG. 4 is a front view thereof; FIG. 5 is a back view thereof; FIG. 6 is a second side view thereof; FIG. 7 is a first side view thereof; FIG. 8 is a top view thereof; and, FIG. 9 is a bottom view thereof.

The uniform repeating broken lines depict portions of a mount for electronic devices in which the claimed design is embodied and form no part of the claimed design.

1 Claim, 9 Drawing Sheets



(56) **References Cited**

U.S. PATENT DOCUMENTS

D847,800	S	5/2019	Wengreen	
D847,801	S	5/2019	Wengreen	
D905,067	S *	12/2020	Wengreen	D14/447
D905,069	S *	12/2020	Wengreen	D14/447
D914,690	S *	3/2021	Wengreen	D14/447
D931,290	S *	9/2021	Wengreen	D14/447
D985,548	S *	5/2023	Koons	D14/251

* cited by examiner

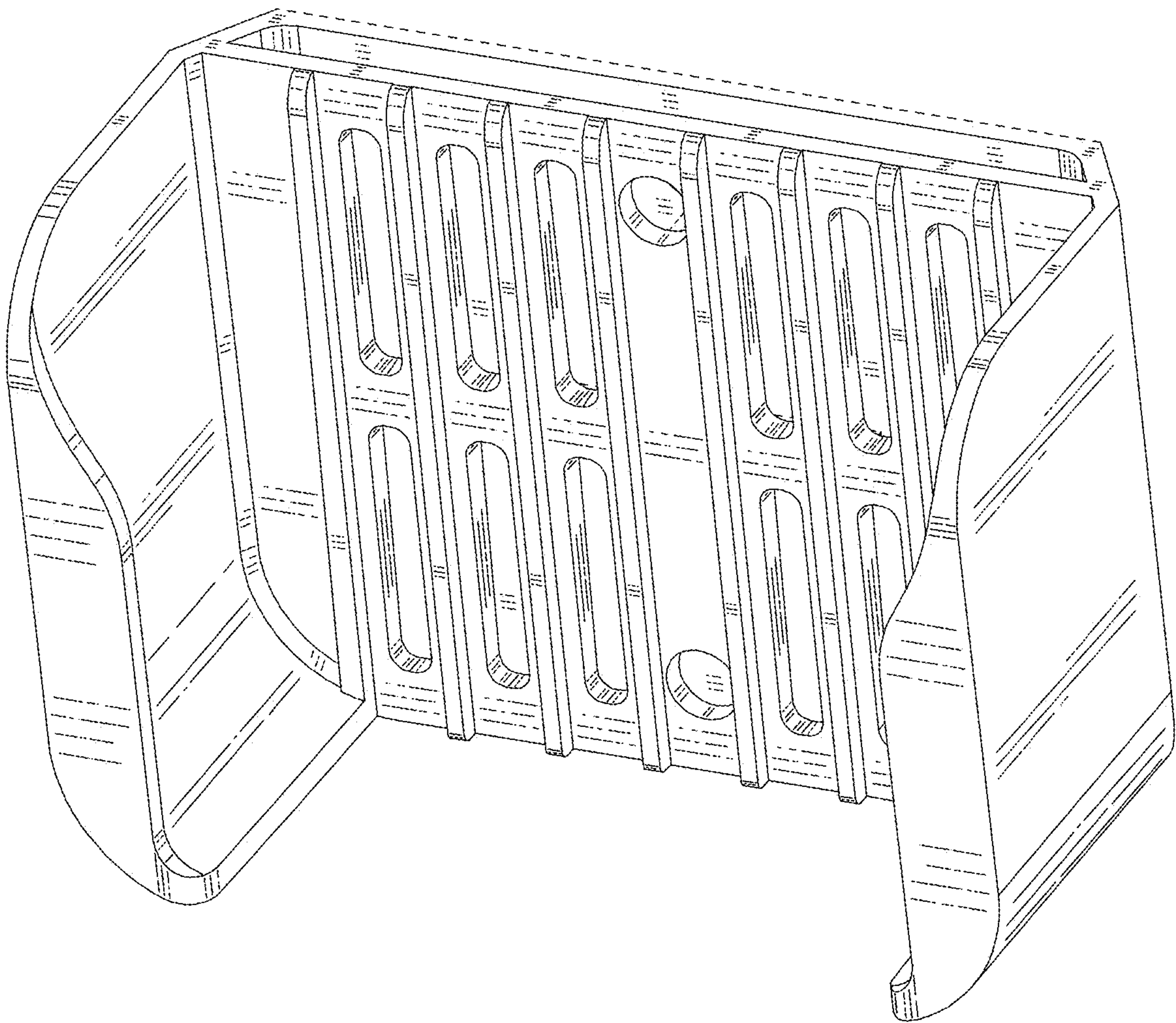


FIG. 1

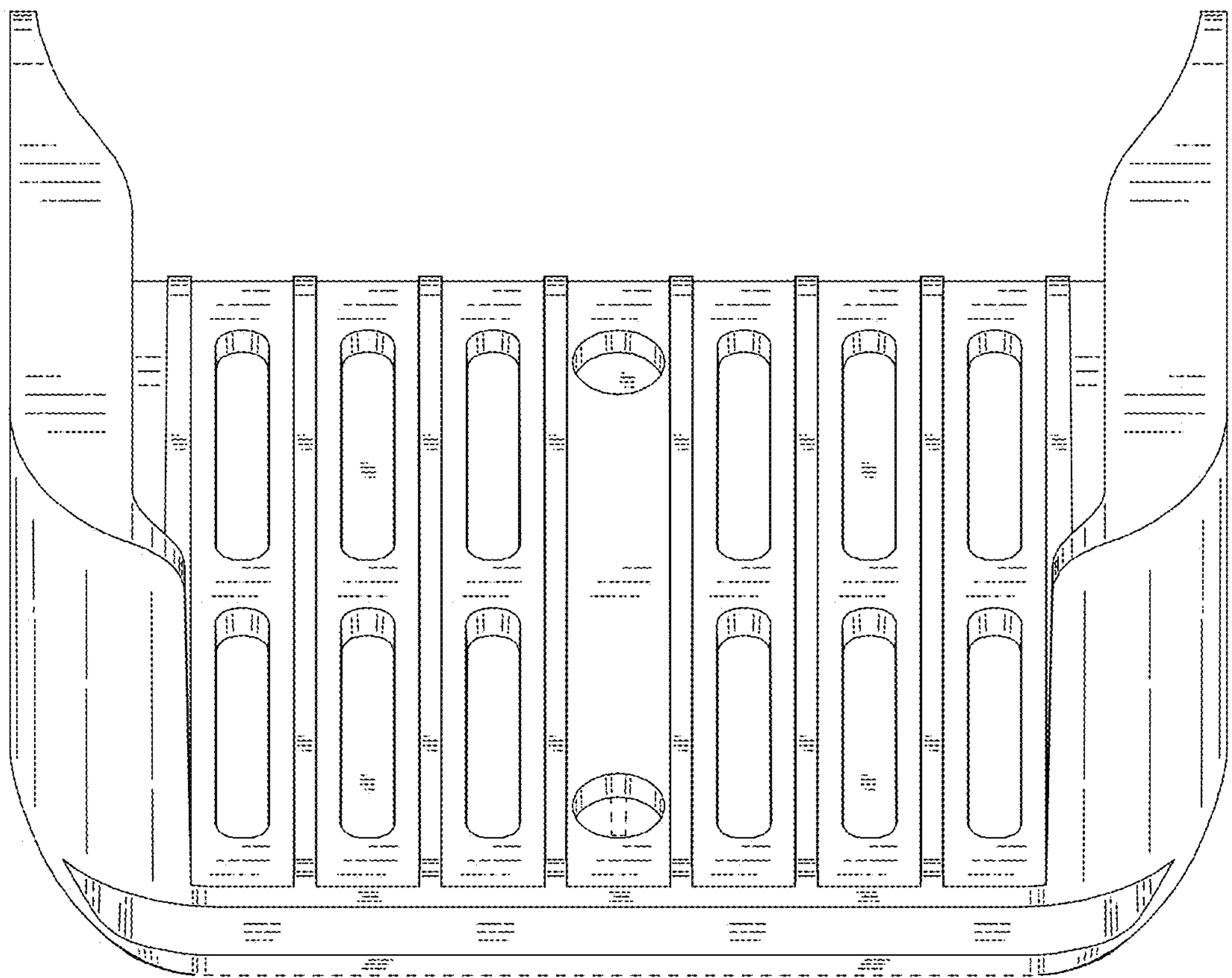


FIG. 2

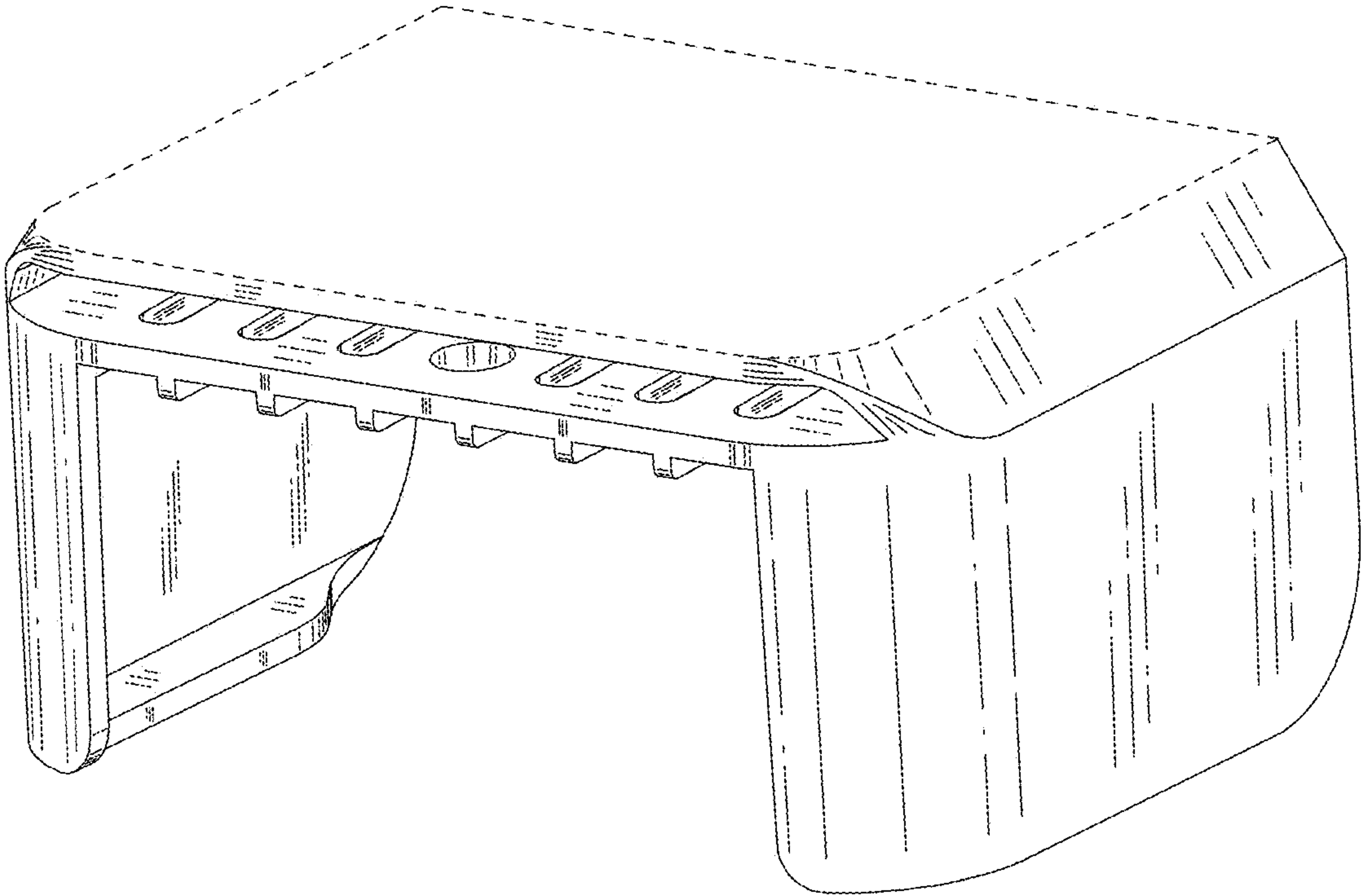


FIG. 3

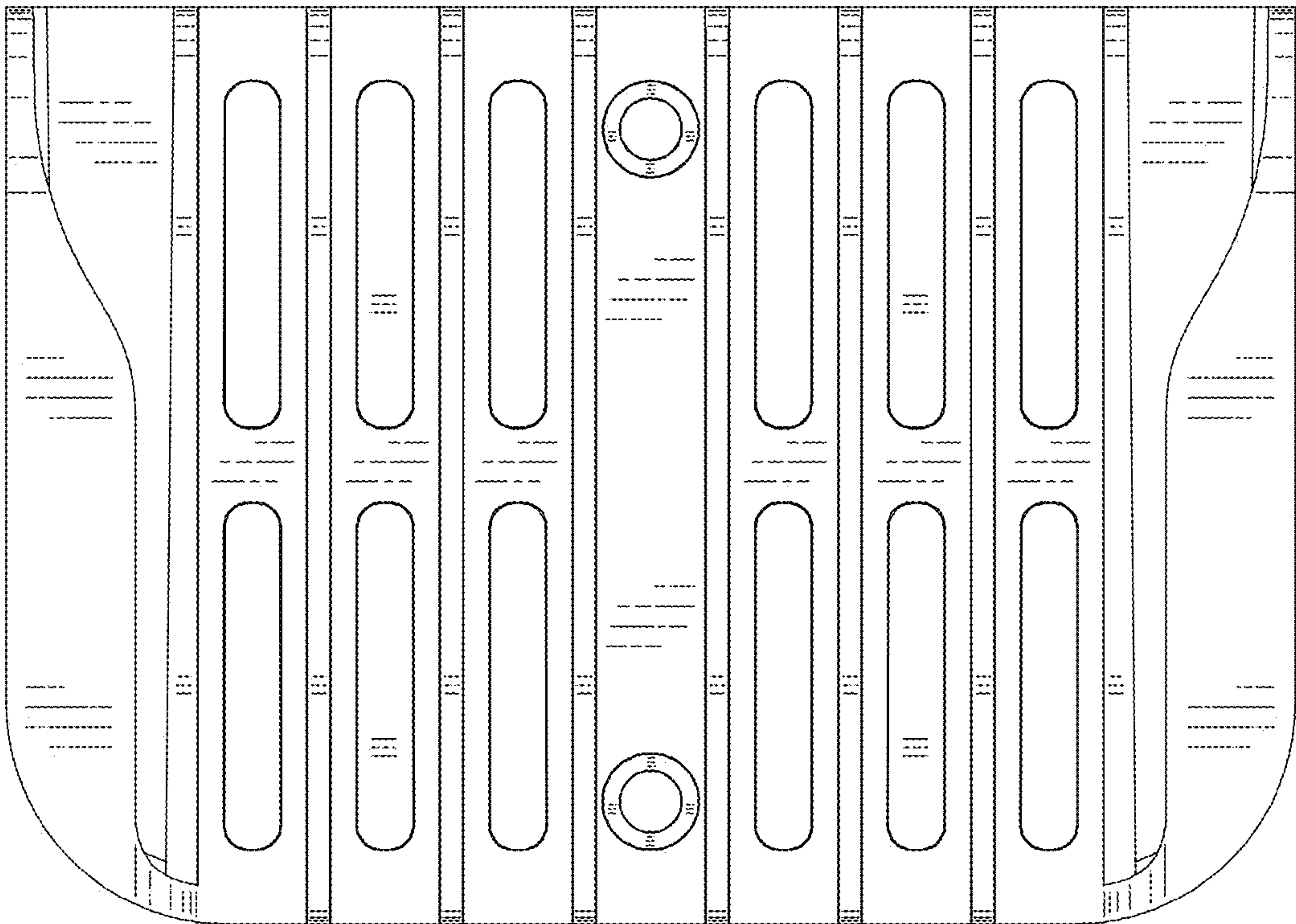


FIG. 4



FIG. 5

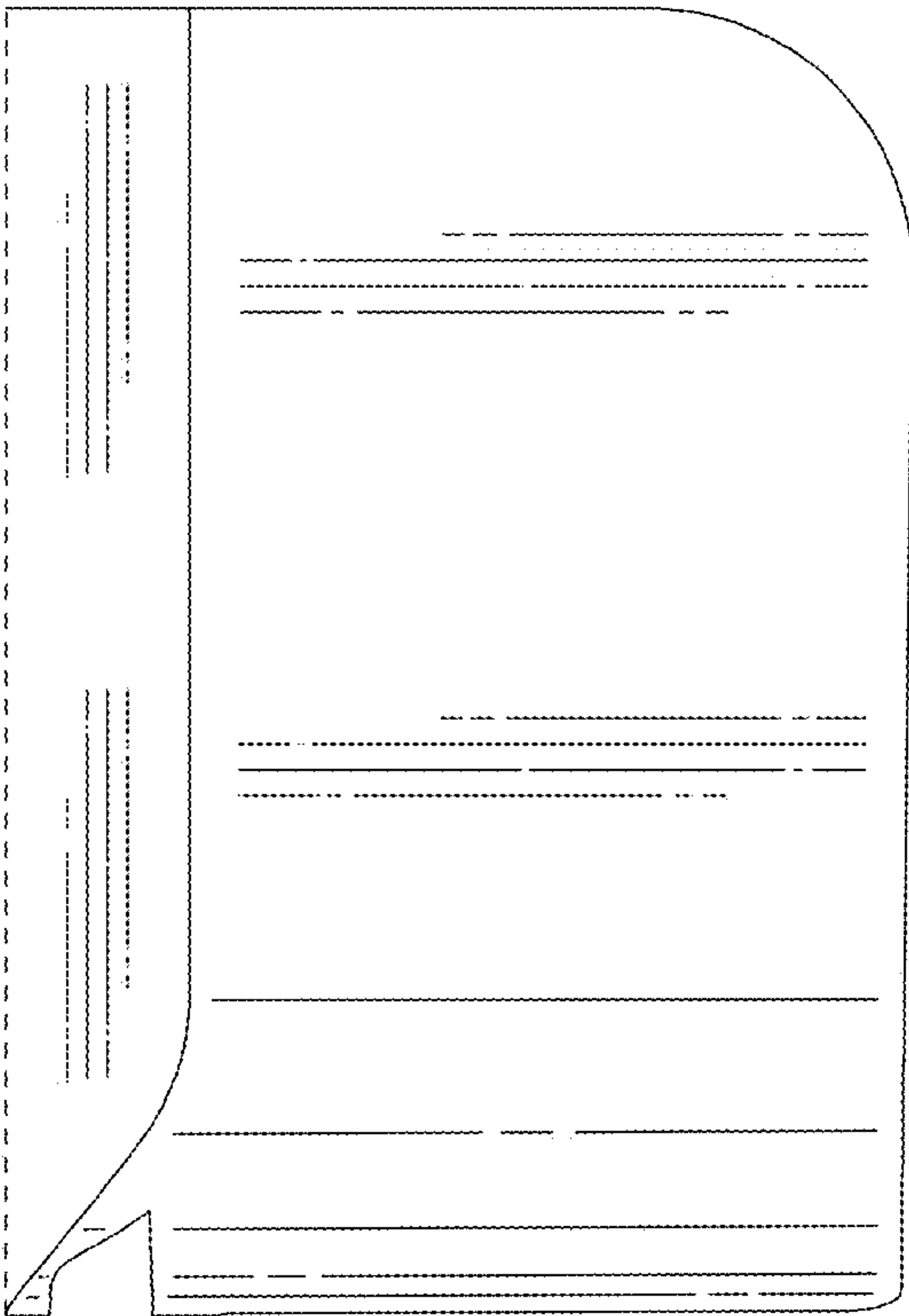


FIG. 6

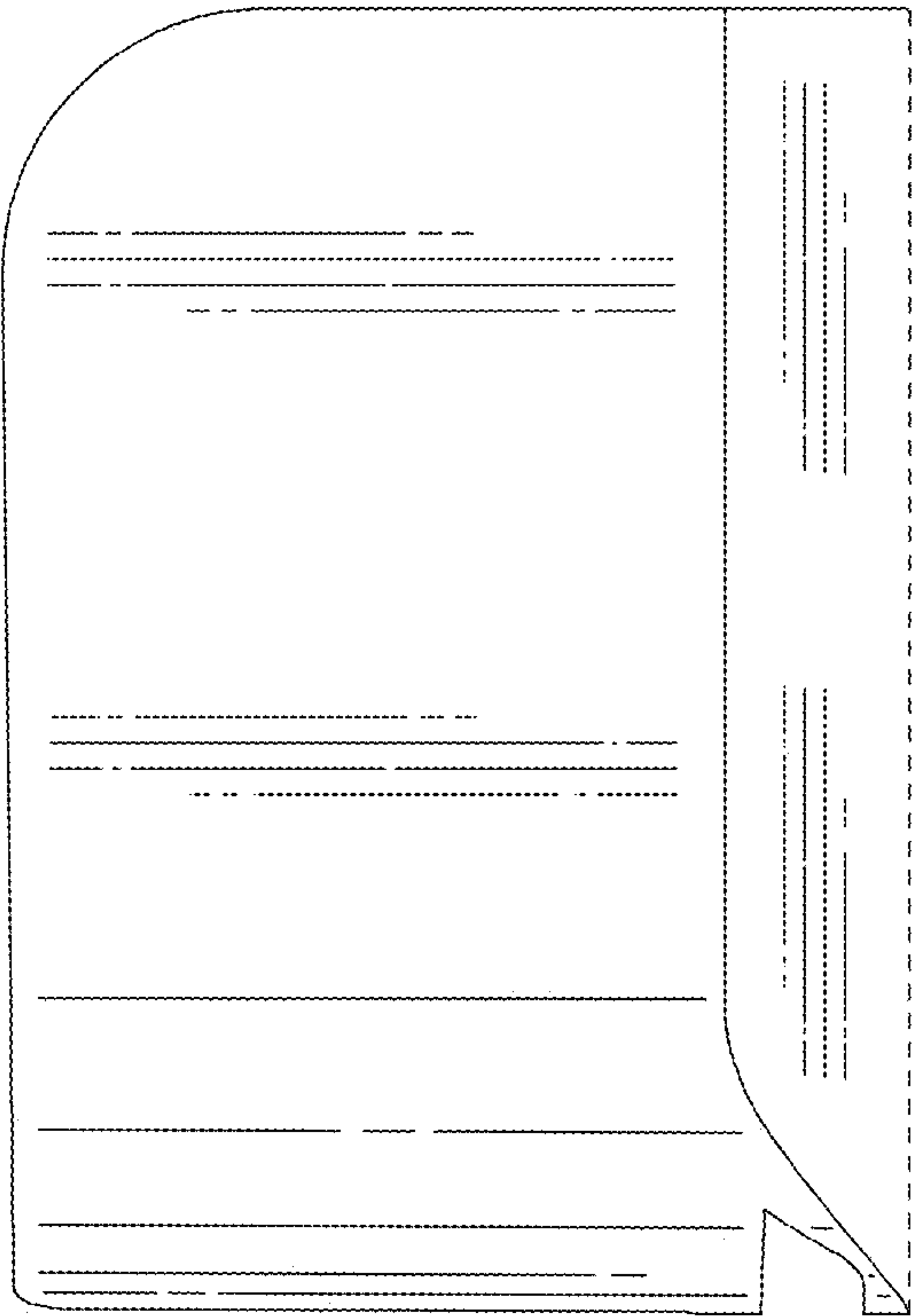


FIG. 7

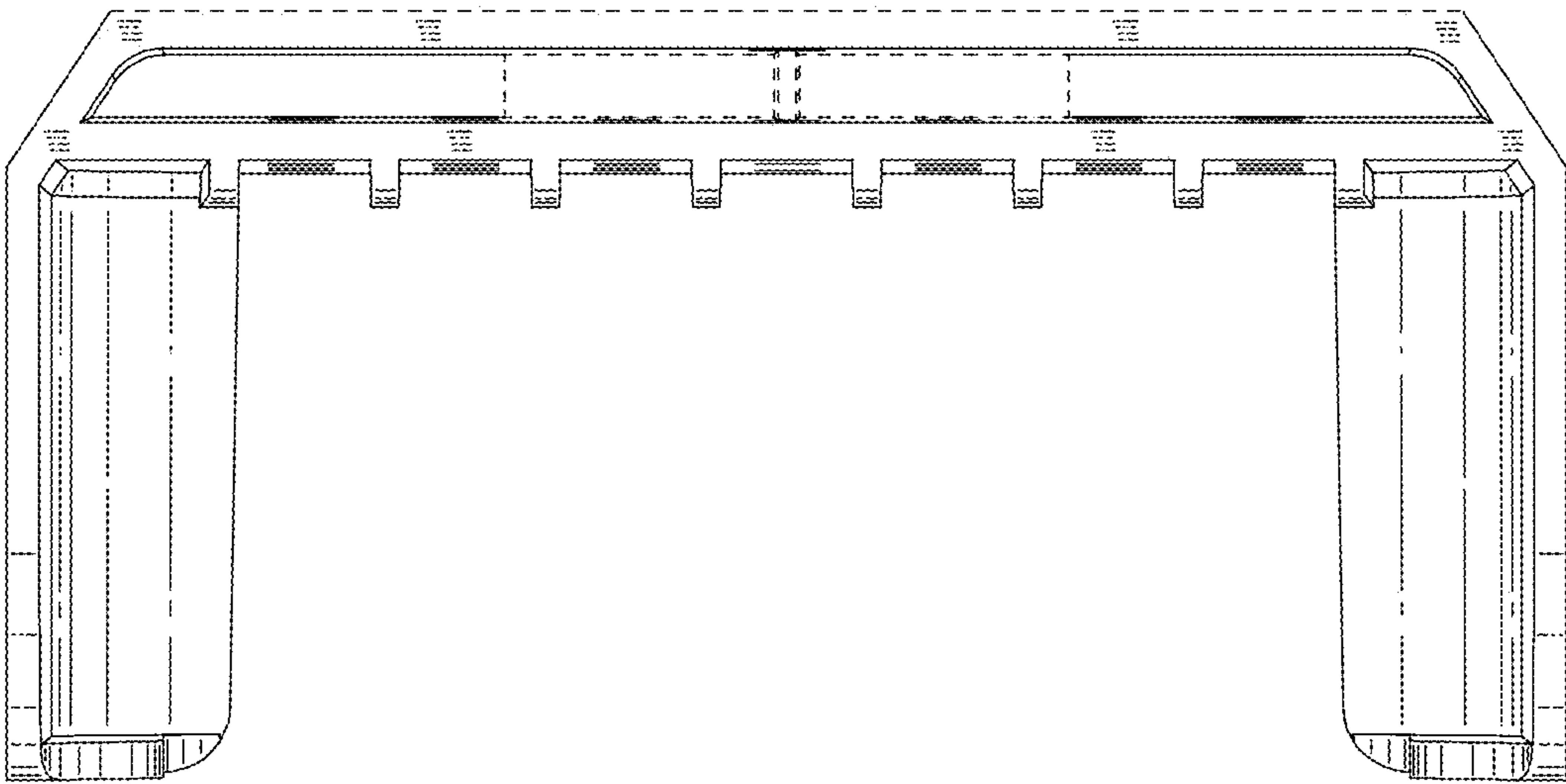


FIG. 8

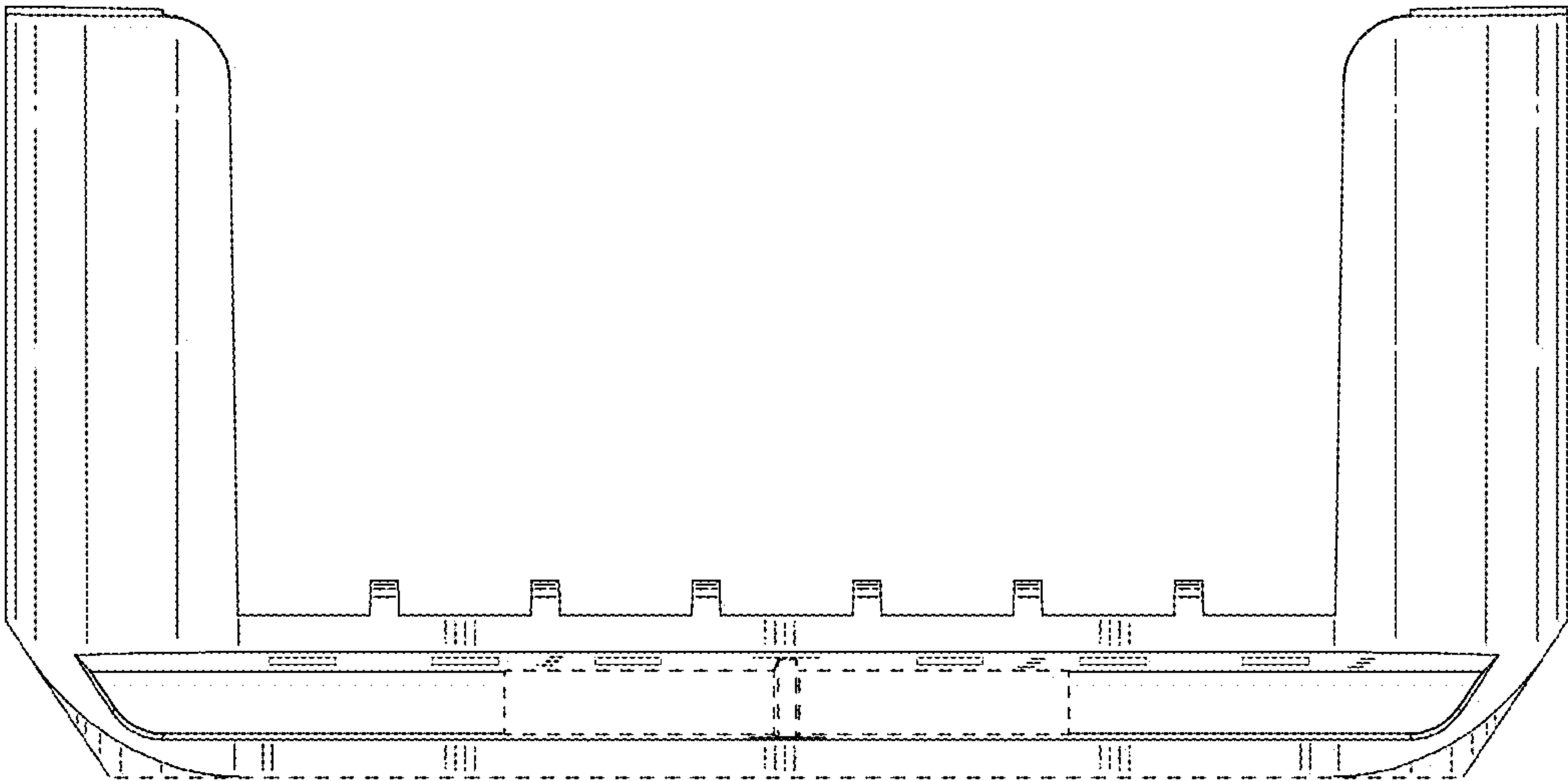


FIG. 9