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(54) **COMPUTING SYSTEM WITH POWER REQUIREMENT EVALUATION**

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G06F 1/32 (2006.01)

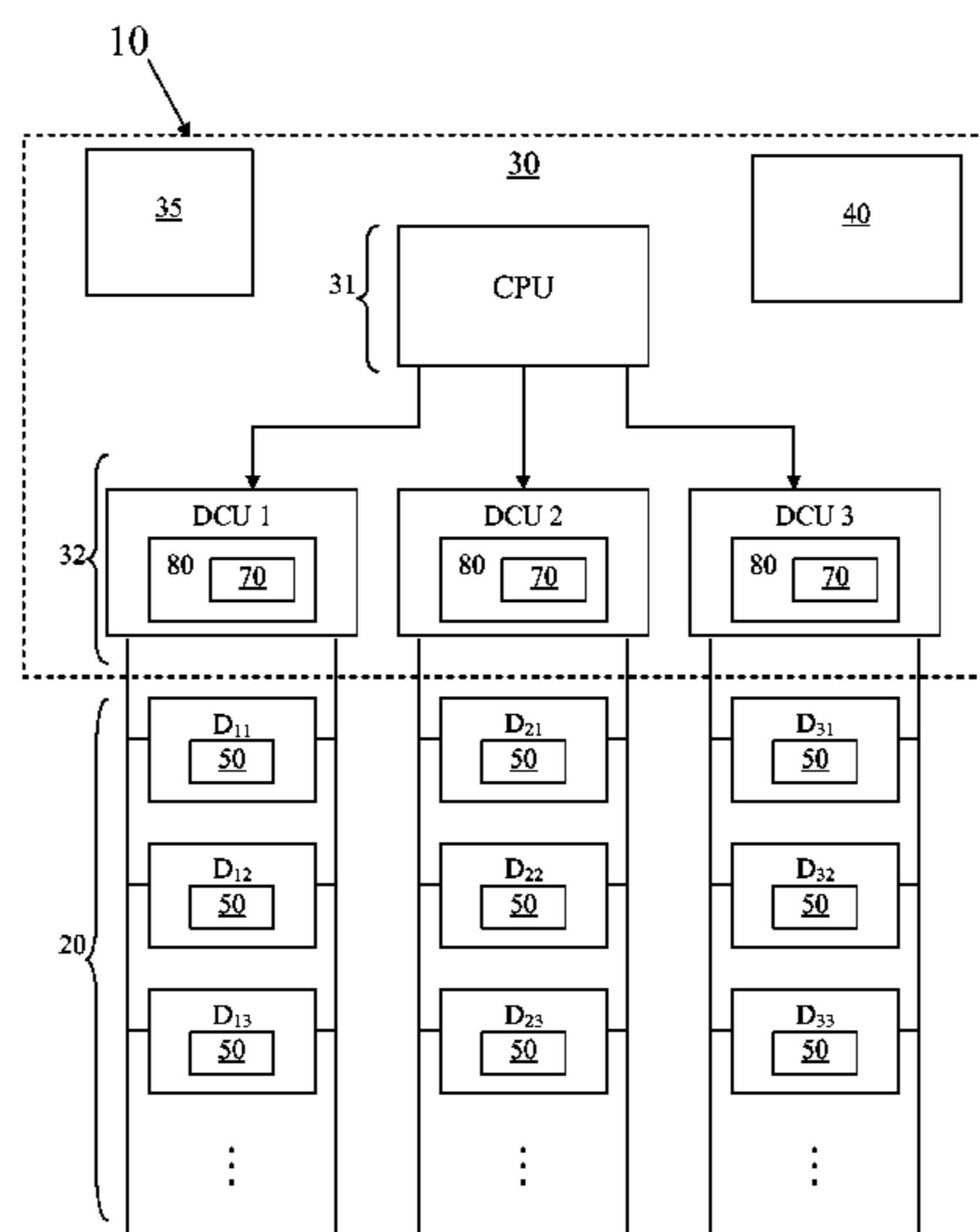
(52) **U.S. Cl.**
CPC **G06F 1/3225** (2013.01)

(58) **Field of Classification Search**
CPC **G06F 1/3225**
See application file for complete search history.

(57) **ABSTRACT**

A computing system is provided and includes recording media relative to which input/output (I/O) operations are executable, and a processor, disposed in signal communication with the recording media, which is configured to execute the I/O operations and to evaluate power requirements associated with executions of the I/O operations relative to each individual recording medium. The processor includes a computer-readable medium having a set of instructions stored thereon, which, when executed, cause the processor to schedule the executions of the I/O operations relative to each individual recording medium or to refuse the executions of the I/O operations in accordance with the evaluated power requirements.

16 Claims, 4 Drawing Sheets



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FIG. 1

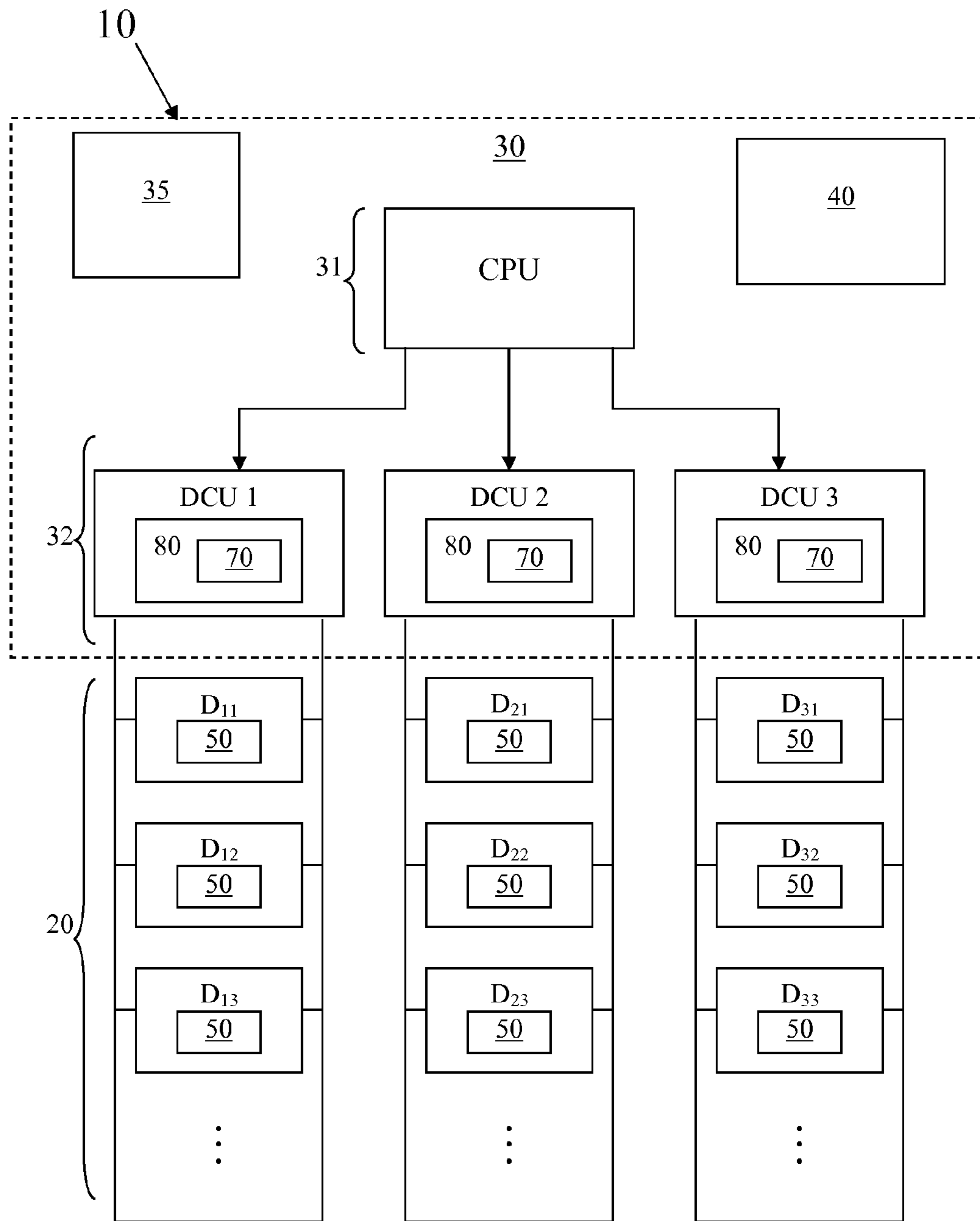
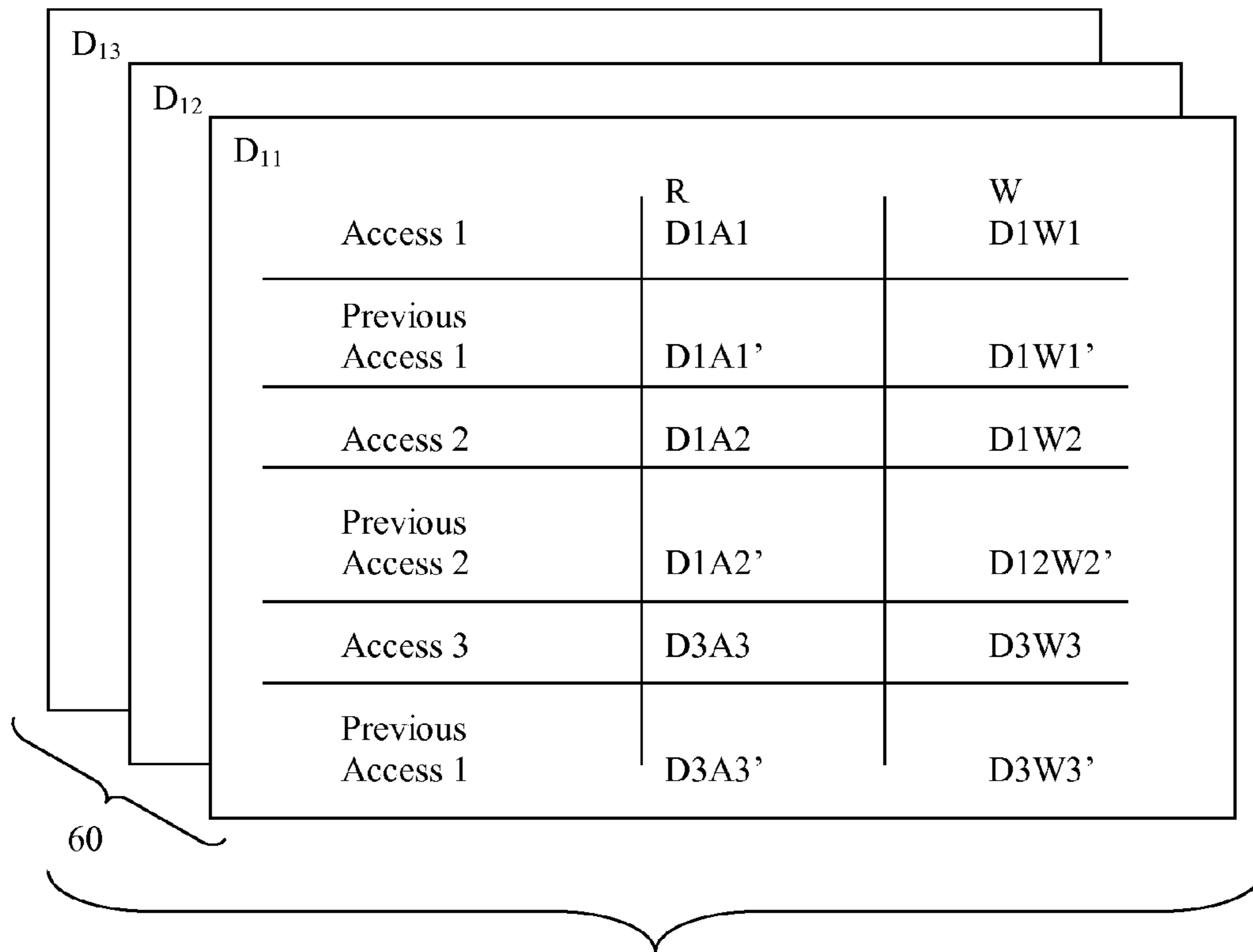


FIG. 2



<u>70</u>	R	W
Access 1	$D1A1+D2A1+D3A1$	$D1W1+D2W1+D3W1$
Previous Access 1 Data	$D1A1'+D2A1'+D3A1'$	$D1W1'+D2W1'+D3W1'$
Access 2	$D1A2+D2A2+D3A2$	$D1W2+D2W2+D3W2$
Previous Access 2 Data	$D1A2'+D2A2'+D3A2'$	$D1W2'+D2W2'+D3W2'$
Access 3	$D1A3+D2A3+D3A3$	$D1W3+D2W3+D3W3$
Previous Access 3 Data	$D1A3'+D2A3'+D3A3'$	$D1W3'+D2W3'+D3W3'$

FIG. 3

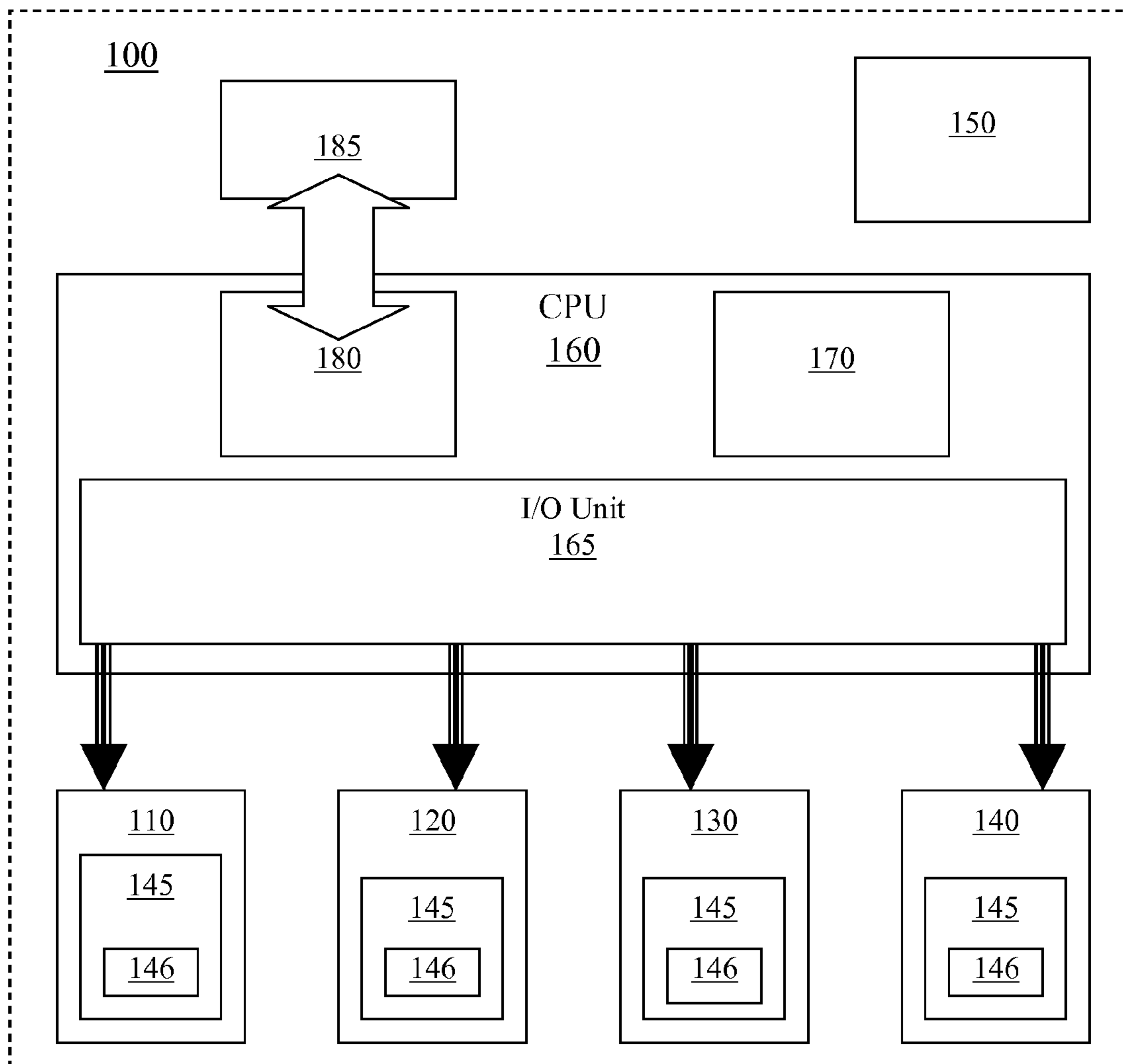
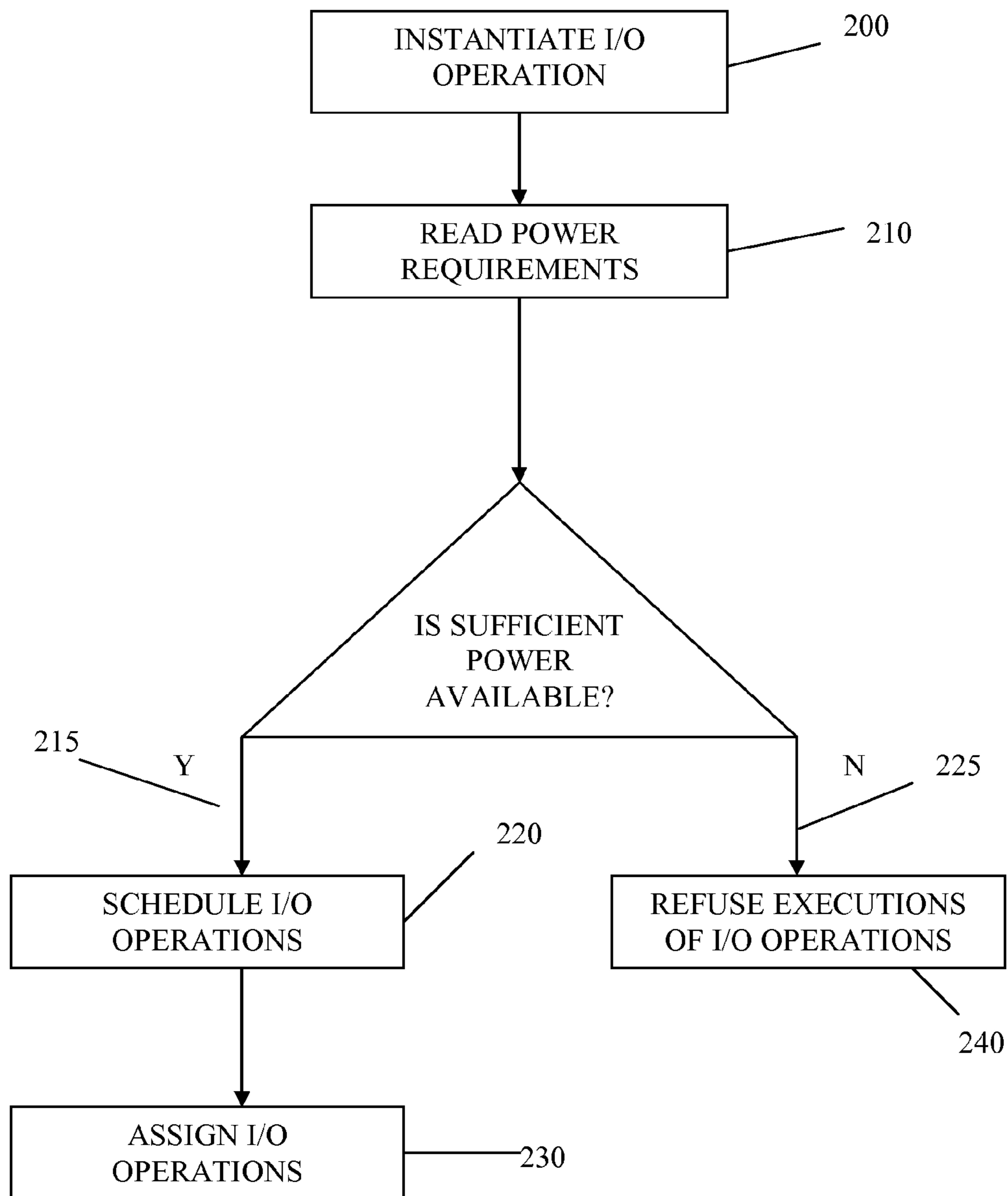


FIG. 4



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COMPUTING SYSTEM WITH POWER
REQUIREMENT EVALUATIONCROSS-REFERENCE TO RELATED
APPLICATION

This application is a continuation of and claims the benefit of priority to U.S. application Ser. No. 12/468,214, which was filed on May 19, 2009. The entire contents of U.S. application Ser. No. 12/468,214 are incorporated herein by reference.

BACKGROUND

Aspects of the present invention are directed to a computing system with power requirement evaluation.

Machines and computers, such as portable laptop computers, are often employed to execute input/output (I/O) operations, such as the burning of a CD or the writing of information to removable media. To be successfully executed, the I/O operation must be instantiated and fully conducted so as not to risk the loss of possibly important data. This requires a given amount of computing time, which is based on processing speeds and the size of the I/O operation, and the availability of sufficient power for powering the execution. The power can be provided by way of, e.g., a building's power supply when the machines and computers are plugged in or, in the case of the portable laptop computer which is not plugged into a power supply, a rechargeable battery.

If the portable laptop computer draws power from the rechargeable battery, the computer will only have access to a limited power reservoir. Over time, the amount of power within this power reservoir will decrease as the amount of available battery power dwindles. Eventually, the amount of available battery power will approach such a low level that warnings will be issued to the user to stop use and recharge. Nevertheless, in these cases, the user may persist in having his computer continue to perform I/O operations without regard to whether sufficient battery power will be available to complete the I/O operations. Thus, without further layers of protection, the user risks instantiating I/O operations without sufficient power and, therefore, the potential loss of data.

Enterprise computing systems, in which multiple possible remote servers communicate with multiple possible remote client computers, are not normally operated with battery power and are therefore not at risk of running out of power in the middle of an execution. These systems are generally coupled to one or more power grids and, as such, it is unlikely that the systems will ever lack sufficient power to complete I/O operations. With that said, costs associated with the use of the power by these systems can be significant. However, the systems do not typically consider such costs when undertaking I/O operations. Thus, a particular I/O operation may be completed using relatively expensive power even though the same I/O operation could have been conducted using cheaper power at a different time or at a remote location.

SUMMARY

In accordance with an aspect of the invention, a computing system is provided and includes recording media relative to which input/output (I/O) operations are executable, and a processor, disposed in signal communication with the recording media, which is configured to execute the I/O operations and to evaluate power requirements associated with executions of the I/O operations relative to each individual recording medium. The processor includes a computer-readable medium having a set of instructions stored thereon, which,

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when executed, cause the processor to schedule the executions of the I/O operations relative to each individual recording medium or to refuse the executions of the I/O operations in accordance with the evaluated power requirements.

5 In accordance with an aspect of the invention, a portable personal computer is provided and includes recording media relative to which input/output (I/O) operations are executable, a power source by which a quantity of power is made available for the executions of the I/O operations and a processor, disposed in signal communication with the recording media and the power source, which is configured to execute the I/O operations, to evaluate power requirements associated with executions of the I/O operations relative to each individual recording medium and to read the quantity of available power.
10 The processor includes a computer-readable medium having a set of instructions stored thereon, which, when executed, cause the processor to schedule the executions of the I/O operations relative to each individual recording medium or to refuse the executions of the I/O operations in accordance with the evaluated power requirements and the quantity of available power.

15 In accordance with an aspect of the invention, a computer readable medium having instructions stored thereon, which, when executed cause a processor of a computing system, including recording media, to execute a method of controlling the computing system is provided. The method includes instantiating input/output (I/O) operations, reading power requirements for executions of the I/O operations with respect to the recording media and scheduling the executions of the I/O operations relative to each individual recording medium, or refusing the executions of the I/O operations in accordance with the evaluated power requirements.

BRIEF DESCRIPTIONS OF THE SEVERAL
VIEWS OF THE DRAWINGS

The subject matter regarded as the invention is particularly pointed out and distinctly claimed in the claims at the conclusion of the specification. The foregoing and other aspects, features, and advantages of the invention are apparent from the following detailed description taken in conjunction with the accompanying drawings in which:

FIG. 1 is a schematic illustration of a computing system in accordance with embodiments of the invention;

45 FIG. 2 is a schematic illustration of registers in use by the computing system of FIG. 1;

FIG. 3 is a schematic illustration of a personal computer in accordance with embodiments of the invention; and

50 FIG. 4 is a flow diagram illustrating a method of controlling a computing system in accordance with embodiments of the invention.

DETAILED DESCRIPTION

55 With reference to FIG. 1, a computing system 10 is provided. The computing system 10 may be a personal computer or an enterprise computing system with multiple servers and client devices at possibly remote locations. The computing system 10 includes recording media 20, relative to which input/output (I/O) operations are executable, a processor 30 and, in some cases, a power source 40 by which a quantity of available power is provided for the executions of the I/O operations. The processor 30 is disposed in signal communication with the recording media 20 and is configured to execute the I/O operations and to evaluate power requirements associated with executions of the I/O operations relative to each individual recording medium 20. To this end, the

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processor **30** includes a memory unit **35**. The memory unit **35** includes a computer-readable medium having a set of instructions stored thereon, which, when executed, cause the processor **30** to schedule the executions of the I/O operations relative to each individual recording medium **20** or to refuse the executions of the I/O operations in accordance with the evaluated power requirements.

As will be described below, the processor **30** may include multiple processing levels arranged in a tree format that includes a top level **31**, at which a central processing unit (CPU) may reside, and a bottom level **32**, at which one or more disk control units (DCU **1**, DCU **2** and DCU **3**) reside. Additional processing levels may, of course, be provided between the top and bottom levels **31** and **32**. I/O operations are normally instantiated at the top level **31** as the CPU receives and interprets inputted read and write commands. The CPU then assigns the I/O operations downstream in accordance with read power requirements until they reach each of the various DCUs at the bottom level **32** as atomized I/O operations. Each of the atomized I/O operations is then carried out in accordance with read power requirements at a recording medium **20** coupled to an associated one of the DCUs.

The recording media **20** may include removable media that can be removably coupled to the computing system **10** or non-removable media that are normally coupled to the computing system **10**. They may also include recording media, such as a universal serial bus (USB) flash drive, an optical disk, a DVD-ROM, etc., from which information is read or onto which information is written in accordance with the executions of the I/O operations. In any case, each recording medium **20** is provided with a power requirement gauge **50**. The power requirement gauge **50** is readable by the processor **30** and provides an indication to the processor **30** as to how much power is required for an I/O operation to be executed with respect to the recording medium **20**.

As shown in FIG. 1, each DCU is normally associated with a set of devices acting as recording media. Thus, DCU **1** is associated with devices $D_{11}, D_{12}, D_{13}, \dots$, in such a way as to allow DCU **1** to manage the executions of I/O operations of those devices and to read their respective power requirements indicated by their respective power requirement gauges **50**. Similarly, DCU **2** is associated with devices $D_{21}, D_{22}, D_{23}, \dots$, in such a way as to allow DCU **2** to manage the executions of I/O operations of those devices and to read their respective power requirement gauges **50** and, finally, DCU **3** is associated with devices $D_{31}, D_{32}, D_{33}, \dots$, in such a way as to allow DCU **2** to manage the executions of I/O operations of those devices and to read their respective power requirement gauges **50**.

In accordance with an embodiment of the invention and, as shown in FIG. 2, the power requirement gauge **50** for each device may indicate the respective power requirement for executing an I/O operation of the device in the form of a register **60**. That is, each register **60** may be configured to describe the power requirement for its device for each read (R) and each write (W) access incidence of any I/O operation executed with respect to the associated device. Thus, at a particular time, device D_{11} requires D1A1 and D1W1 power for read and write operations of access **1**, D1A2 and D1W2 power for read and write operations of access **2** and D1A3 and D1W3 power for read and write operations of access **3**, and so on. Each register **60** may further be configured to indicate previous access data (i.e., D1A1', D1A2', etc.) that is periodically updated and reflective of power requirements of previous read and write access incidences.

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The register **60** for each device is readable by the DCU to which that device is associated. Thus, DCU **1** reads the registers **60** of devices D_{11}, D_{12} and D_{13} . Information gleaned from the registers **60** may then be compared with the quantity of power available for I/O operations (this being more important where the computing system **10** is being run with battery power) and is then employed by DCU **1** to schedule the executions of the I/O operations relative to devices D_{11}, D_{12} and D_{13} or to refuse the executions of the I/O operations in accordance with the quantity of available power.

In accordance with embodiments of the invention, if the DCU **1** compares the information gleaned from register **60** with the quantity of power available for I/O operations and the DCU **1** or the processor **30** subsequently determines that the quantity of power available may be insufficient for a given atomized I/O operation, the DCU **1** may either refuse the executions of the I/O operation, in which case DCUs **2** or **3** may take over, or warn the user before commencing with the I/O operation and only proceeding upon receipt of a user inputted command to do so. Similarly, if the DCU **1** subsequently determines that the quantity of power available for the I/O operation is so diminished that an execution of the I/O operation will definitely fail, the DCU **1** may simply refuse the execution without offering the user the opportunity to override that judgment.

If the user inputs the command to commence with the I/O operation, in accordance with further embodiments of the invention, the I/O operation could be conducted in such a manner as to increase the likelihood that the I/O operation could be relatively easily resumed once power is restored. As an example, while a file is written to device D_{11} as part of the I/O operation, a counter of the offset could also be written to the file. Here, the write operation could be resumed from the last check-pointed location on device D_{11} even after a complete power outage and restart.

In accordance with other embodiments of the inventions, if the DCU **1** determines that sufficient power for one or more atomized I/O operations is available, the DCU **1** schedules the executions of the I/O operations relative to devices D_{11}, D_{12} and D_{13} . That is, the DCU **1** assigns the atomized I/O operations to any one or more of the devices in various combinations based upon the information gleaned from register **60** and predefined algorithms. For example, the DCU **1** could be configured to assign atomized I/O operations to the device that requires the least power for execution of the atomized I/O operations. Alternatively, the DCU **1** or the processor **30** could be configured to prioritize the atomized I/O operations with the DCU **1** being further configured to consider the power requirements and the priorities in assigning the atomized I/O operations.

Still further, the DCU **1** or the processor **30** may additionally analyze the previous access data and modify the assignments based on results of that analysis. That is, if the previous access data were found to be significantly different than the access data in a particular instance for device D_{11} , the DCU **1** may judge that the access data is unreliable and assign a pending I/O operation to another device even though the access data for the other device suggests that it is more power intensive than device D_{11} .

The information gleaned from register **60** of devices D_{11}, D_{12} and D_{13} is also aggregated at the DCU **1** register **70** where it can be stored, at least temporarily, in memory unit **80**, which is disposed in relative association with the DCU **1**. The DCU **1** register **70** includes information reflective of the total power requirement for access incidences of I/O operations for devices D_{11}, D_{12} and D_{13} along with information reflective of historical power requirements for devices D_{11}, D_{12} and D_{13} .

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The DCU 1 register 70 along with registers for any other DCUs in computing system 10 can be transmitted to higher processing levels, such as top level 31, where additional I/O operation assignments can be made.

That is, with reference to FIG. 1, if processor 30 receives the DCU registers 70 from DCU 1, DCU 2 and DCU 3, the processor 30 can determine based on information gleaned from the DCU registers 70 and predefined algorithms which DCU and, by the same token, which recording media 20, will be best equipped to execute certain I/O operations. For example, if DCU 1 uses slightly less power than DCU 2 but DCU 1 and its associated recording media 20 are located in a region in which power is relatively very expensive, the processor 30 may assign I/O operations to DCU 2 to save costs.

With reference to FIG. 3 and, in accordance with an aspect of the invention, a portable personal computer, such as a laptop 100, is provided. The laptop 100 may include removable and non-removable and fragmented and non-fragmented recording media, such as a universal serial bus (USB) flash drive 110, an optical disk drive 120, a DVD recorder 130 and a hard disk drive 140 relative to which input/output (I/O) operations are executable, a power source 150, such as a lithium-ion rechargeable battery, and a processor 160, such as a central processing unit (CPU). Each recording medium includes a power requirement gauge 145 and a register 146 that are configured in a similar manner as described above. The power source 150 is coupled to the recording media and the processor 160 and makes available a quantity of power to allow for the executions of the I/O operations.

The processor 160 is coupled to the power source 150 and includes an I/O unit 165, which is disposed in signal communication with the recording media. The processor 160 is thereby configured to execute the I/O operations by way of the I/O unit 165, to evaluate power requirements associated with executions of the I/O operations relative to each individual recording medium and to read the quantity of available power made available by the power source 150. To this end, the processor 160 includes a memory unit 170. The memory unit 170 may be a computer-readable medium having a set of instructions stored thereon. When executed, the instructions cause the processor 160 to schedule the executions of the I/O operations relative to each individual recording medium or to refuse the executions of the I/O operations in accordance with the evaluated power requirements and the quantity of available power.

Thus, as described above and, in accordance with an embodiment of the invention, if a user of the laptop 100 inputs a command to the processor 160 to, e.g., burn a DVD, the processor 160 may interrogate the power source 150 for a reading of the available power and, at the same time, the I/O unit 165 may read the power requirement from register 146 of the DVD recorder 130. If the available power is subsequently determined to be greater than the power requirement of the DVD recorder 130, the processor 160 executes the DVD burn command at the DVD recorder by way of the I/O unit 165. If, on the other hand, the available power is determined to be less than the power requirement, the processor 160 may simply refuse the execution or may issue a warning to the user as to the insufficient power situation by way of a display driver 180 and a display unit 185. If the user chooses to proceed, the processor 160 may then execute the DVD burn command by way of the I/O unit 165 in such a manner as to allow the command to be resumed if a power outage occurs before completion, as described above.

As described above, register 146 may include current power requirement data and historical power requirement data, which is periodically updated. In this way, the laptop

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100 can adjust the determinations of whether sufficient power will be available for certain I/O operations over time. This can be particularly relevant where the recording medium being read from or written to is fragmented. In this case, the power requirement for similar I/O operations with respect to that recording medium will increase over time. As such, the processor 160 will consider the historical power requirement data of register 146 to judge whether the current power requirement data is reliable or not.

In an alternate embodiment, the I/O unit 165 may also read the power requirements of recording media that are reasonably analogous to the DVD recorder 130, such as the USB flash drive 110, to determine whether the DVD burn command can be executed with respect to another device with a lower power requirement. In this case, if the USB flash drive 110 is found to have a lower power requirement than the DVD recorder 130 and the quantity of available power from the power source 150, the processor 160 may suggest copying the data that was to be burned to the DVD onto the USB flash drive 110. If the user chooses this option, the processor 160 executes the copy command with respect to the USB flash drive by way of the I/O unit 165.

With reference to FIG. 4 and, in accordance with another aspect of the invention, a computer readable medium is provided. The computer readable medium has instructions stored thereon, which, when executed cause a processor of a computing system, including recording media, to execute a method of controlling the computing system. The method includes instantiating input/output (I/O) operations 200, reading power requirements for executions of the I/O operations with respect to the recording media 210, and, if sufficient power is available 215, scheduling the executions of the I/O operations relative to each individual recording medium 220 to occur immediately or following a delay, or, if sufficient power is unavailable 225, refusing the executions of the I/O operations in accordance with the evaluated power requirements 230 and, in some cases, in accordance with priorities of the executions. Where the executions are scheduled to occur, the method may further include assigning the executions to particular recording media 240.

A collection of processors configured as described previously, can collectively be managed by yet another layer of systems management. Just as in the case where a single processor, with a collection of DCUs attached may observe the relative power requirements to drive I/O for each DCU and make I/O Scheduling decisions according to that data, so too can a workload management system observe the relative power requirements for scheduling a workload across multiple processors and schedule work to run on the most power efficient processor, or the processor with the lowest power cost per unit of work. Additionally, given that the workload manager knows the power cost of each application running across all the processors in the environment, it may elect to stop low priority applications on several processors to lower the overall power consumption of the collection of processors such that the total power consumed by the collection of processors is below a threshold value.

While the disclosure has been described with reference to exemplary embodiments, it will be understood by those skilled in the art that various changes may be made and equivalents may be substituted for elements thereof without departing from the scope of the disclosure. In addition, many modifications may be made to adapt a particular situation or material to the teachings of the disclosure without departing from the essential scope thereof. Therefore, it is intended that the disclosure not be limited to the particular exemplary embodiment disclosed as the best mode contemplated for

carrying out this disclosure, but that the disclosure will include all embodiments falling within the scope of the appended claims.

What is claimed is:

1. A computing system, comprising:
 - recording media relative at which write accesses are executable; and
 - a processor, disposed in signal communication with the recording media, which is configured to execute the write accesses and to evaluate power requirements associated with executions of the write accesses relative to each individual recording medium, the processor including:
 - a computer-readable medium having a set of instructions stored thereon, which, when executed, cause the processor to:
 - refuse the executions of the write accesses relative to each individual recording medium in accordance with the quantity of available power provided by a power source for the write accesses being determined to be insufficient at the power source to complete the write accesses at each individual recording medium relative to the respective evaluated power requirements of each individual recording medium, and
 - commence execution of a refused write access of a file by writing a counter to the file, restoring power following a complete power outage and, in an event the writing was incomplete at the instance of the complete power outage, resuming the writing in accordance with the counter.
2. The computing system according to claim 1, wherein the recording media comprise at least one of removable and non-removable recording media.
3. The computing system according to claim 1, further comprising a plurality of processor readable power requirement gauges, wherein each individual recording medium comprises:
 - a corresponding, single one of the plurality of processor readable power requirement gauges,
 - each one of the plurality of processor readable power requirement gauges being indicative of the respective power requirements of each incidence of a write access.
4. The computing system according to claim 1, further comprising a power source by which the quantity of available power is provided for the executions of the write accesses.
5. The computing system according to claim 1, wherein, when executed, the instructions further cause the processor to prioritize the executions.
6. The computing system according to claim 1, wherein, when executed, the instructions further cause the processor to schedule the executions by assigning atomic write accesses to individual ones of the recording media.
7. The computing system according to claim 1, wherein the processor is organized in multiple levels, including at least top and bottom levels, with each level including one or more processing units.
8. The computing system according to claim 7, wherein the write accesses are instantiated at the top level and provided as atomic write accesses at the bottom level.
9. The computing system according to claim 7, wherein the top level includes a central processing unit (CPU) and each lower level includes disk control units (DCUs), each DCU being respectively associated with at least one individual recording medium.
10. The computing system according to claim 9, wherein, when executed, the instructions cause each DCU at the bottom level to read power requirements associated with execu-

tions of the write accesses relative to each individual associated recording medium from corresponding DCU registers.

11. The computing system according to claim 10, wherein the read power requirements associated with executions of the write accesses relative to each individual associated recording medium are up-level transmittable.

12. The computing system according to claim 11, wherein the DCU registers are, at least, temporarily stored in recording media of each of the DCUs.

13. A portable personal computer, comprising:
 - recording media relative to which write accesses are executable;
 - a power source by which a quantity of power is made available for the executions of the write accesses; and
 - a processor, disposed in signal communication with the recording media and the power source, which is configured to execute the write accesses, to evaluate power requirements associated with executions of the write accesses relative to each individual recording medium and to read the quantity of available power, the processor including:
 - a computer-readable medium having a set of instructions stored thereon, which, when executed, cause the processor to:
 - refuse the executions of the write accesses relative to each individual recording medium in accordance with the quantity of available power provided by the power source for the write accesses being determined to be insufficient at the power source to complete the write accesses at each individual recording medium relative to the respective evaluated power requirements of each individual recording medium, and
 - commence execution of a refused write access of a file by writing a counter to the file, restoring power following a complete power outage and, in an event the writing was incomplete at the instance of the complete power outage, resuming the writing in accordance with the counter.
14. A non-transitory computer readable medium having instructions stored thereon, which, when executed cause a processor of a computing system, including recording media, to execute a method of controlling the computing system, the method comprising:
 - instantiating write accesses;
 - interrogating a power source for a reading of a quantity of available power at the power source;
 - reading power requirements for executions of the write accesses with respect to the recording media;
 - determining whether the read quantity of the available power at the power source is insufficient for completing the executions of the write accesses relative to the respective evaluated power requirements of each individual recording medium;
 - refusing the executions of the write accesses relative to each individual recording medium in accordance with the read quantity of available power at the power source for the write accesses being determined to be insufficient for completing write accesses at each individual recording medium relative to the respective evaluated power requirements of each individual recording medium; and
 - commencing execution of a refused write access of a file by writing a counter to the file, restoring power following a complete power outage and, in an event the writing was incomplete at the instance of the complete power outage, resuming the writing in accordance with the counter.

15. The computer readable medium according to claim 14, wherein the method further comprises assigning the execution of the I/O operations to the recording media in accordance with the refusing.

16. A computing system for assuring completion of write accesses to files when power available to the system is deteriorating, the system comprising:

a processor configured to communicate with recording media, the processor configured to perform a method comprising:

in a first stage of operation, receiving, by the processor, requests to execute the write accesses, executing requested write accesses to each recording medium of said recording media and determining power consumption associated with executions of the write accesses of each recording medium; and

in a second stage of operation, receiving, by the processor, a request to execute a write access to a file at a recording medium;

interrogating, by the processor, a power source for a reading of a quantity of available power at the power source; determining, by the processor, the current amount of available power as a result of the interrogating and determin-

ing an amount of power required to complete the write access at the recording medium based on the determined power consumption associated with executions of the write accesses of each recording medium;

executing the write access to the recording medium based on the determined amount of power required to complete the write access at the recording medium being less than the determined current amount of available power at the power source; and

blocking execution of the write access to the recording medium based on the determined amount of power required to complete the write access at the recording medium being greater than the determined current amount of available power at the power source whereby the current amount of available power at the power source is insufficient to complete the write access at the recording medium; and

commencing execution of the write access by writing a counter to the file, restoring power following a complete power outage and, in an event the writing was incomplete at the instance of the complete power outage, resuming the writing in accordance with the counter.

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