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(54) **LAMINATING SYSTEM**

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257/E23.127; **257/E29.273**

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156/244.11

See application file for complete search history.

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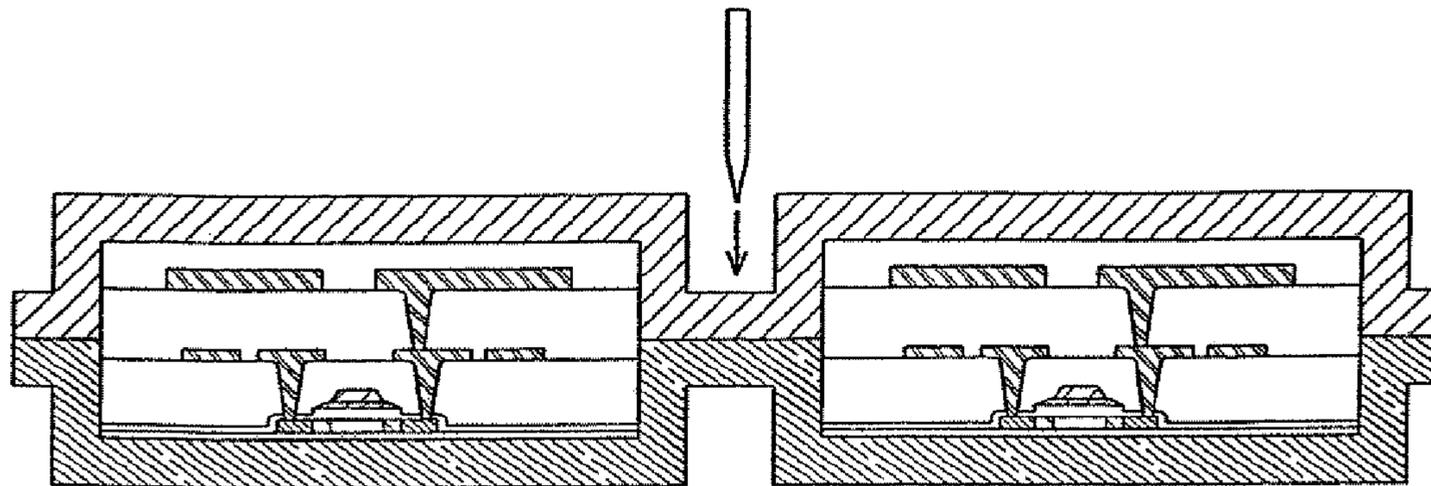
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(57) **ABSTRACT**

It is an object of the invention to improve the production efficiency in sealing a thin film integrated circuit and to prevent the damage and break. Further, it is another object of the invention to prevent a thin film integrated circuit from being damaged in shipment and to make it easier to handle the thin film integrated circuit. The invention provides a laminating system in which rollers are used for supplying a substrate for sealing, receiving IC chips, separating, and sealing. The separation, sealing, and reception of a plurality of thin film integrated circuits can be carried out continuously by rotating the rollers; thus, the production efficiency can be extremely improved. Further, the thin film integrated circuits can be easily sealed since a pair of rollers opposite to each other is used.

20 Claims, 13 Drawing Sheets



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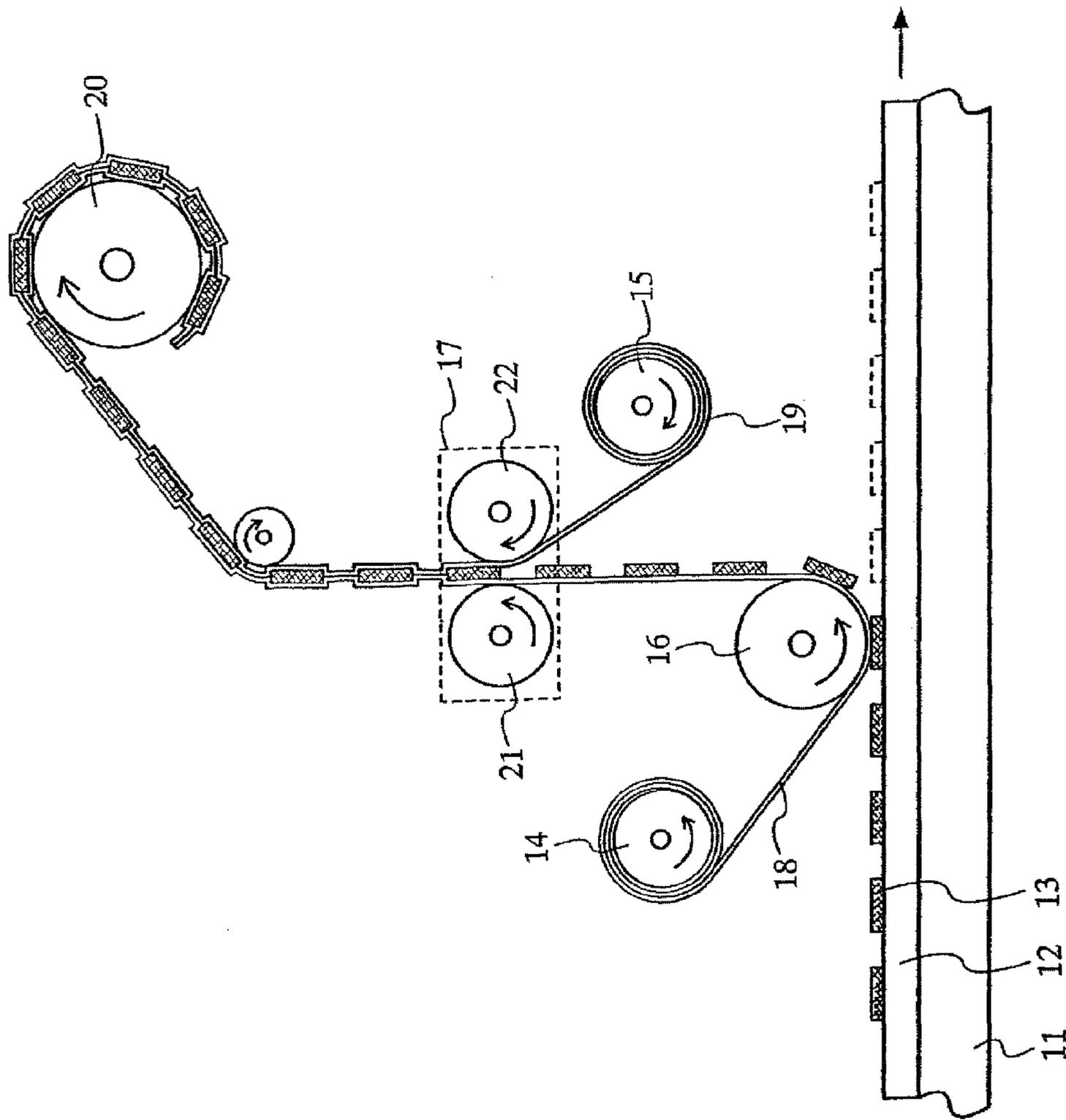
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FIG. 1



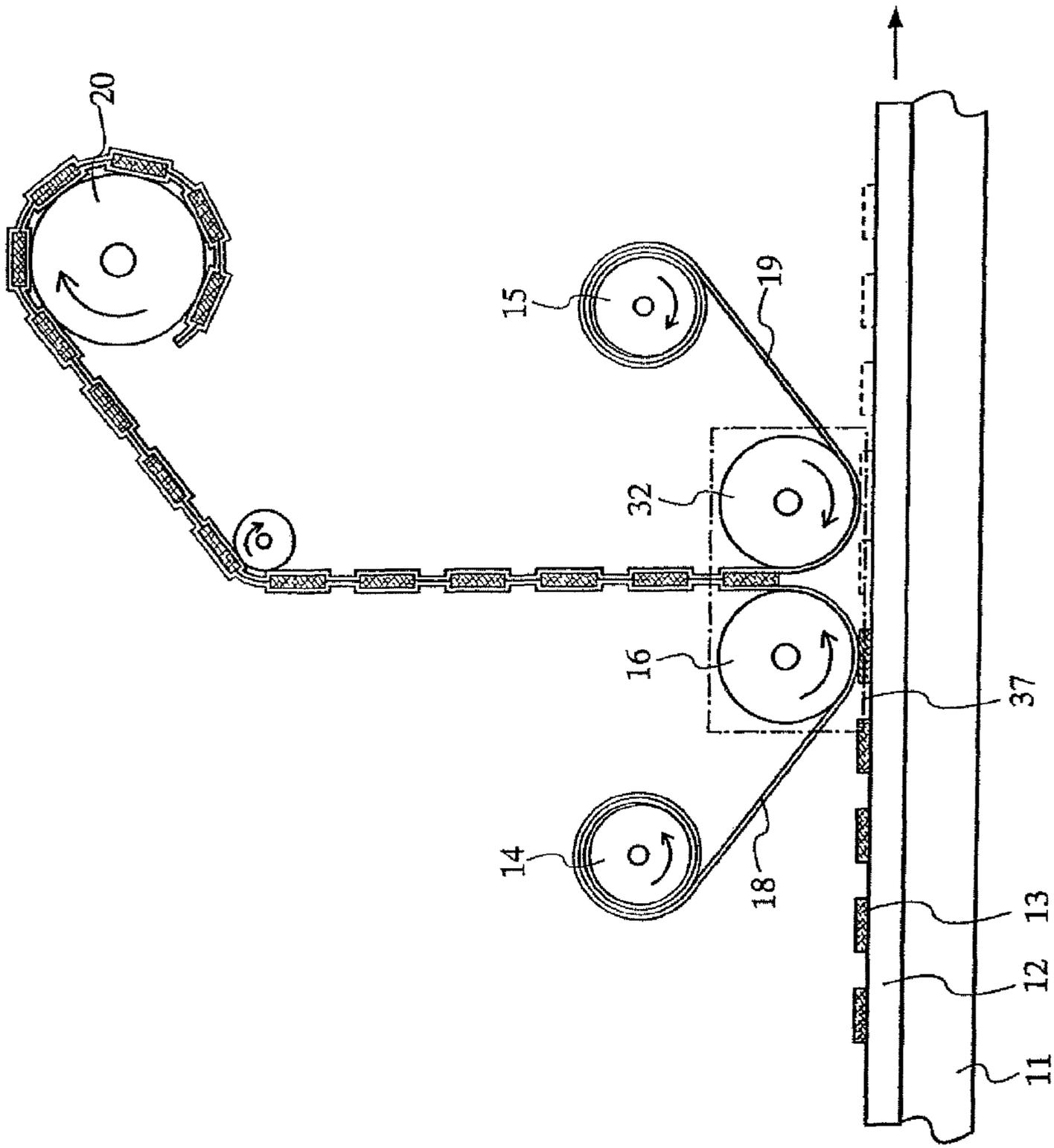


FIG. 2

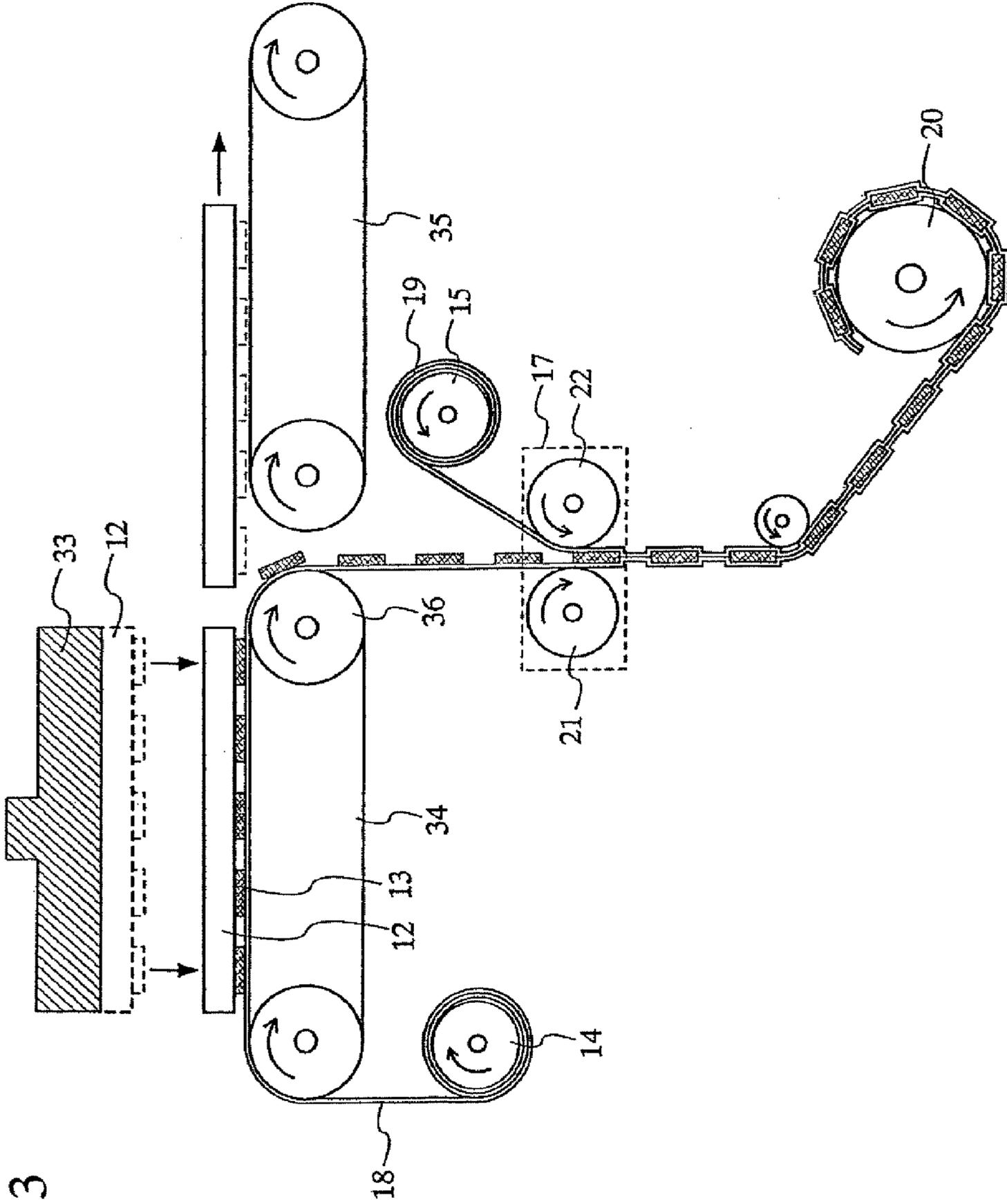


FIG. 3

FIG. 5A

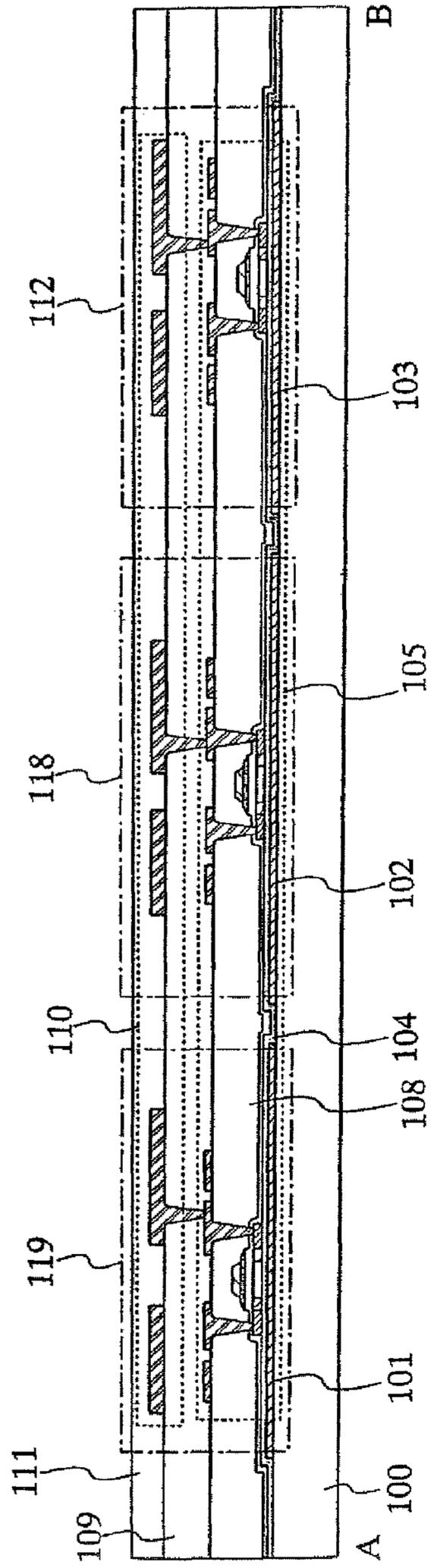


FIG. 5B

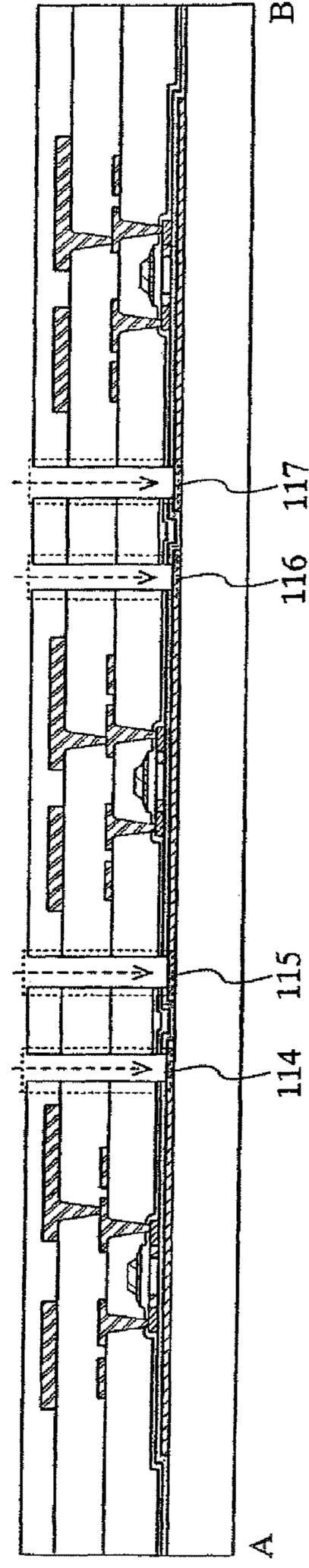


FIG. 6A

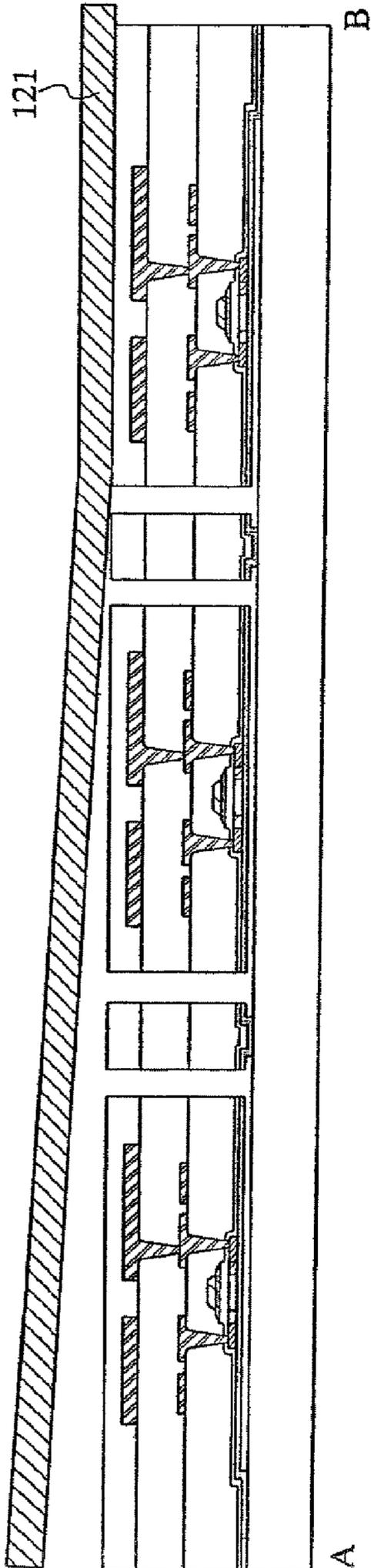


FIG. 6B

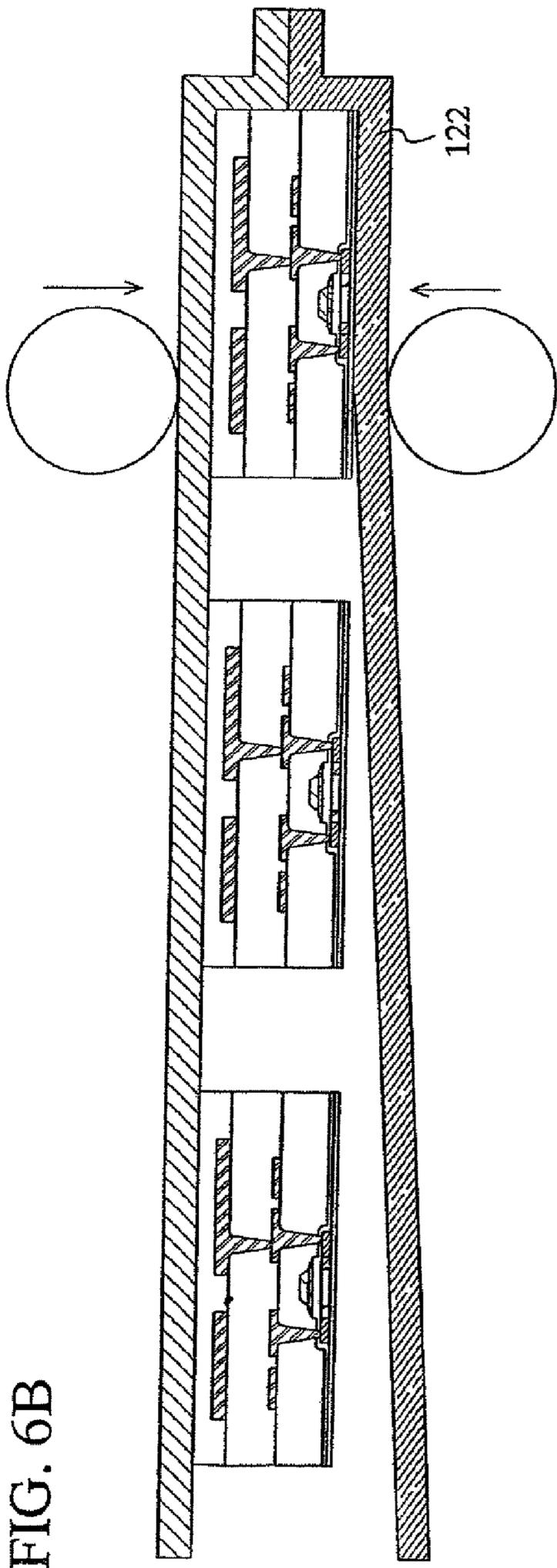


FIG. 7A

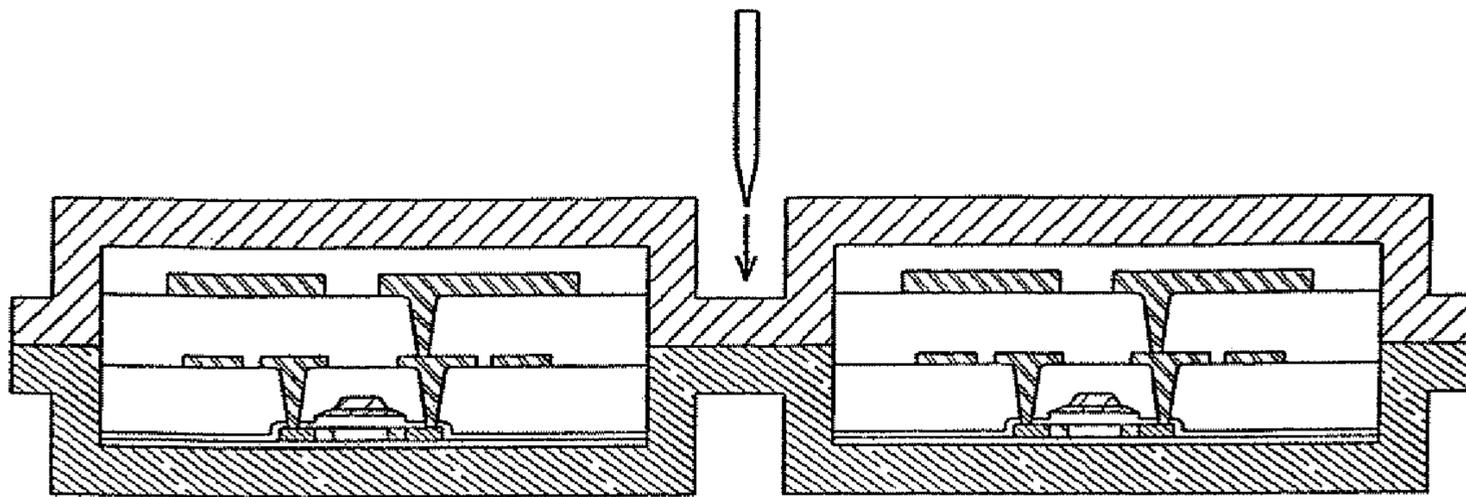


FIG. 7B

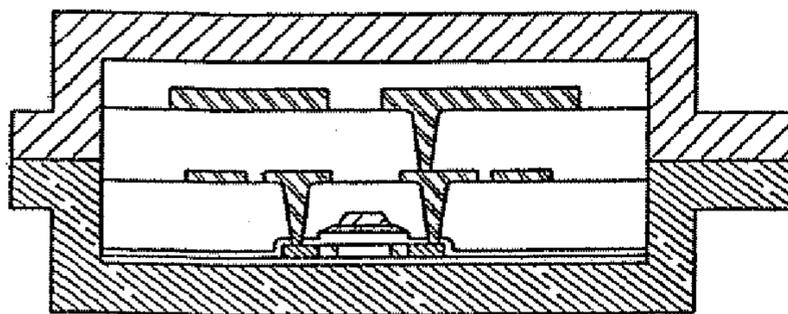


FIG. 8

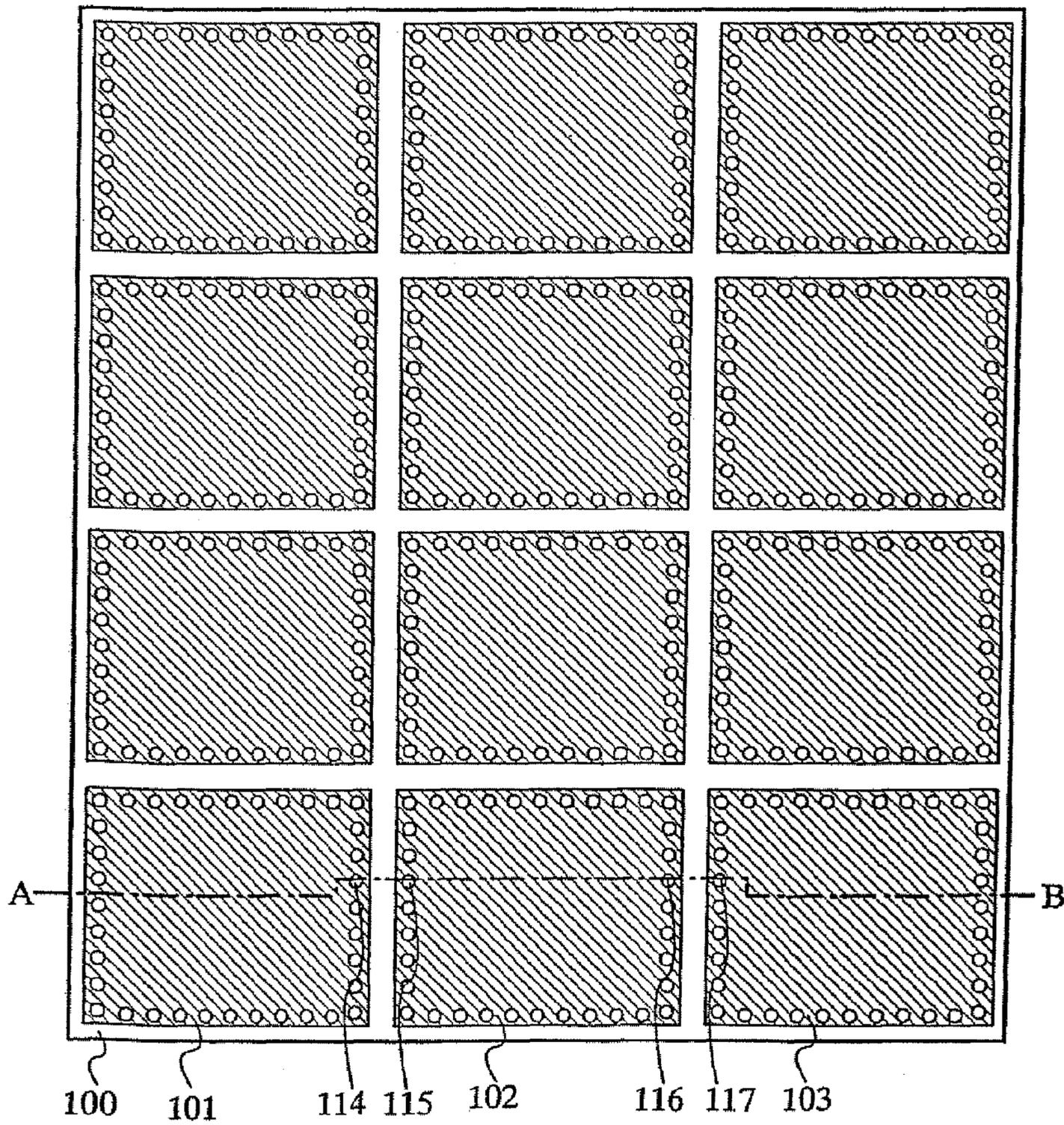


FIG. 9

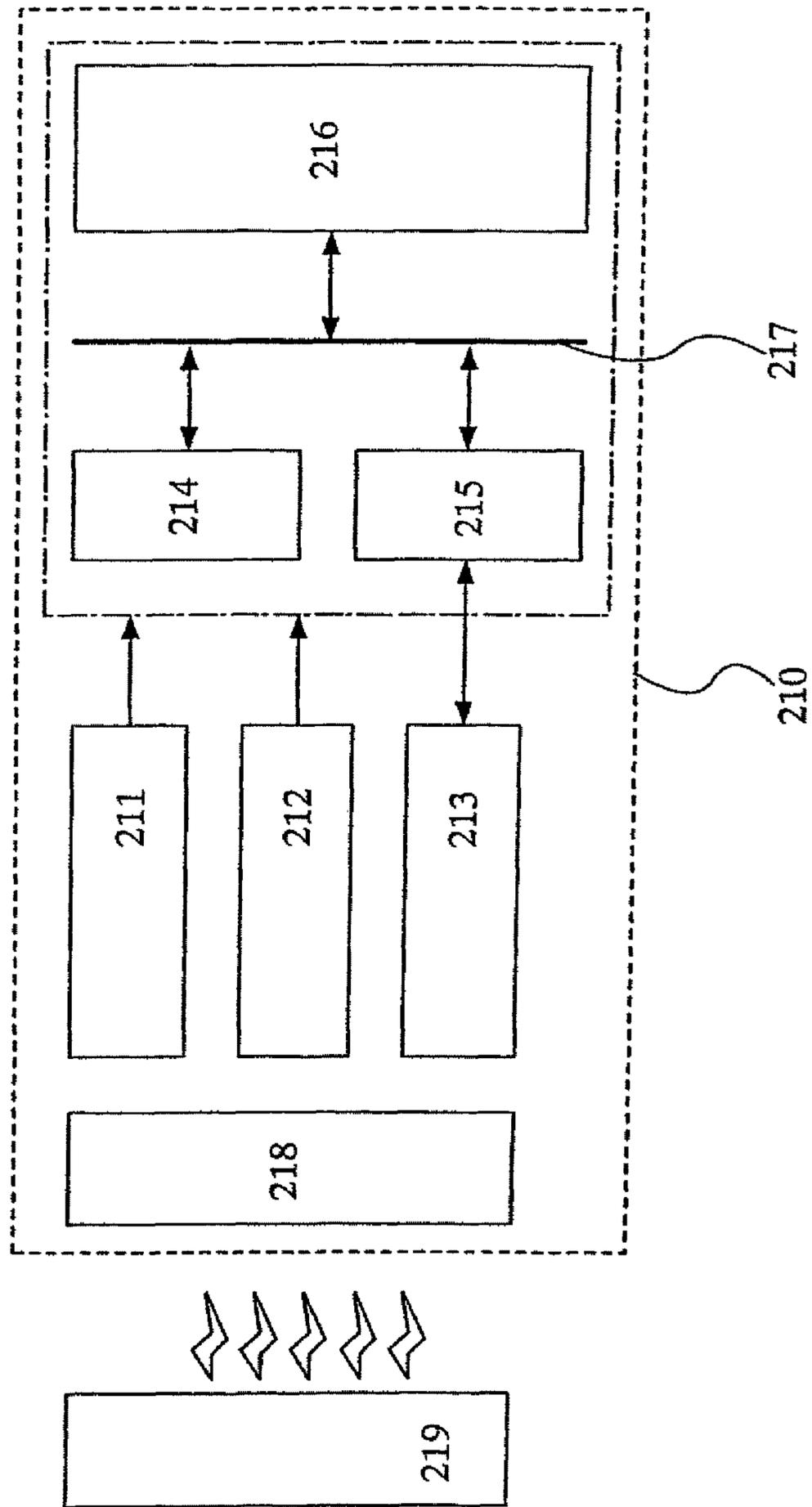


FIG. 10A

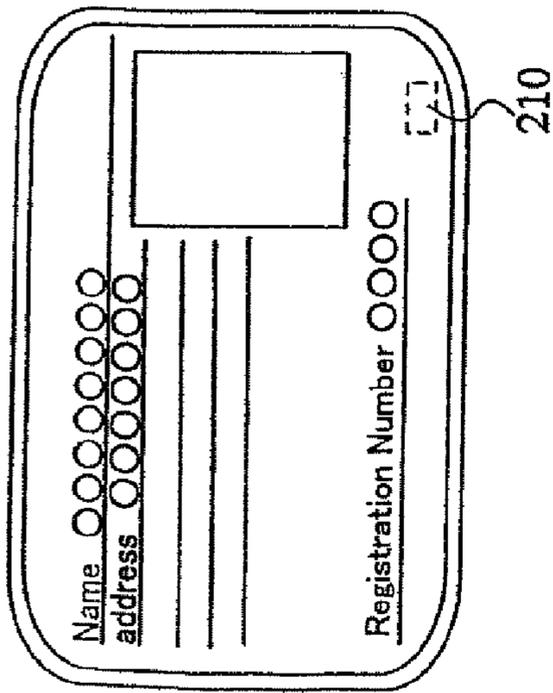


FIG. 10B

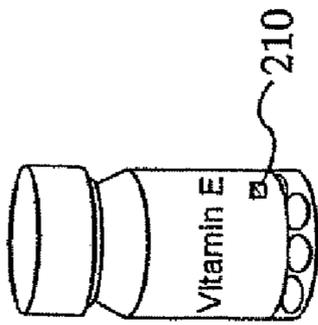


FIG. 10C

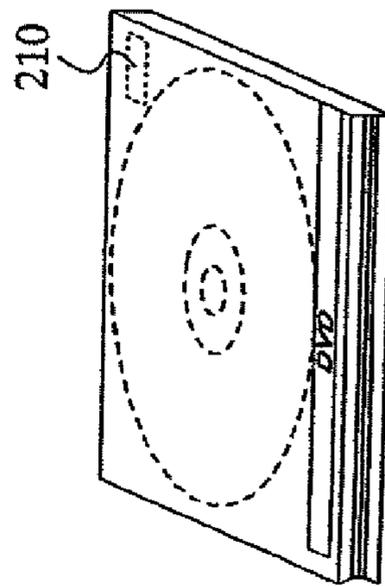


FIG. 10D

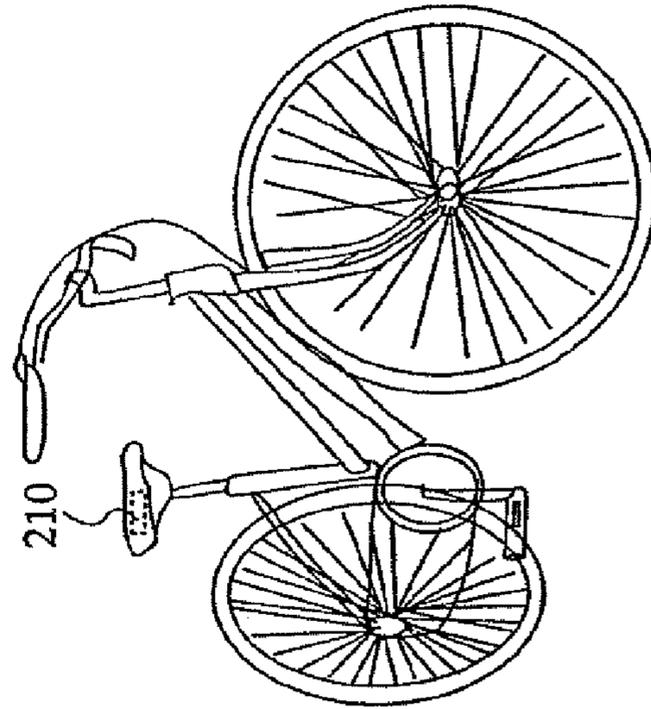


FIG. 10E

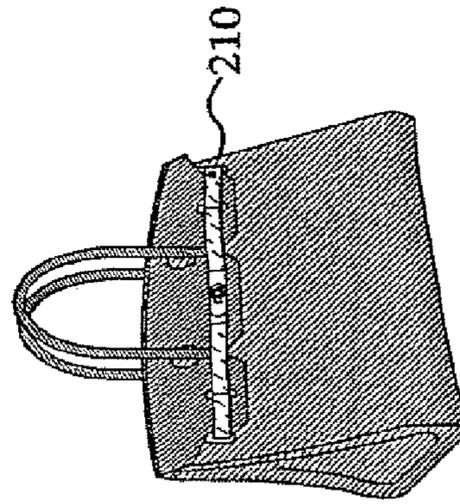


FIG. 11A

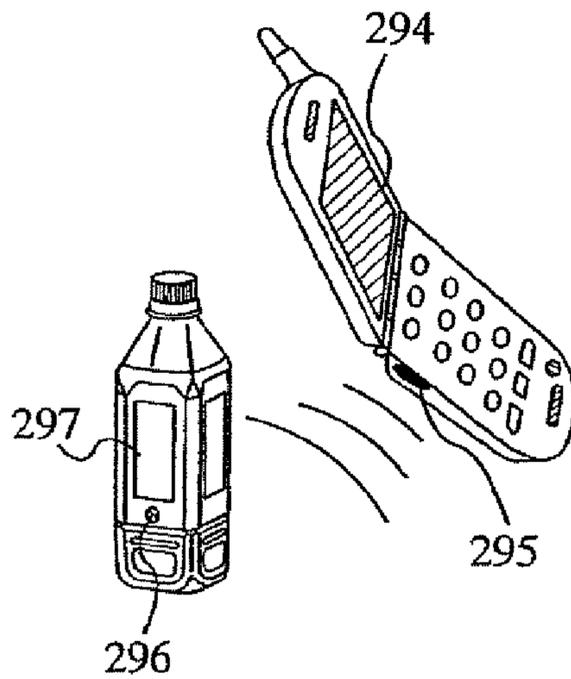
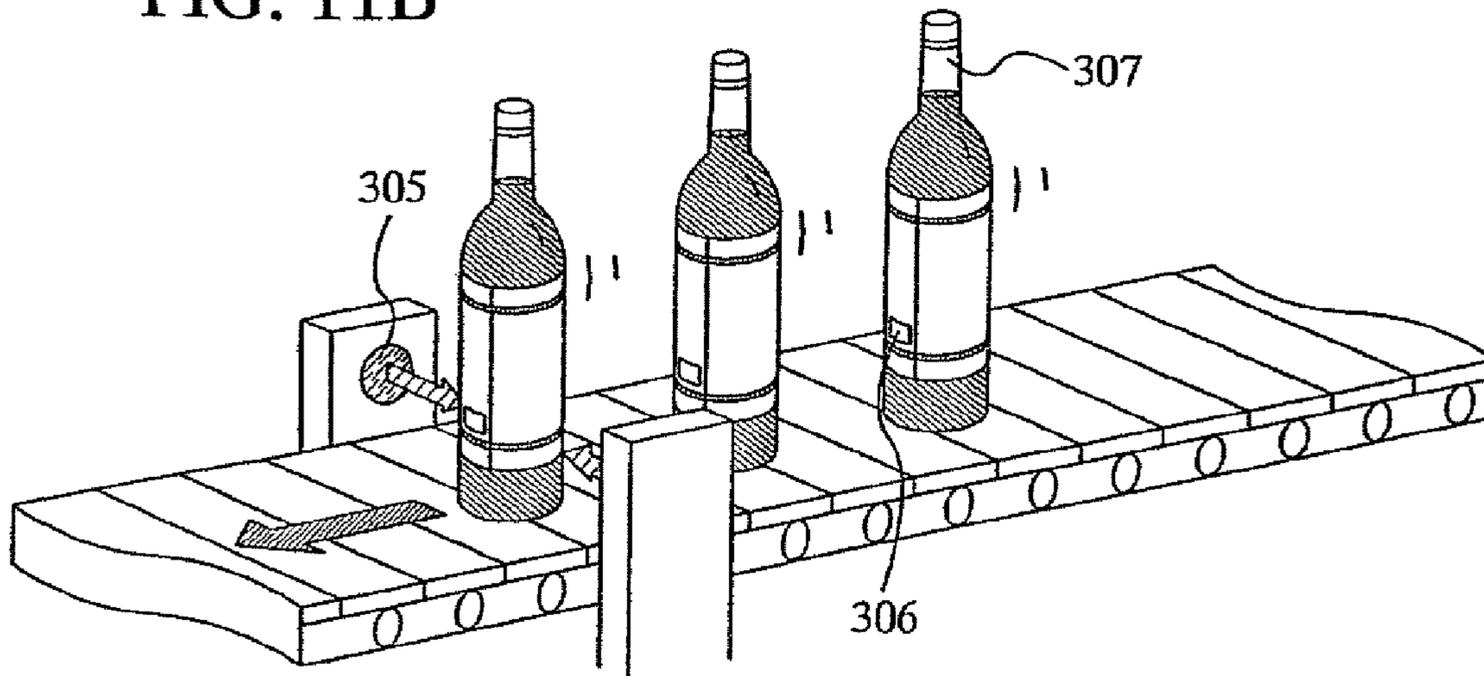


FIG. 11B



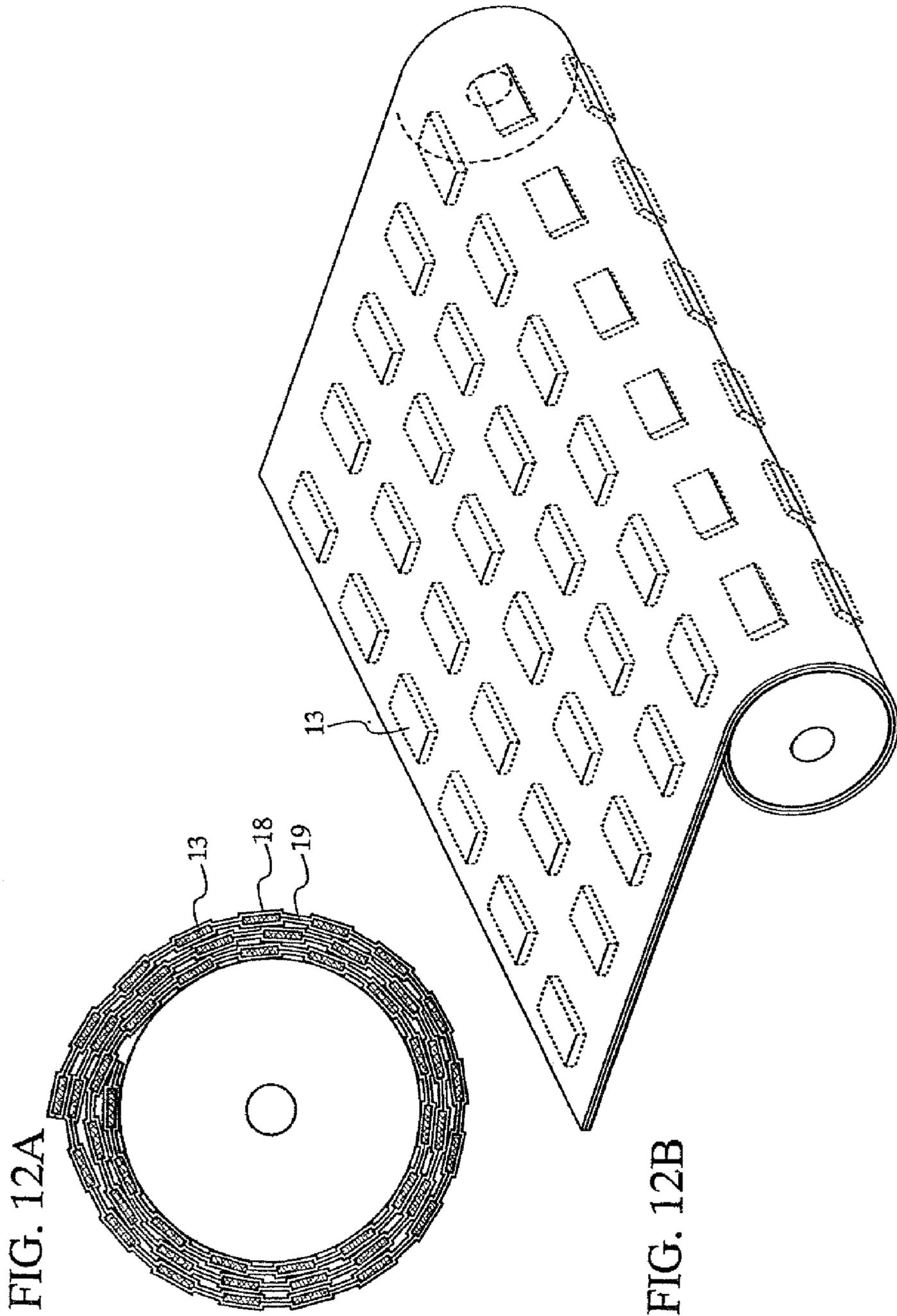
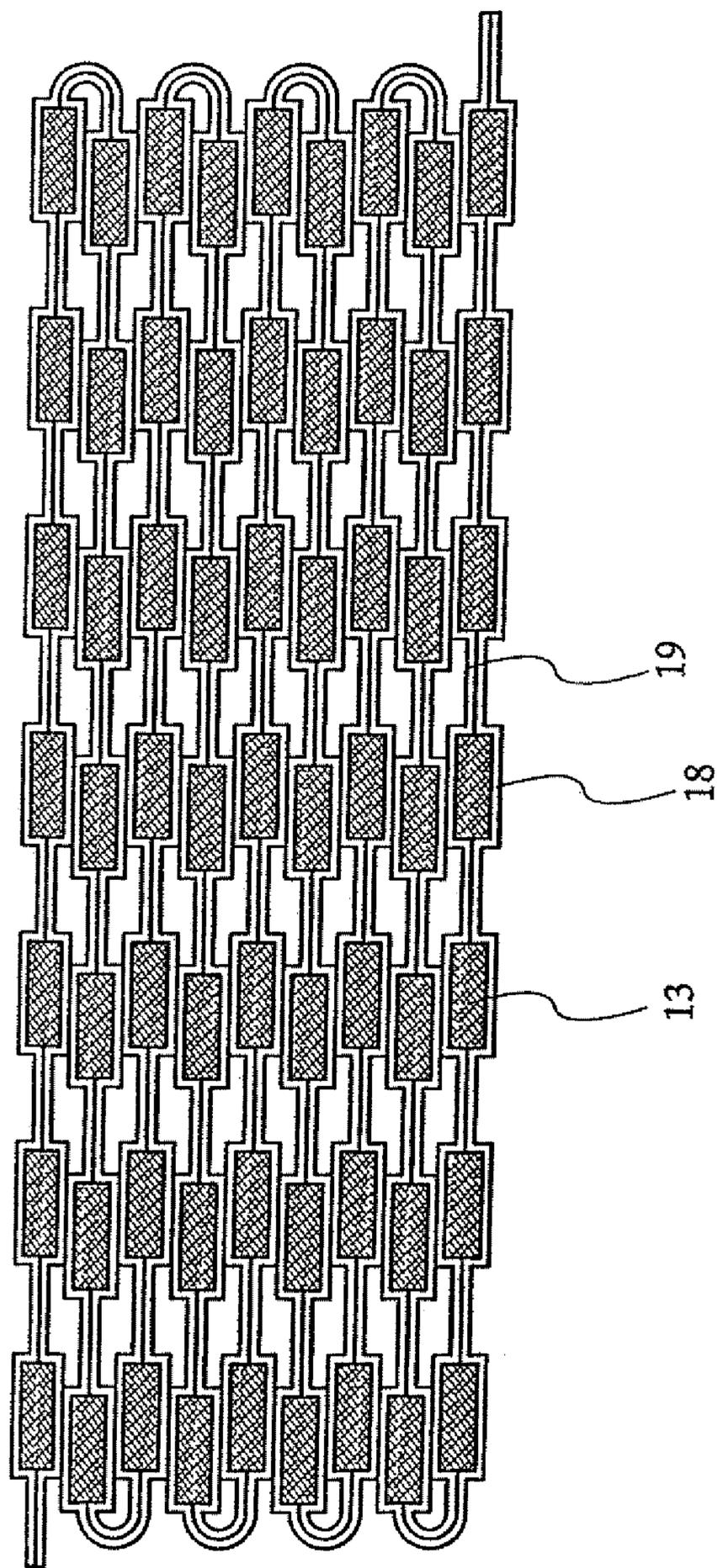


FIG. 13



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LAMINATING SYSTEM

TECHNICAL FIELD

The present invention relates to a laminating system which seals a thin film integrated circuit. The invention further relates to an IC sheet including a plurality of thin film integrated circuits which are sealed. The invention still further relates to a roll of a plurality of thin film integrated circuits which are sealed and wound. Moreover, the invention relates to a method for manufacturing an IC chip in which a thin film integrated circuit is sealed.

BACKGROUND ART

In recent years, a technology of an IC chip using a thin film integrated circuit provided over a glass substrate (also referred to as IC tag, ID tag, RF (Radio Frequency) tag, wireless tag, or electronic tag) has been developed. In such technology, a thin film integrated circuit provided over a glass substrate is required to be separated from the glass substrate, which is a supporting substrate, after the completion. Accordingly, as a technology for separating a thin film integrated circuit which has been provided over a supporting substrate from the supporting substrate; for example, there is a technology in which a release layer containing silicon is provided between a thin film integrated circuit and a supporting substrate and the release layer is removed with the use of a gas containing a halogen fluoride thereby separating the thin film integrated circuit from the supporting substrate (Reference 1: Japanese Patent Laid-Open No. 8-254686).

A plurality of thin film integrated circuits are provided over a glass substrate, and the plurality of thin film integrated circuits are separated individually while the release layer is removed. However, the production efficiency is low in the case of sealing the separated thin film integrated circuits individually. Further, a thin film integrated circuit is thin and lightweight, so that it is difficult to seal the thin film integrated circuit without damage or break.

DISCLOSURE OF INVENTION

In view of the above problem, it is an object of the invention to improve the production efficiency in sealing a thin film integrated circuit and to prevent the damage and break.

Further, as described above, a thin film integrated circuit is broken very easily, and attention is required to handle it even after the sealing step, so that it has been very difficult to ship it without damage and break.

Correspondingly, it is another object of the invention to prevent a thin film integrated circuit from being damaged in shipment and to make the thin film integrated circuit easier to handle.

The invention provides a laminating system in which rollers are used for supplying a substrate for sealing, receiving IC chips, separating, and sealing. The separation, sealing, and reception of a plurality of thin film integrated circuits provided over a substrate can be carried out continuously by rotating the rollers; thus, the production efficiency can be extremely improved. Further, the thin film integrated circuits can be sealed easily since a pair of rollers opposite to each other is used.

A laminating system according to the invention comprises: a transfer means for transferring a first substrate provided with at least one thin film integrated circuit; a first supplying roller on which a second substrate winds; a peeling roller which separates the thin film integrated circuit from the first

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substrate by attaching a first surface of the thin film integrated circuit to the second substrate; a second supplying roller on which a third substrate to be attached to a second surface of the thin film integrated circuit winds; a laminating means which seals the thin film integrated circuit between the second substrate and the third substrate; and a receiving roller on which the sealed thin film integrated circuit winds, wherein the first surface and the second surface of the thin film integrated circuit are opposed to each other.

A laminating system according to the invention comprises: a first supplying roller on which a second substrate winds; a peeling roller which separates at least one thin film integrated circuit provided over a first substrate from the first substrate by attaching a first surface of the thin film integrated circuit to the second substrate; a second supplying roller on which a third substrate to be attached to a second surface of the thin film integrated circuit winds; a laminating means which seals the thin film integrated circuit between the second substrate and the third substrate; and a receiving roller on which the sealed thin film integrated circuit winds, wherein the first surface and the second surface of the thin film integrated circuit are opposed to each other.

A laminating system according to the invention comprises: a transfer means for transferring a first substrate provided with at least one thin film integrated circuit; a first supplying roller on which a second substrate winds; a second supplying roller on which a third substrate winds; a laminating means which separates the thin film integrated circuit from the first substrate by attaching a first surface of the thin film integrated circuit to the second substrate and seals the thin film integrated circuit between the second substrate and the third substrate; and a receiving roller on which the sealed thin film integrated circuit winds, wherein the first surface and the second surface of the thin film integrated circuit are opposed to each other.

A laminating system according to the invention comprises: a first substrate having a surface provided with at least one thin film integrated circuit; a first supplying roller on which a second substrate winds; a fixing/moving means (also referred to as a first substrate control means) which fixes the first substrate so that the surface of the first substrate and the second substrate are opposed to each other and moves the first substrate so that a first surface of the thin film integrated circuit is attached to the second substrate; a peeling means which separates the thin film integrated circuit from the surface of the first substrate by attaching the first surface of the thin film integrated circuit to the second substrate; a second supplying roller on which a surface of a third substrate to be attached to a second surface of the thin film integrated circuit winds; a laminating means which seals the thin film integrated circuit between the second substrate and the third substrate; and a receiving roller on which the sealed thin film integrated circuit winds, wherein the first surface and the second surface of the thin film integrated circuit are opposed to each other.

In a laminating system having any one of the above structures the laminating means comprises a first roller and a second roller which are opposed to each other. At least one of the first roller and the second roller has a heating means. The laminating means seals the thin film integrated circuit by performing at least one of a pressure treatment and a heat treatment while the thin film integrated circuit passes between the first roller and the second roller which are opposed to each other.

Further, the second substrate and the third substrate comprise laminate films. The surface of the second substrate

comprises an adhesive surface. The surface of the third substrate comprises an adhesive surface.

Still further, the invention provides an IC sheet which comprises at least one sealed thin film integrated circuit that is made to have a sheet like shape to be handled easily. An IC sheet according to the invention has a plurality of thin film integrated circuits sealed between a second substrate and a third substrate.

Moreover, the invention provides a roll which comprises at least one sealed thin film integrated circuit that is wound to be handled easily. A roll according to the invention comprises a plurality of thin film integrated circuits which are sealed between a second substrate and a third substrate which are wound.

As to an IC sheet or a roll comprising the above structure, the plurality of thin film integrated circuits each has a thin film transistor and a conductive film which serve as an antenna. The plurality of thin film integrated circuits are arranged regularly. Further, the second substrate and the third substrate comprise laminate films.

A method for manufacturing an IC chip according to the invention comprises the steps of: forming a release layer over a first substrate having an insulating surface, forming at least one of thin film integrated circuit over the first substrate, forming an opening at a boundary of the thin film integrated circuit to expose a part of the release layer, introducing a gas or a liquid containing a halogen fluoride into the opening to remove the release layer, attaching a first surface of the thin film integrated circuit to the second substrate to separate the thin film integrated circuit from the first substrate, attaching a second surface of the thin film integrated circuit to the third substrate, so that the thin film integrated circuit is sealed between the second substrate and the third substrate, wherein the first surface and the second surface of the thin film integrated circuit are opposed to each other.

In a laminating system according to the present invention in which a roller for supplying a substrate for sealing, a roller for winding thin film integrated circuits rollers for separating and sealing the thin film integrated circuits, the separation, sealing, and reception of the plurality of thin film integrated circuits provided over a substrate can be carried out continuously; thus, the production efficiency can be improved and the manufacturing time can be reduced.

Further, a laminating system according to the invention which seals thin film integrated circuits using a pair of rollers which is opposite to each other as laminating means (also referred to as sealing means) can easily seal the thin film integrated circuits.

As to an IC sheet and a roll according to the invention, since thin film integrated circuits have already been sealed; thus, they can be easily handled and the thin film integrated circuits are prevented from being damaged and broken. Further, a great amount of thin film integrated circuits can be easily shipped.

BRIEF DESCRIPTION OF DRAWINGS

FIG. 1 is a figure showing a laminating system according to the invention.

FIG. 2 is a figure showing a laminating system according to the invention.

FIG. 3 is a figure showing a laminating system according to the invention.

FIG. 4 is a figure showing a laminating system according to the invention.

FIGS. 5A and 5B are figures showing a method for manufacturing an IC chip.

FIGS. 6A and 6B are figures showing a method for manufacturing an IC chip.

FIGS. 7A and 7B are figures showing a method for manufacturing an IC chip.

FIG. 8 is a figure showing a method for manufacturing an IC chip.

FIG. 9 is a figure showing an IC chip.

FIGS. 10A to 10E are figures showing usage patterns of IC chips.

FIGS. 11A and 11B are figures showing usage patterns of IC chips.

FIGS. 12A and 12B are figures showing a roll according to the invention.

FIG. 13 is a figure showing an IC sheet according to the invention.

BEST MODE FOR CARRYING OUT THE INVENTION

Embodiment modes and Embodiments of the invention will be described in detail with reference to the drawings. However, it is easily understood by those skilled in the art that the invention is not limited by the following descriptions and various changes may be made in forms and details without departing from the spirit and the scope of the invention. Therefore, the invention should not be limited by descriptions of Embodiment Modes and Embodiments below. The same reference numerals are commonly given to the same components in the construction of the invention, which will be described below.

Embodiment Mode 1

The invention provides a laminating system in which rollers are used for supplying a substrate for sealing, receiving IC chips, separating, and sealing. Major modes of the laminating system will be described with reference to the drawings.

A laminating system according to the invention comprises: a transfer means 11 for transferring a first substrate 12 provided with a plurality of thin film integrated circuits 13, a first supplying roller 14 on which a second substrate 18 winds, a peeling roller 16 which separates the thin film integrated circuits 13 from the first substrate 12, a second supplying roller 15 on which a third substrate 19 winds, a laminating means 17 which seals the thin film integrated circuits 13 between the second substrate 18 and the third substrate 19, and a receiving roller 20 on which the sealed thin film integrated circuits 13 wind (FIG. 1).

In an apparatus shown in FIG. 1, thin film integrated circuits 13 over the first substrate 12 which is transferred by a transfer means 11 is attached to the second substrate 18 which has traveled from the first supplying roller 14 toward the peeling roller 16 to separate the thin film integrated circuits 13 from the first substrate 12. The second substrate 18 to which the thin film integrated circuits 13 are attached travels in the direction of the laminating means 17. The third substrate 19 travels from the second supplying roller 15 toward the laminating means 17. At the laminating means 17, either or both of a pressure treatment and a heat treatment are carried out while the thin film integrated circuits 13 are attached to the third substrate 19. Finally, the thin film integrated circuits 13 which are sealed between the second substrate 18 and the third substrate 19 travel in the direction of the receiving roller 20 to be wound on the receiving roller 20.

In accordance with the above operations, in a laminating system according to the invention, the peeling roller 16, the laminating means 17, and the receiving roller 20 are provided

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so that the thin film integrated circuits **13** which is attached to the second substrate **18** and the third substrate **19** pass them in order. The peeling roller **16** and the receiving roller **20** rotate in directions different from each other. The first supplying roller **14**, the peeling roller **16**, and a roller **21** included in the laminating means **17** are provided so that the second substrate **18** pass them in order. Further, the first supplying roller **14**, the peeling roller **16**, and the roller **21** rotate in the same direction. The second supplying roller **15** and a roller **22** included in the laminating means **17** are provided so that the third substrate **19** pass them in order. The second supplying roller **15** and the roller **22** rotate in the same direction.

The transfer means **11** is for transferring the first substrate **12** provided with a plurality of thin film integrated circuits **13**. For example, the transfer means comprises a conveyer belt, a plurality of rollers, and a robot arm. A robot arm transfers the first substrate **12** itself or transfers a stage provided with the first substrate **12**. The transfer means **11** transfers the first substrate **12** at a predetermined speed in accordance with a speed at which the first supplying roller **14** rotates.

The second substrate **18** is wound on the first supplying roller **14**, and the third substrate **19** is wound on the second supplying roller **15**. The second substrate **18** is made travel toward the peeling roller **16** by rotating the first supplying roller **14** at a predetermined speed. The third substrate **19** is made travel toward the laminating means **17** by rotating the second supplying roller **15** at a predetermined speed. The first supplying roller **14** and the second supplying roller **15** have circular cylinder shape and are formed from a resin material, a metal material, or the like.

The second substrate **18** and the third substrate **19** each correspond to a laminate film, a paper made of a fibrous material, or the like. A laminate film may be made of a material such as polypropylene, polyester, vinyl, polyvinyl fluoride, polyvinyl chloride and the surface may be processed, for example, embossed.

The laminate film corresponding to the second substrate **18** and the third substrate **19** may be made of a material such as polyethylene and ethylene vinyl acetate. The surface may be coated with powders of silicon dioxide (silica). The coating keeps water resistance even under an atmosphere of high temperature and high humidity.

Either or both of a surface of the second substrate **18** and the third substrate **19** may have adhesive surfaces. The adhesive surface is coated with an adhesive such as a thermosetting resin, an ultraviolet curable resin, an epoxy based adhesive, or a resin additive.

Either or both of the second substrate **18** and the third substrate **19** may have light-transmitting properties. Further, either or both of the second substrate **18** and the third substrate **19** may be coated with a conductive material on the surfaces to protect the thin film integrated circuit to be sealed by charging static electricity in either or both of the second substrate **18** and the third substrate **19**.

Either or both of the second substrate **18** and the third substrate **19** may be coated with a conductive material such as a thin film containing carbon as a main component (a diamond-like carbon film) or indium tin oxide (ITO) as a protective film.

The peeling roller **16** is provided to attach a first surface of the thin film integrated circuits **13** to a surface of the second substrate **18** in order to separate the thin film integrated circuits **13** from the first substrate **12**. When the peeling roller **16** rotates, the thin film integrated circuits **13** is attached to the second substrate **18**; thus, the thin film integrated circuits **13** are separated from the first substrate **12**. Consequently, the

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peeling roller **16** is opposed to the side of the first substrate **12** where the thin film integrated circuits **13** are provided.

According to the above structure, the first substrate **12** is moved by the transfer means **11**, and the peeling roller **16** is fixed, however, the invention is not limited thereto. The first substrate **12** may be fixed, and the peeling roller **16** may be moved so that the thin film integrated circuits **13** are separated from the first substrate **12**. The peeling roller **16** has a circular cylinder shape and is formed from a resin material, a metal material, or the like. Preferably, the peeling roller **16** is formed of a soft material.

When the thin film integrated circuits **13** comprising a first surface which is attached to the second substrate **18** reach the laminating means **17**, the laminating means **17** seals the thin film integrated circuits **13** between the second substrate **18** and the third substrate **19** while making the third substrate **19** attach to a second surface of the thin film integrated circuits **13**.

The laminating means **17** comprises the roller **21** and the roller **22** which are opposed to each other. A second surface of the thin film integrated circuits **13** are attached to the third substrate **19** traveling from the second supplying roller **15** toward the roller **22**, and either or both of a pressure treatment and a heat treatment are performed using the roller **21** and the roller **22** while the third substrate **19** passes between the roller **21** and the roller **22**. Through the above steps, the thin film integrated circuits **13** are sealed between the second substrate **18** and the third substrate **19**.

Either or both of the rollers **21** and **22** constituting the laminating means **17** include heating means. The heating means, for example, corresponds to a heating medium such as a heating wire or oil. In the case where a heat treatment is not carried out by the rollers **21** and **22**, the rollers **21** and **22** may not necessarily have a heating means.

The rollers **21** and **22** rotate at a predetermined speed in accordance with the speed at which the peeling roller **16** and the second supplying roller **15** rotate. The rollers **21** and **22** have circular cylinder shape, and are formed from a resin material, a metal material, or the like, preferably, from a soft material.

The receiving roller **20** is a roller which receives the thin film integrated circuits **13** which are sealed by the second substrate **18** and the third substrate **19** by winding them. The receiving roller **20** rotates at a predetermined speed in accordance with the speed at which the rollers **21** and **22** rotate. The receiving roller **20** have circular cylinder shape, and are formed from a resin material, a metal material, or the like, preferably, from a soft material.

Using a system according to the invention, the first supplying roller **14**, the second supplying roller **15**, the peeling roller **16**, the rollers **21** and **22**, and the receiving roller **20** rotate; thus, the plurality of thin film integrated circuits **13** over the first substrate **12** can be peeled, sealed, and received in sequence. Therefore, a system according to the invention can provide high productivity and manufacturing efficiency.

Next, a laminating system having a structure different from the above laminating system will be described with reference to FIG. 2.

A laminating system according to the invention includes a transfer means **11** for transferring a first substrate **12** provided with a plurality of thin film integrated circuits **13**, a first supplying roller **14** on which a second substrate **18** winds, a second supplying roller **15** on which a third substrate **19** winds, a laminating means **17** which separates the thin film integrated circuits **13** from the first substrate **12** and seals the thin film integrated circuits **13** with the second substrate **18**

and the third substrate 19, and a receiving roller 20 on which the plurality of sealed thin film integrated circuits 13 wind (FIG. 2)

Such a structure has a feature that a roller 32 which is opposed to the peeling roller 16 is provided and a laminating means 37 is constituted by the peeling roller 16 and the roller 32. In other words, the peeling roller 16 also serves as a laminating means 37. Either or both of the peeling roller 16 and the roller 32 have heating means.

In a system shown in FIG. 2, a first surface of the thin film integrated circuits 13 is attached to the second substrate 18 by the peeling roller 16 to separate the thin film integrated circuits 13 from the first substrate 12 while a second surface of the thin film integrated circuits 13 is attached to the third substrate 19 by the roller 32. Further, while the thin film integrated circuits 13 passes between the peeling roller 16 and the roller 32, the thin film integrated circuits 13 are sealed between the second substrate 18 and the third substrate 19 by performing either or both of a pressure treatment and a heat treatment.

Accordingly, in the laminating system according to the invention, the laminating means 37 including the peeling roller 16, and the receiving roller 20 are provided so that the thin film integrated circuits 13 which are attached to the second substrate 18 and the third substrate 19 pass them in order. The peeling roller 16 and the receiving roller 20 rotate in different directions from each other. The first supplying roller 14 and the peeling roller 16 included in the laminating means 37 are provided so that the second substrate 18 passes them in order. The first supplying roller 14 and the peeling roller 16 rotate in the same direction. The second supplying roller 15 and the roller 32 included in the laminating means 37 are provided so that the third substrate 19 passes them in order. The second supplying roller 15 and the roller 32 rotate in the same direction.

Using a system according to the invention, the first supplying roller 14, the second supplying roller 15, the peeling roller 16, the roller 32, and the receiving roller 20 rotate; thus, the plurality of thin film integrated circuits 13 over the first substrate 12 can be peeled, sealed, and received in sequence. Therefore, a system according to the invention can provide high productivity and manufacturing efficiency.

Next, a laminating system having a structure different from the above laminating system will be described with reference to FIG. 3.

A laminating system according to the invention includes a fixing/moving means (also referred to as a first substrate control means) 33 which fixes and moves the first substrate 12, a peeling means 36 which separates the thin film integrated circuits 13 from a surface of the first substrate 12, a first supplying roller 14 on which a second substrate 18 winds, a second supplying roller 15 on which a third substrate 19 winds, a laminating means 17 which seals the thin film integrated circuits 13 with the second substrate 18 and the third substrate 19, and a receiving roller 20 on which the sealed thin film integrated circuits 13 wind (FIG. 3). Further, transfer means 34 and 35 are included in addition to the above components. The structure shown in FIG. 3 has an upside-down structure of the structure shown in FIG. 1 and is newly provided with a fixing/moving means 33 and the transfer means 34 and 35.

In this system, the thin film integrated circuits 13 over the first substrate 12 moved by the fixing/moving means 33 are attached to the second substrate 18 which has traveled from the first supplying roller 14 toward the transfer means 34. The thin film integrated circuits 13 are separated from the first substrate 12 by the peeling means 36 included in the transfer

means 34. Further, the second substrate 18, to which the thin film integrated circuits 13 are attached, travels toward the laminating means 17. The third substrate 19 travels from the second supplying roller 15 toward the laminating means 17.

At the laminating means 17, either or both of a pressure treatment and a heat treatment are performed while the thin film integrated circuits 13 are attached to the third substrate 19. Finally, the thin film integrated circuits 13 which are sealed between the second substrate 18 and the third substrate 19 travel toward the receiving roller 20 to wind on the receiving roller 20.

Accordingly, in the laminating system according to the invention, the peeling means 36, the roller 21 included in the laminating means 17, the receiving roller 20 are provided so that the thin film integrated circuits 13 attached to the second substrate 18 and the third substrate 19 pass them in order. The peeling means 36 and the receiving roller 20 rotate in different directions from each other. The first supplying roller 14, the transfer means 34, and the roller 21 included in the laminating means 17 are provided so that the second substrate 18 passes them in order. The first supplying roller 14 and the roller 21 rotate in the same direction. The second supplying roller 15 and the roller 22 included in the laminating means 17 are provided so that the third substrate 19 pass them in order. The second supplying roller 15 and the roller 22 rotate in the same direction.

The fixing/moving means 33 has a function of fixing the first substrate 12 so that a surface of the first substrate 12 where the thin film integrated circuits 13 are provided (hereinafter referred to as a first surface of the first substrate 12) is opposed to the second substrate 18 and a function of moving the first substrate 12 so that the thin film integrated circuits 13 over the first surface of the first substrate 12 are attached to the second substrate 18. The first substrate 12 is moved by a vacuum adsorption method or the like. The first substrate 12 is moved by moving the fixing/moving means 33.

The fixing/moving means 33 may be one which processes the first substrate 12 one by one as shown in the figure, and may have a shape of a polyhedron such as a circular cylinder or a prism. In the case of using one with the shape of a circular cylinder or a prism, the first substrate 12 is fixed to the side surface thereof and the first substrate 12 is moved by rotating the circular cylinder or a prism.

The transfer means 34 transfers the second substrate 18 and the first substrate 12 provided with the plurality of thin film integrated circuits 13. The peeling means 36 disposed on an end of the transfer means 34 attaches the first surface of the thin film integrated circuits 13 to the second substrate 18 to separate the thin film integrated circuits 13 from the first surface of the first substrate 12. In the structure shown in the figure, the peeling means 36 corresponds to a roller. The transfer means 35 transfers the first substrate 12 from which the thin film integrated circuits 13 are peeled.

After the thin film integrated circuits 13 are separated from the first substrate 12, the third substrate 19 is attached to a second surface of the thin film integrated circuits 13 which is opposed to the first surface of the thin film integrated circuits 13 by the laminating means 17 while the thin film integrated circuits 13 are sealed between the second substrate 18 and the third substrate 19 as with the structure of the laminating system in FIG. 1. Subsequently, the thin film integrated circuits 13 are received by the receiving roller 20.

Using a system according to the invention, the transfer means 34 and 35, the first supplying roller 14, the second supplying roller 15, the rollers 21 and 22, and the receiving roller 20 rotate; thus, the plurality of thin film integrated circuits 13 over the first substrate 12 can be peeled, sealed,

and received in sequence. Therefore, a system according to the invention can provide high productivity and manufacturing efficiency.

Next, a general structure of a laminating system will be described with reference to FIG. 4. Here, a general structure of a laminating system having a structure shown in FIG. 1 will be described.

A cassette 23 is a cassette for supplying a substrate, and a first substrate 12 provided with a plurality of thin film integrated circuits 13 are set therein. A cassette 24 is a cassette for receiving a substrate, and the first substrate 12 is to be set therein. A plurality of rollers 25 to 27 are provided as transfer means between the cassette 23 and the cassette 24. The first substrate 12 is transferred when the rollers 25 to 27 rotate. Thereafter, the thin film integrated circuits 13 are peeled and sealed, and the sealed thin film integrated circuits 13 are cut by a cutting means 28. The cutting means 28 may use a dicing system, a scribing system, a laser irradiation apparatus (particularly, a CO₂ laser irradiation apparatus) or the like. The sealed thin film integrated circuits 13 are completed through the above steps.

In the structures shown in FIGS. 1 to 4, the thin film integrated circuits 13 provide over the first substrate 12 include an element group of a plurality of elements and a conductive layer serving as an antenna. However, the invention is not limited thereto.

The thin film integrated circuits 13 provided over the first substrate 12 may only include an element group. A conductive layer serving as an antenna is attached to the second substrate 18 or the third substrate 19, and a plurality of elements included in the thin film integrated circuits 13 may be connected to the conductive layer in attaching the thin film integrated circuits 13 to the second substrate 18 or the third substrate 19.

Embodiment Mode 2

A structure of an IC sheet (also referred to as an IC film, a sheet element, and a film element) according to the invention will be described. An IC sheet according to the invention is a second substrate 18 and a third substrate 19 which are attached to each of a plurality of thin film integrated circuits 13 from both surfaces which are wound in a roller shape (see cross-sectional view of an IC sheet in FIG. 13). Each of the plurality of thin film integrated circuits 13 has a plurality of elements and a conductive layer serving as an antenna. Each of the plurality of thin film integrated circuits 13 are arranged regularly.

As described above, sheet-like IC sheets comprising a plurality of thin film integrated circuits 13 sealed with a pair of substrates for sealing are easy to be shipped. In particular, it is advantageous in the shipment of a large amount of thin film integrated circuits 13. Further, the plurality of thin film integrated circuits 13 are difficult to be handled when each of them are divided; however, an IC sheet provided by the invention has a sheet shape, so that it is easy to handle and the break and damage of the thin film integrated circuits 13 can be prevented.

Embodiment Mode 3

A structure of a roll (also referred to as wound element, roll body or the like) according to the invention will be described. A roll according to the invention winds a substrate, more specifically, a second substrate 18 and a third substrate 19 which seal each of a plurality of thin film integrated circuits 13 are wound in a roll shape (see a cross-sectional view of a

roll in FIG. 12A and a perspective view of a roll in FIG. 12B). Each of the plurality of thin film integrated circuits 13 has a plurality of elements and a conductive film as an antenna. The plurality of thin film integrated circuits 13 are arranged regularly.

As described above, a roll which is a plurality of thin film integrated circuits 13 sealed with a pair of substrates are wound can easily be shipped. In particular, it is advantageous in the shipment of a large amount of thin film integrated circuits 13. Further, the plurality of thin film integrated circuits 13 are difficult to be handled when each of them are divided; however, a roll provided by the invention is wound, so that it is easy to handle and the break and damage of the thin film integrated circuits 13 can be prevented.

Embodiment Mode 4

A method for manufacturing an IC chip according to the invention will be described with reference to the drawings.

First, release layers 101 to 103 are formed over a first substrate 100 having an insulating surface (FIG. 5A). The first substrate 100 having an insulating surface corresponds to a glass substrate, a quartz substrate, a plastic substrate, a resin substrate formed of a flexible synthetic resin such as acrylic, a metal substrate, or a silicon substrate. Note that in the case where a silicon substrate is used, a release layer is not required to be provided.

The release layers 101 to 103 are layers containing silicon which are formed by sputtering, plasma CVD, or the like. A layer containing silicon corresponds to an amorphous semiconductor layer, a semiamorphous semiconductor layer in which an amorphous state and a crystalline state are mixed, or a crystalline semiconductor layer.

The release layers 101 to 103 are each formed of a layer of an element selected from tungsten (W), molybdenum (Mo), titanium (Ti), tantalum (Ta), niobium (Nb), nickel (Ni), cobalt (Co), zirconium (Zr), zinc (Zn), ruthenium (Ru), rhodium (Rh), palladium (Pd), osmium (Os), iridium (Ir), and silicon (Si), an alloy material or a compound material containing the element as a main component which is formed by a known method (such as sputtering or plasma CVD). The release layers each may have a single layer structure or a layered structure.

The release layers 101 to 103 are formed selectively over the first substrate 100. A top view thereof is shown in FIG. 8. FIGS. 5A and 5B each show a cross-sectional view taken along line A-B in FIG. 8. Such selective formation is carried out in order to prevent dispersion of a plurality of thin film integrated circuits 112, 118, and 119 provided over the release layers 101 to 103 after removing the release layers 101 to 103.

Next, a base insulating film 104 is formed over the release layer 101 to 103. Subsequently, an element group 105 is formed over the insulating layer 104. The element group 105 includes a thin film transistor; a capacitor, a resistor, a diode or a plurality of the respective elements. Next, an insulating film 108 is formed to cover the element group 105 and an insulating film 109 is formed over the insulating film 108. A conductive layer 110 serving as an antenna is formed over the insulating film 109. Further, an insulating film 111 serving as a protective film is formed over the conductive layer 110. Through the above steps, the thin film integrated circuits 112, 118, and 119 comprising the element group 105 and the conductive layer 110 are completed.

The insulating films 108, 109, and 111 are formed from an organic material or an inorganic material. Polyimide, acrylic, polyamide, siloxane, epoxy, or the like can be used as an

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organic material. Siloxane includes a skeleton formed from a bond of silicon (Si) and oxygen (O), and an organic group containing hydrogen (for example, an alkyl group or an aromatic hydrocarbon), a fluoro group or an organic group containing hydrogen and a fluoro group is used for a substituent. Silicon oxide, silicon nitride, silicon oxynitride, silicon nitride oxide, or the like is used for an inorganic material.

In stead of selectively forming the release layers **101** to **103**, the insulating film **111** may have thick thickness for preventing the thin film integrated circuits **112**, **118**, and **119** from scattering. When the thickness of the insulating film **111** is thicker than normal, the thin film integrated circuits **112**, **118**, and **119** can be prevented from scattering, owing to the weight of the insulating film **111**.

Next, openings **114** to **117** are respectively formed between the thin film integrated circuits **112**, **118**, and **119** so as to expose the release layers **101** to **103** (FIG. 5B). The openings **114** to **117** are formed by etching using a mask, dicing or the like.

Subsequently, an etchant for removing the release layers **101** to **103** is introduced into the openings **114** to **117** to make them gradually recede, thereby removing the release layers **101** to **103** (FIG. 6A). A gas or a liquid containing halogen fluoride is used as the etchant. For example, chlorine trifluoride (ClF₃) is used as a halogen fluoride.

Alternatively, nitrogen trifluoride (NF₃), bromine trifluoride (BrF₃), or hydrogen fluoride (HF) may be used as a halogen fluoride. Note that hydrogen fluoride is used in the case of forming a layer containing silicon as a release layer.

Further, as described above, since the release layers **101** to **103** are selectively formed here; thus, a part of the insulating film **104** is in contact with the first substrate **100** after removing the release layers **101** to **103**. Therefore, the thin film integrated circuits **112**, **118**, and **119** can be prevented from scattering.

Next a first surface of each of the thin film integrated circuit **112**, **118**, and **119** is attached to a second substrate **121**. Correspondingly, the thin film integrated circuits **112**, **118**, and **119** are separated from the first substrate **100**.

In the above steps, a part of the insulating film **104** remains over the first substrate **100**; however, the invention is not limited thereto. In the case where the adhesion between the first substrate **100** and the insulating film **104** is low, the insulating film **104** would completely separated from the first substrate **100** by carrying out the above steps.

Next, a second surface of the thin film integrated circuits **112**, **118**, and **119** are attached to a third substrate **122** and the thin film integrated circuits **112**, **118**, and **119** are sealed between the second substrate **121** and the third substrate **122** (FIG. 6B). Thus, the thin film integrated circuits **112**, **118**, and **119** are sealed with the second substrate **121** and the third substrate **122**.

A part of each of the second substrate **121** and the third substrate **122** between the respective thin film integrated circuits **112**, **118**, and **119** is cut by dicing, scribing, or laser cutting. Thus, a sealed IC chip is completed (FIGS. 7A and 7B).

An IC chip sealed through the above steps has the size of 5 mm square (25 mm²) or less, preferably, 0.3 mm square (0.09 mm²) to 4 mm square (16 mm²).

Since a thin film integrated circuit formed over an insulating substrate is used for an IC chip according to the invention without using a silicon substrate; therefore, the IC chip has less limitations in the shape of the mother substrate compared with a chip formed from a circular silicon wafer. That increases the productivity of IC chips and makes it possible to mass-produce the IC chips. Consequently, cost of IC chips

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can be reduced. Further, a semiconductor film with a thickness of 0.2 μm or less, typically, 40 nm to 170 nm, preferably, 50 nm to 150 nm is used for an IC chip according to the invention; thus, the IC chips are very thin compared with a chip formed from a silicon substrate. As a result, the presence of a thin film integrated circuit is hardly noticed even when it is applied to an article, which leads to protection against falsification. Further, an IC chip according to the invention can receive signals with high sensitivity without electromagnetic wave absorption compared with an IC chip formed from a silicon substrate. In the case where a silicon substrate is not used, the thin film integrated circuit can have light-transmitting properties. Therefore, the IC chip according to the invention can be applied to various articles, for example, it can be mounted on a printed surface without spoiling the design. This embodiment mode can be freely combined with any one of the above embodiment modes.

Embodiment 1

An IC chip formed with the use of a laminating system according to the invention includes a plurality of elements, and a conductive layer serving as an antenna. The plurality of elements correspond to a thin film transistor, a capacitor, a resistor, and a diode, for example.

An IC chip **210** has a function of communicating data without contact, and constitutes a variety of circuits. For example, a power circuit **211**, a clock generator circuit **212**, a data demodulation/modulation circuit **213**, a control circuit **214** (corresponding to a CPU or an MPU, for example), an interface circuit **215**, a memory **216**, a data bus **217**, an antenna (also referred to as an antenna coil) **218**, and the like are provided (FIG. 9).

The power circuit **211** is a circuit which generates a variety of power source which is to be supplied to the above respective circuits based on an AC signal inputted from the antenna **218**. The clock generator circuit **212** is a circuit for generating various clocks to be supplied to the above respective circuits based on an AC signal inputted from the antenna **218**. The demodulation/modulation circuit **213** has a function of demodulating/modulating data in communication with a reader/writer **219**. The control means **214**, for example, corresponds to a central processing unit (CPU), a micro processor unit (MPU), or the like and has a function of controlling other circuits. The antenna **218** has a function of transmitting and receiving electromagnetic wave. The reader/writer **219** controls processes regarding communication with a thin film integrated circuit, control of the thin film integrated circuit, and data of the thin film integrated circuit.

The structure of a circuit constituted by thin film integrated circuits is not limited to the above structure. For example, a structure with another component such as a limiter circuit for source voltage or a hardware dedicated for cryptographic processing may be used.

Embodiment 2

An IC chip manufactured using a laminating system according to the invention is in wide use. For example, IC chips **210** can be used in paper money, coin, securities, bearer bonds, a certificate (such as a driver's license, a resident's card (FIG. 10A)), a packing case (such as a wrapper, or a bottle (FIG. 10B)), a storage medium (such as a DVD, a video tape (FIG. 10C)), a vehicle (such as a bicycle (FIG. 10D)), belongings (such as a bag, glasses (FIG. 10E)), food, clothing, commodities, and electronics. Electronics include a liq-

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uid crystal display device, an EL display device, a television device (also referred to as TV or a television receiver), and a cellular phone.

An IC chip is fixed to an article by attaching it onto the surface of the article, embedding it in the article, or the like. For example, an IC chip may be embedded in paper of a book, or in an organic resin of a package formed of an organic resin. Paper money, coin, securities, bearer bonds, a certificate, or the like may be provided with an IC chip, so that forgery can be prevented. Further, a packing case, a storage medium, belongings, foods, commodities, and electronics may be provided with IC chips, so that efficiency of an inspection system, a system for a rental shop, or the like can be improved. Vehicles may be provided with IC chips, so that forgery or robbery can be prevented.

Further, IC chips may be applied to a system of commodity management and commodity distribution, so that the functionality of the system can be improved. For example, a side surface of a portable terminal including a display area **294** is provided with a reader/writer **295**, and a side surface of an article **297** is provided with an IC chip **296** (FIG. 11A). In this case, when the IC chip **296** is held to the reader/writer **295**, information of the article **297** such as the raw materials, the place of origin, the history of distribution, or the like are displayed on the display area **294**. As an alternative, a reader/writer **305** can be provided at the side of a conveyer belt (FIG. 11). In this case, an article **397** can be easily checked using an IC chip **306** provided on a side surface of the article **397**.

EXPLANATION OF REFERENCE

11: a transfer means, **12**: a first substrate, **13**: a thin film integrated circuit, **14**: a first supplying roller, **15**: a second supplying roller, **16**: a peeling roller, **17**: a laminating means, **18**: a second substrate, **19**: a third substrate, **20**: a receiving roller, **21**: a roller, **22**: a roller, **23**: cassette, **24**: cassette, **25**: a roller, **26**: a roller, **27**: a roller, **28**: cutting means, **32**: a roller, **33**: a fixing moving means, **34**: a transfer means, **35**: a transfer means, **36**: a peeling means, **37**: laminating means, **100**: a first substrate, **101**: release layer, **102**: release layer, **103**: release layer, **104**: an insulating film, **105**: an element group, **108**: an insulating film, **109**: an insulating film **110**: conductive layer, **111**: an insulating film, **112**: a thin film integrated circuit, **114**: an opening, **115**: an opening, **116**: an opening, **117**: an opening, **118**: a thin film integrated circuit, **119**: a thin film integrated circuit, **121**: a second substrate, **122**: a third substrate, **210**: an IC chip, **211**: a power circuit, **212**: a clock generator circuit, **213**: a data recovery/modulation circuit, **214**: a control circuit, **215**: an interface circuit, **216**: a memory, **217**: data bus, **218**: an antenna, **219**: a reader/writer, **294**: a display area, **295**: a reader/writer, **296**: an IC chip, **297**: an article, **305**: a reader/writer, **307**: an IC chip, **397**: an article

What is claimed is:

1. A semiconductor device comprising:

an integrated circuit disposed between a first substrate and a second substrate, the integrated circuit comprising:

a first insulating film;

a thin film transistor over the first insulating film;

a conductive layer over the thin film transistor; and

a second insulating film over the thin film transistor;

wherein the integrated circuit is sealed with the first and

the second substrate and the first substrate is in direct

contact with the second substrate at a region outside

the integrated circuit,

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wherein either or both of the first substrate and the second substrate are coated with a conductive material.

2. The semiconductor device according to claim **1**, wherein the second insulating film includes a material selected from the group consisting of silicon oxide, silicon nitride, silicon oxynitride, and silicon nitride oxide.

3. The semiconductor device according to claim **1**, wherein each of the first substrate and the second substrate comprises a resin.

4. The semiconductor device according to claim **1**, wherein each of the first substrate and the second substrate includes a laminate film or a paper made of a fibrous material.

5. The semiconductor device according to claim **1**, wherein the conductive layer is an antenna.

6. A semiconductor device comprising:

an integrated circuit disposed between a first substrate and a second substrate, the integrated circuit comprising:

a first insulating film;

a thin film transistor over the first insulating film;

a conductive layer over the thin film transistor; and

a second insulating film over the thin film transistor;

wherein the integrated circuit is sealed with the first and

the second substrate and the first substrate is in direct

contact with the second substrate at an edge portion of

the first substrate and the second substrate,

wherein either or both of the first substrate and the second substrate are coated with a conductive material.

7. The semiconductor device according to claim **6**, wherein the second insulating film includes a material selected from the group consisting of silicon oxide, silicon nitride, silicon oxynitride, and silicon nitride oxide.

8. The semiconductor device according to claim **6**, wherein each of the first substrate and the second substrate comprises a resin.

9. The semiconductor device according to claim **6**, wherein each of the first substrate and the second substrate includes a laminate film or a paper made of a fibrous material.

10. The semiconductor device according to claim **6**, wherein the conductive layer is an antenna.

11. A semiconductor device comprising:

an integrated circuit disposed between a first substrate and a second substrate, the integrated circuit comprising:

a first insulating film;

an element group including a thin film transistor and a capacitor, over the first insulating film;

a second insulating film covering the element group;

a first conductive layer electrically connected to the element group, over the second insulating film; and

a third insulating film over the first conductive layer; and

a second conductive layer attaching to the first substrate or the second substrate,

wherein the integrated circuit is sealed with the first and the

second substrate and the first substrate is in contact with

the second substrate at a region outside the integrated

circuit, and

wherein the second conductive layer is an antenna and electrically connected to the first conductive layer.

12. The semiconductor device according to claim **11**, wherein the third insulating film includes a material selected from the group consisting of silicon oxide, silicon nitride, silicon oxynitride, and silicon nitride oxide.

13. The semiconductor device according to claim **11**, wherein each of the first substrate and the second substrate comprises a resin.

14. The semiconductor device according to claim **11**, wherein each of the first substrate and the second substrate includes a laminate film or a paper made of a fibrous material.

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15. The semiconductor device according to claim 11, wherein either or both of the first substrate and the second substrate are coated with a conductive material.

16. A semiconductor device comprising:

an integrated circuit disposed between a first substrate and a second substrate, the integrated circuit comprising:

a first insulating film;

an element group including a thin film transistor and a capacitor, over the first insulating film;

a second insulating film covering the element group;

a first conductive layer electrically connected to the element group, over the second insulating film; and

a third insulating film over the first conductive layer; and

a second conductive layer attaching to the first substrate or the second substrate,

wherein the integrated circuit is sealed with the first and the second substrate and the first substrate is in contact with

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the second substrate at an edge portion of the first substrate and the second substrate, and

wherein the second conductive layer is an antenna and electrically connected to the first conductive layer.

17. The semiconductor device according to claim 16, wherein the third insulating film includes a material selected from the group consisting of silicon oxide, silicon nitride, silicon oxynitride, and silicon nitride oxide.

18. The semiconductor device according to claim 16, wherein each of the first substrate and the second substrate comprises a resin.

19. The semiconductor device according to claim 16, wherein each of the first substrate and the second substrate includes a laminate film or a paper made of a fibrous material.

20. The semiconductor device according to claim 16, wherein either or both of the first substrate and the second substrate are coated with a conductive material.

* * * * *

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 8,698,156 B2
APPLICATION NO. : 12/403988
DATED : April 15, 2014
INVENTOR(S) : Ryosuke Watanabe et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification

Column 4, line 22, change “in may be art” to --in the art--.

Column 4, line 59, change “beat” to --heat--.

Column 6, line 5, change “fixed,” to --fixed;--.

Column 6, line 25, change “beat” to --heat--.

Column 9, line 46, change “antenna Each” to --antenna. Each--.

Column 10, line 40, change “component which” to --component, which--.

Column 10, line 55, change “transistor;” to --transistor,--.

Column 11, line 36, change “Next a” to --Next, a--.

Column 11, line 57, change “713” to --7B--.

Column 12, line 8, change “falsification Further” to --falsification. Further--.

Column 13, lines 26-27, change “FIG. 11” to --FIG. 11B--.

Signed and Sealed this
Twelfth Day of August, 2014



Michelle K. Lee
Deputy Director of the United States Patent and Trademark Office