

[54] BICYCLE WATCH - DUAL MODE CIRCUIT

[75] Inventor: Tom Thinesen, Sunnyvale, Calif.

[73] Assignee: Timex Corporation, Waterbury, Conn.

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[52] U.S. Cl. 368/10; 368/110; 324/160; 340/432

[58] Field of Search 368/107-113, 368/10; 324/166, 160-161; 340/134, 670; 364/443

[56] References Cited

U.S. PATENT DOCUMENTS

3,803,834	4/1974	Reese	58/152 R
3,816,730	6/1974	Yamamoto	235/156
3,955,355	5/1976	Luce	58/152 R
3,999,050	12/1976	Pitroda	368/10
4,022,014	5/1977	Lowdenslager	58/23
4,071,892	4/1978	Genzling	364/424
4,092,822	6/1978	Droz et al.	368/187
4,320,478	3/1982	Daniels et al.	368/69
4,334,190	6/1982	Sochaczewski	324/171
4,633,216	12/1986	Tsuyama	340/134
4,636,769	1/1987	Tsuyama	340/134
4,642,606	2/1987	Tsuyama	340/134
4,780,864	10/1988	Houlihan	368/10

FOREIGN PATENT DOCUMENTS

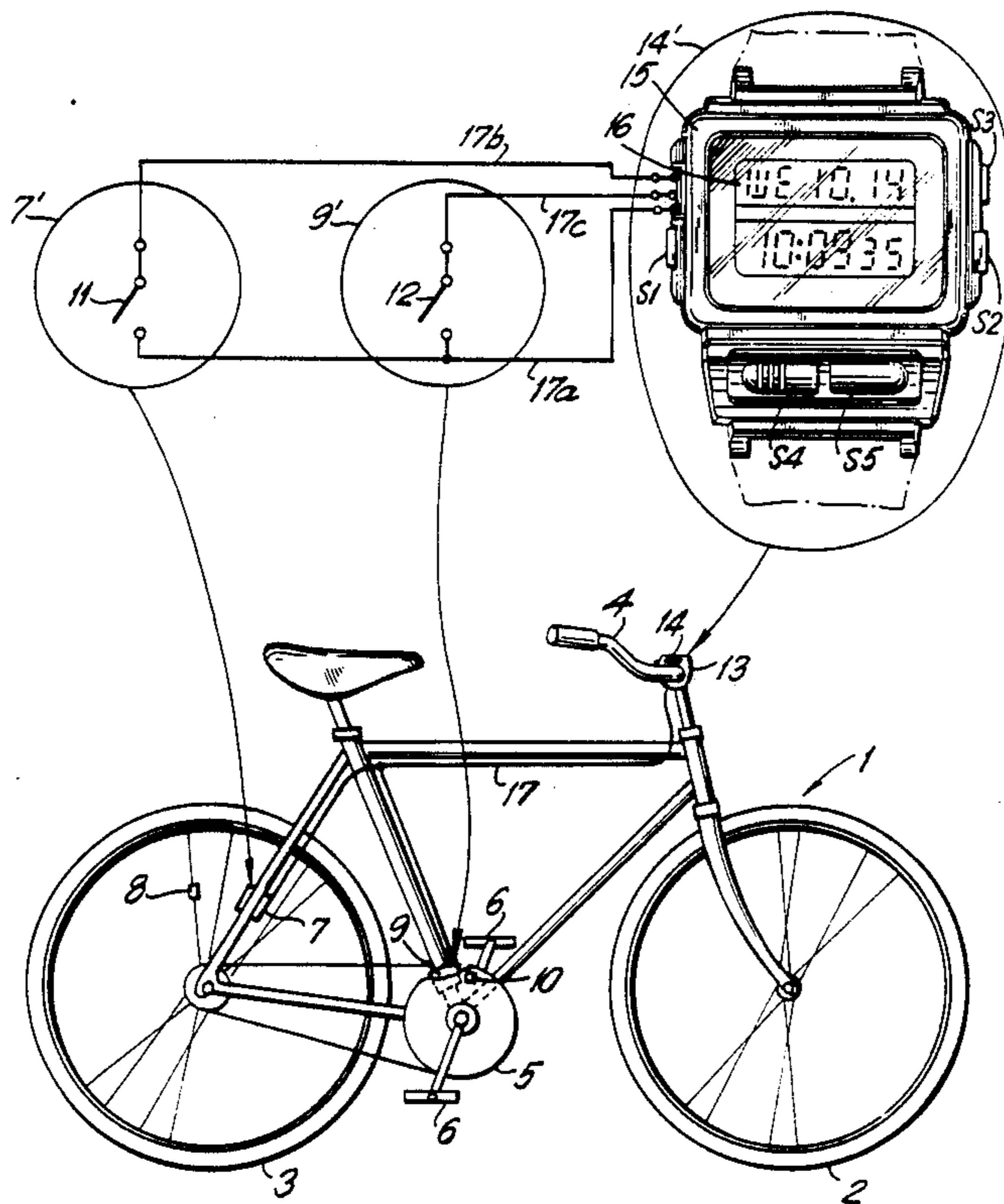
1349044 3/1974 United Kingdom .

Primary Examiner—Vit W. Miska
Attorney, Agent, or Firm—William C. Crutcher

[57] ABSTRACT

A digital wristwatch having a case and strap also serves as a computer to display speed, distance, and pedaling cadence when temporarily attached to a holder on the bicycle handlebar. Wheel and sprocket sensors are connected to electrical terminals in the holder. The wristwatch is provided with external electrical terminals connected to a microcomputer in the watchcase which uses signals from the sensors when the watchcase is temporarily retained in the holder. The microcomputer is preprogrammed to provide an improved dual mode circuit which keeps time in a "watch mode" and which uses the sensor signals to make speed, distance, and cadence computations and display them in a "bike mode." The computations may be altered and new constants entered in a "calibration mode," through manual actuators. Changes of state within a mode are responsive to manually actuated push-buttons, to lapse of predetermined time periods or to condition of an annunciator.

16 Claims, 8 Drawing Sheets



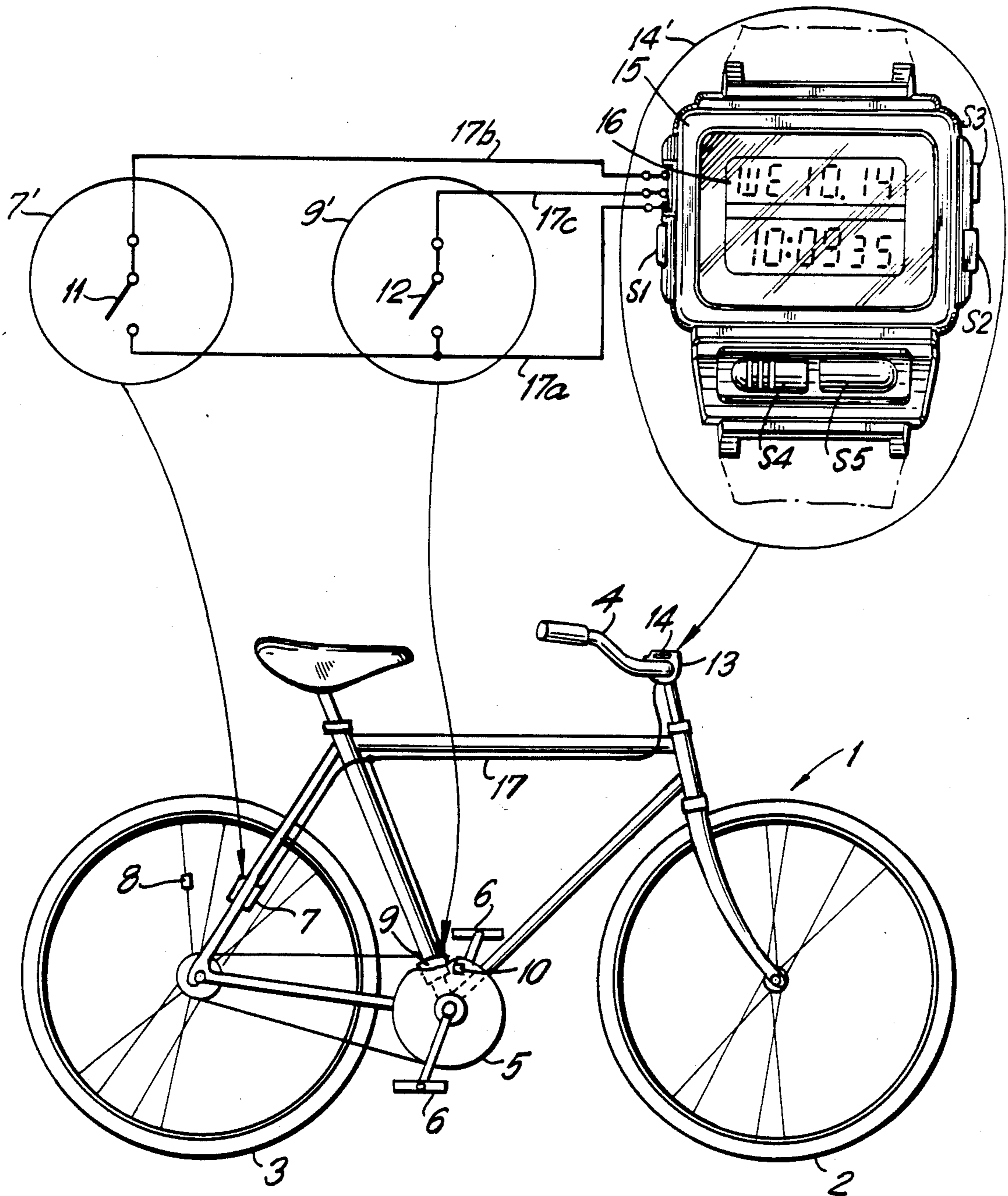


FIG. 1

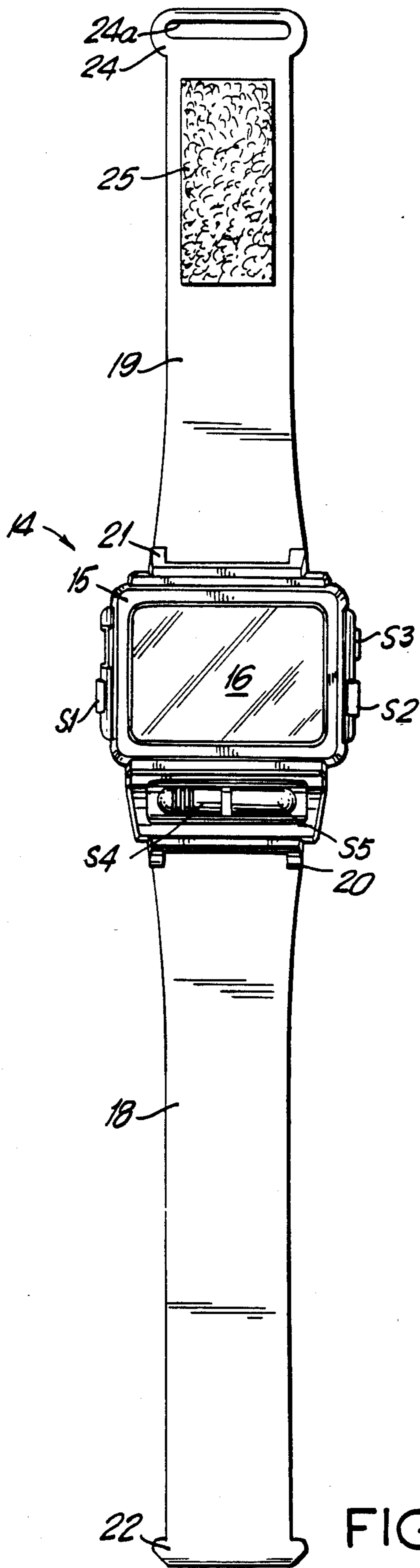


FIG. 2

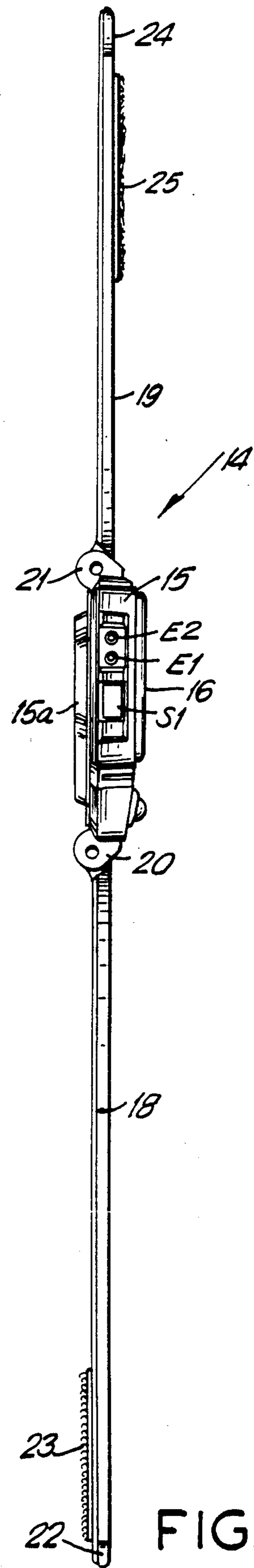


FIG. 3

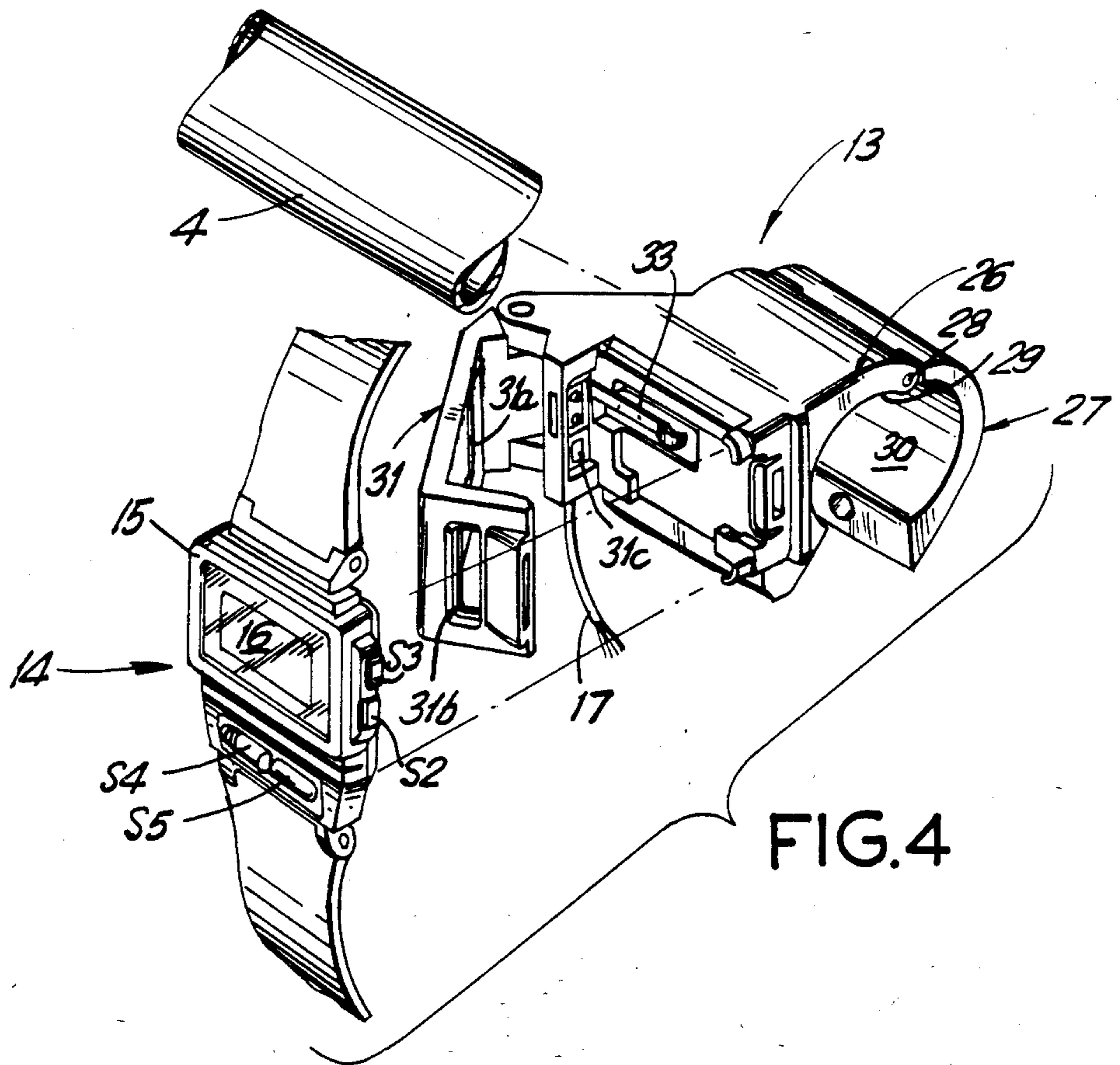


FIG.4

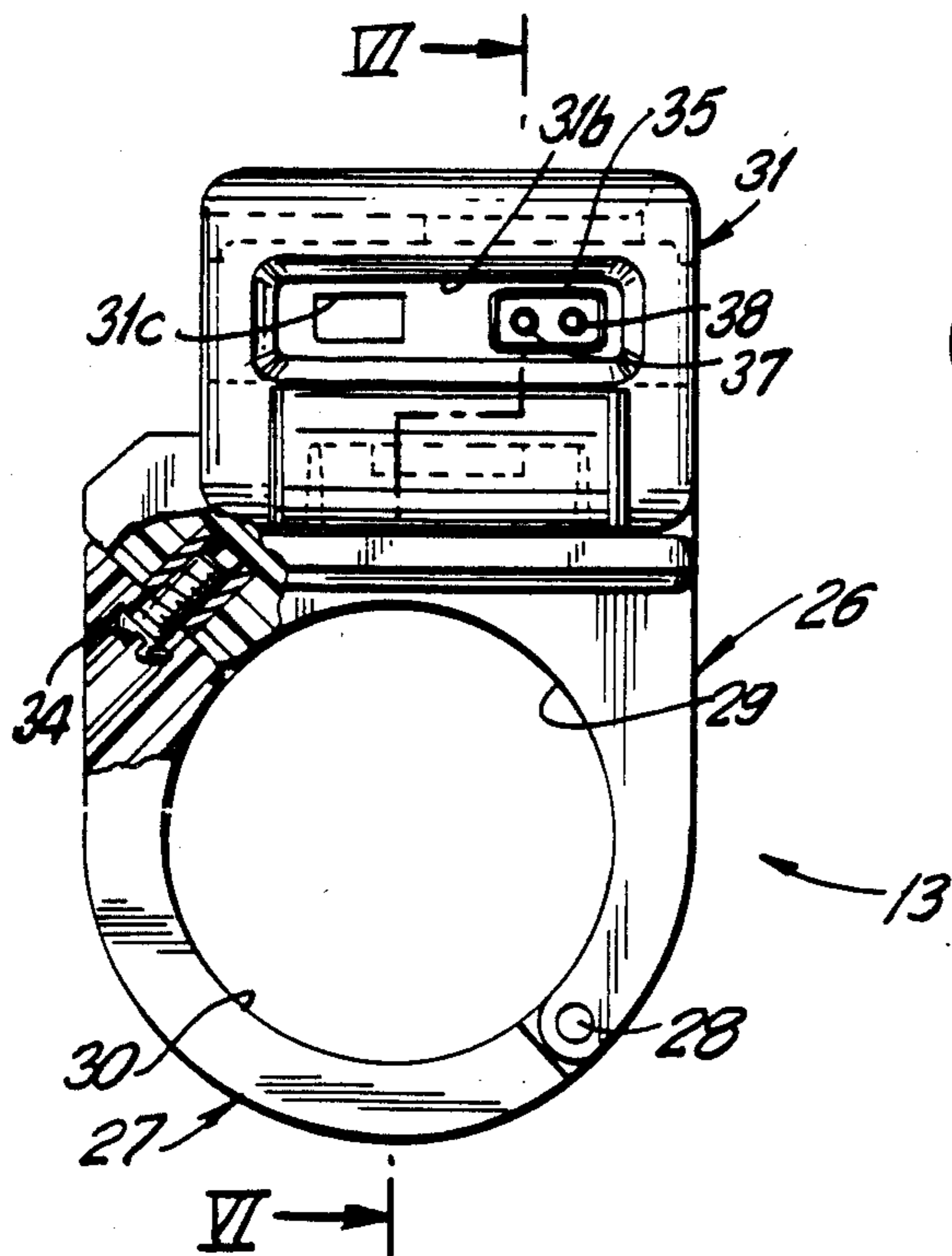


FIG.5

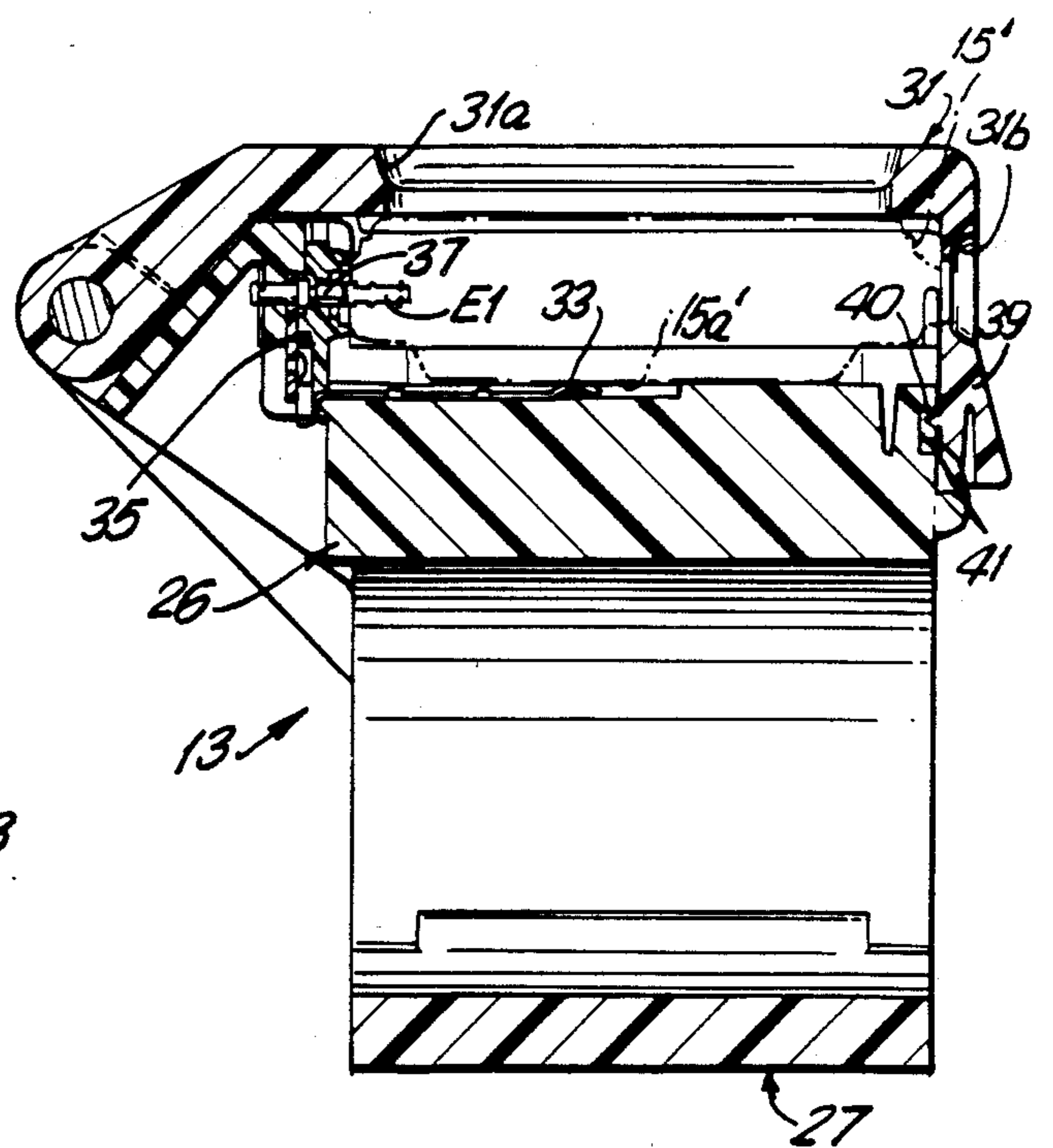
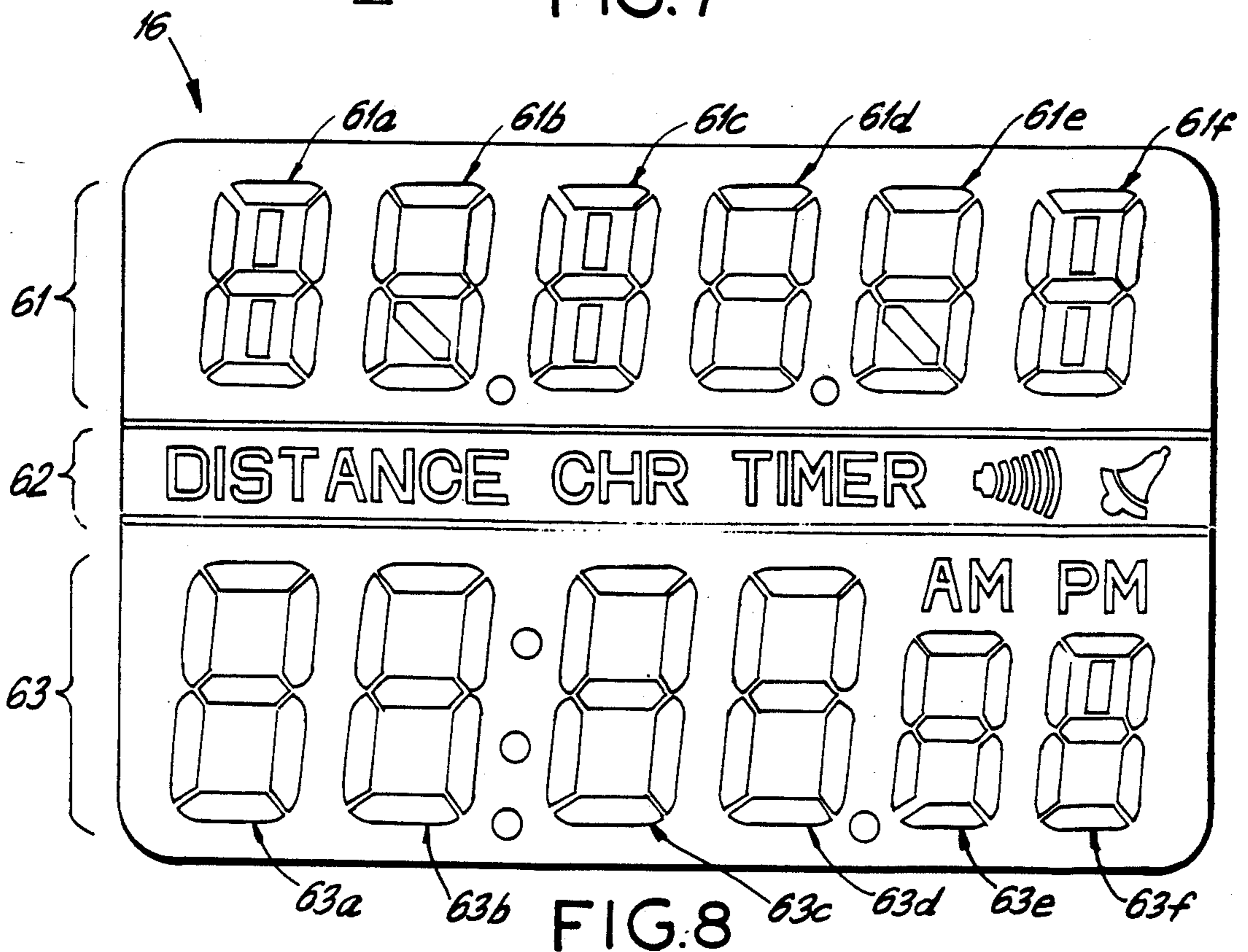
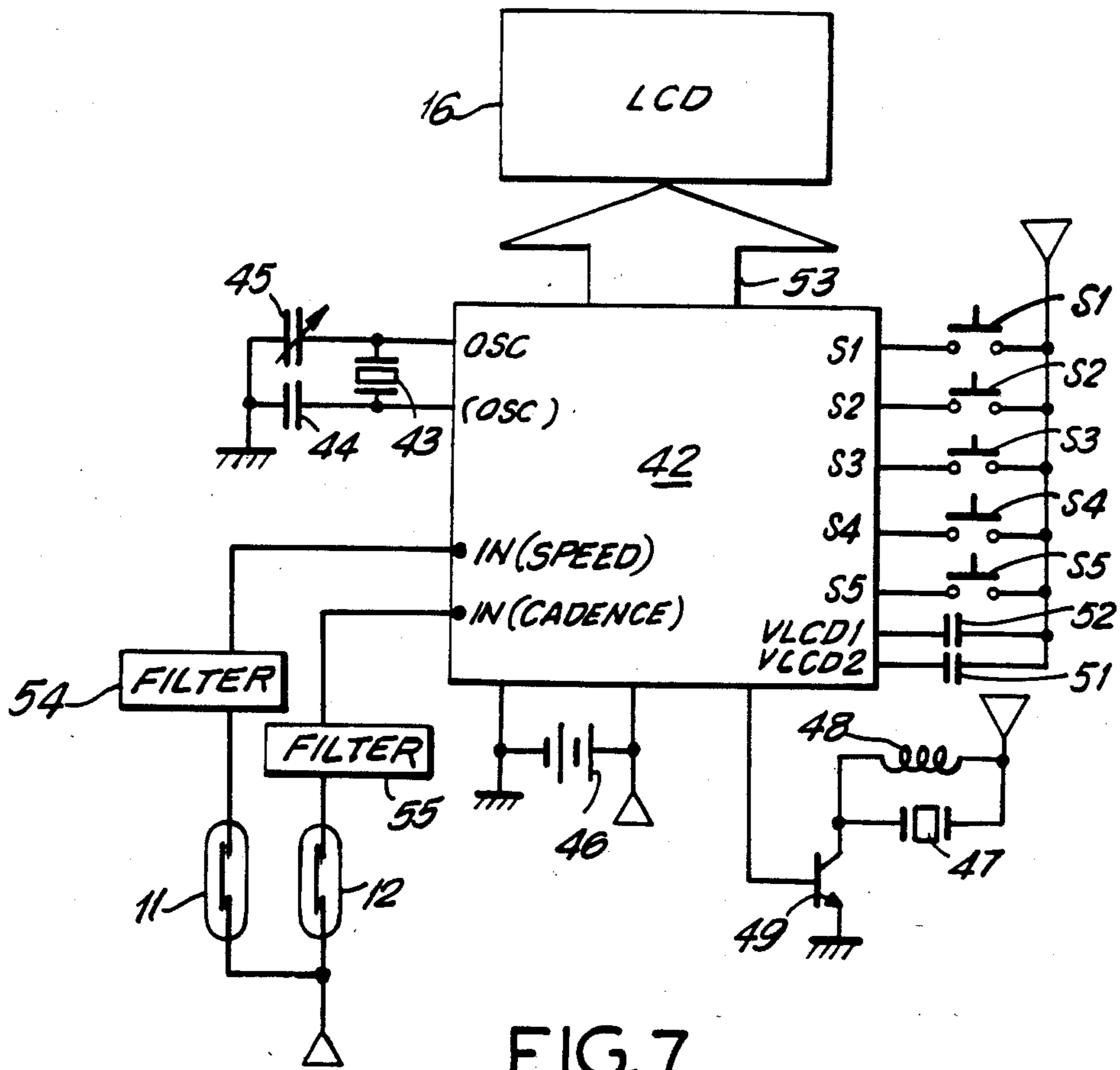


FIG.6



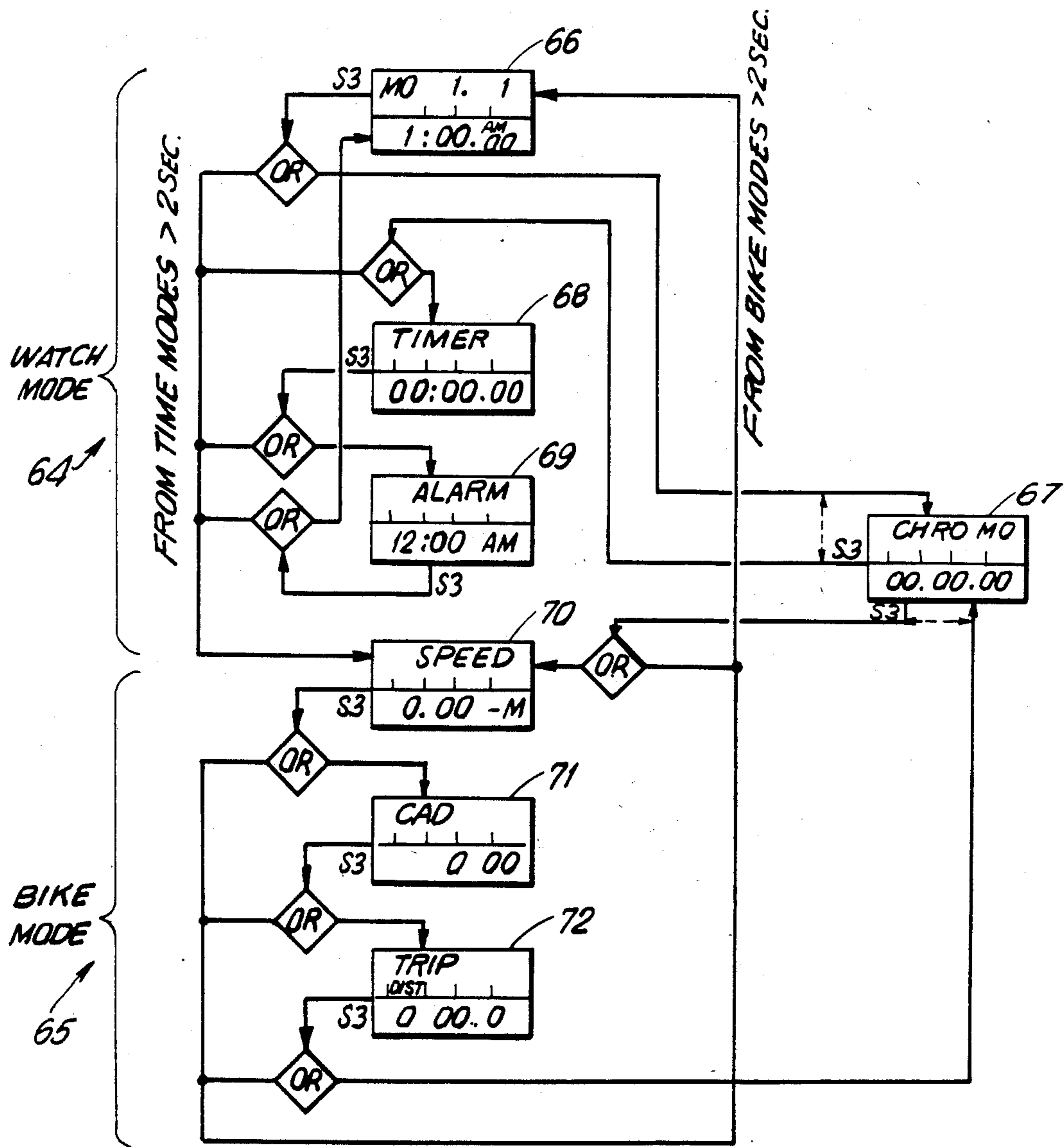


FIG.9

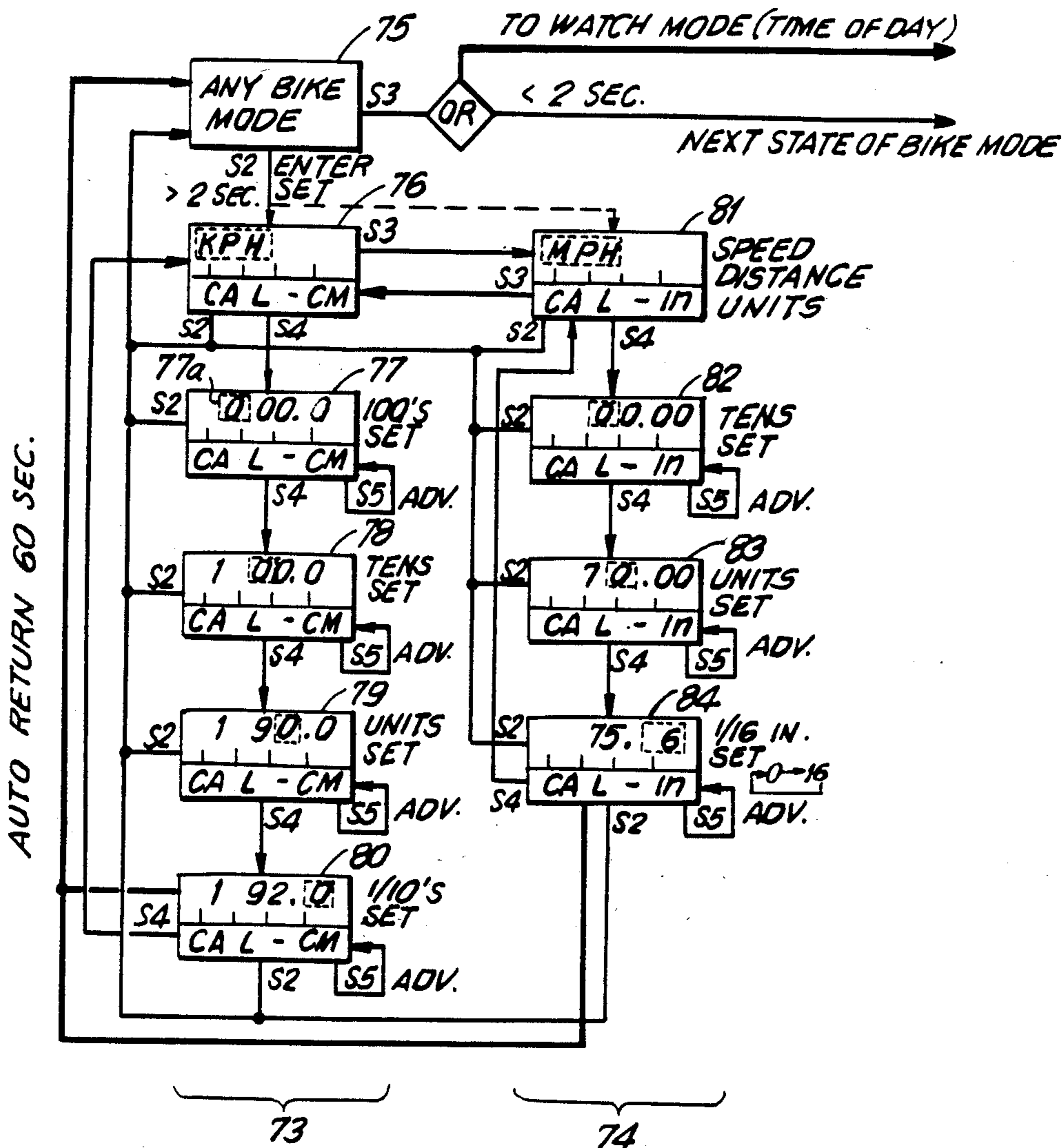


FIG.10

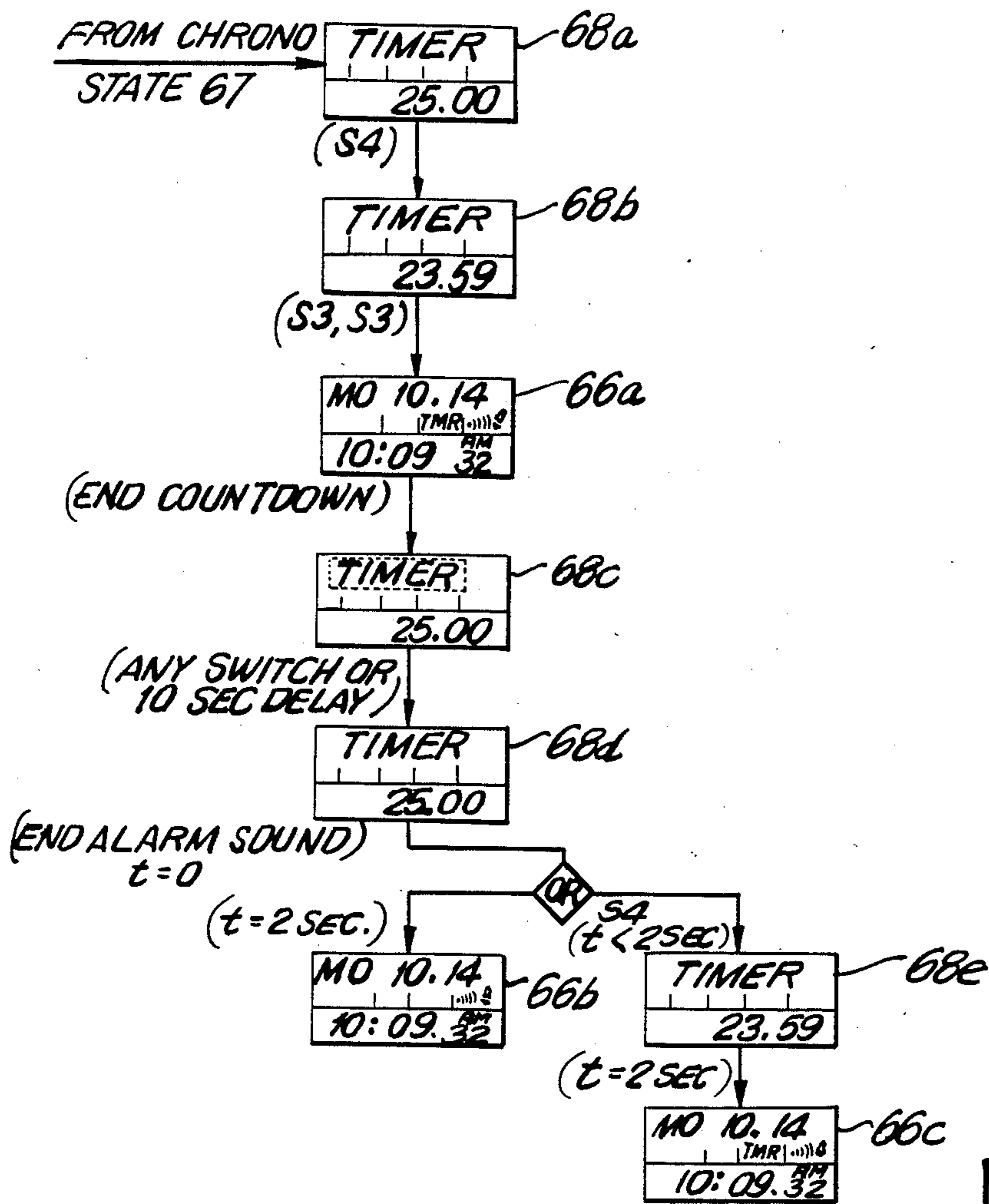


FIG. 11

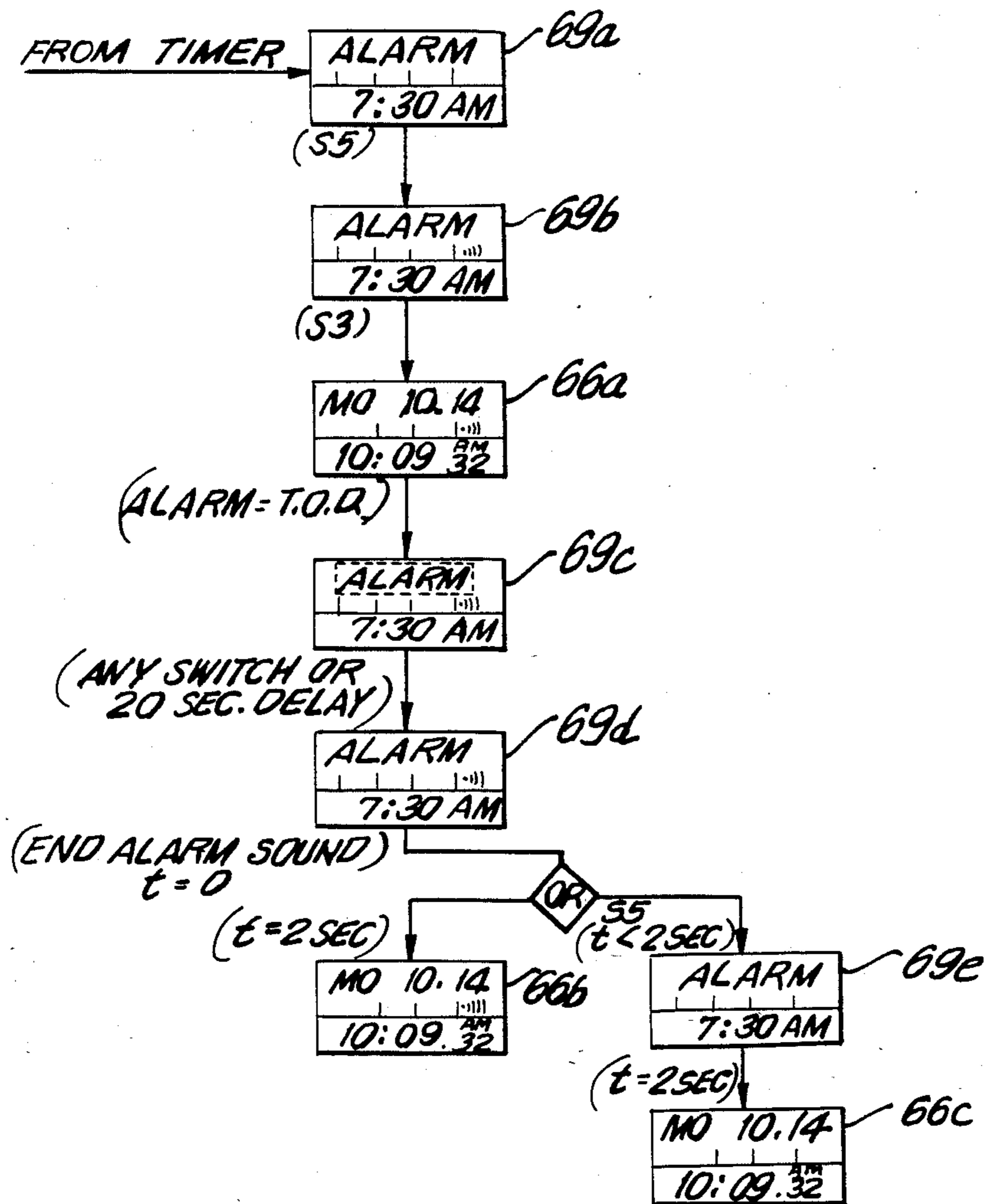


FIG.12

BICYCLE WATCH - DUAL MODE CIRCUIT

BACKGROUND OF THE INVENTION

This invention relates generally to improvements in a digital wristwatch which also functions as a computer to monitor operation of a bicycle, such as speed, distance traveled, and cadence (pedaling speed of the cyclist). More particularly, the invention relates to an improved dual mode circuit which temporarily converts a digital wristwatch into a bicycle computer.

Bicycle speedometers, odometers, and cadence counters are well-known which utilize sensors mounted with respect to the moving elements of the bicycle. Patents exemplary of such devices are seen in U.S. Pat. Nos. 4,156,190—Chittenden, 4,352,063—Jones, et al., 4,007,419—Jasmine, and 3,898,563—Erisman. Bicycle computers have been provided with microprocessor integrated circuits, with means for externally programming, setting, or changing the computations performed by the bicycle computer with push buttons or keyboards. Exemplary of such devices are U.S. Pat. No. 4,334,190—Sochaczewski, and 4,633,216, 4,636,769, and 4,642,606—all in the name of Tsuyama.

A number of patents have illustrated digital speed or distance measuring systems which may be calibrated or adjusted to allow for variations in wheel size, among these being the aforesaid Sochaczewski Pat. No. 4,334,190, and 3,865,305—Sampey, and 3,614,617—Blake.

All of the aforesaid patents describe devices which are intended to only perform computations for the bicycle or other vehicle. While some of the aforesaid devices provide visual or aural indication of cadence, they do not provide the functions furnished by a modern digital wristwatch, such as time of day, day, date, elapsed time, chronograph, and so forth.

Digital wristwatches have been proposed, which also serve as calculators or computers to perform computations from an external keyboard, such as exemplary U.S. Pat. Nos. 3,955,355—Luce, 3,186,730—Yamamoto et al., 3,803,834—Reese, and United Kingdom Patent Specification No. 1,349,044.

One such digital wristwatch has been proposed which receives external electrical signals, makes internal computations and displays the result of the computations on the normal timekeeping display in U.S. Pat. No. 4,022,014 issued May 10, 1977 to Lowdenslager and assigned to the applicant's assignee. This wristwatch has no provision for altering the type of computations or for changing the constants to be used in computations when the wristwatch is receiving external signals.

Accordingly, one object of the present invention is to provide an improved dual mode circuit for a bicycle watch.

Another object of the invention is to provide an improved circuit for keeping time in a "watch mode" and for receiving external signals and making computations in a "bike mode" and displaying the computations.

Another object of the invention is to provide an improved bicycle watch with means for altering the type of computations and changing the constants to be used in a "calibration mode."

DRAWINGS

The invention, both as to organization and method of practice, together with other objects and advantages thereof, will best be understood by reference to the

following description, taken in connection with the accompanying drawings, in which:

FIG. 1 is a simplified schematic view of a bicycle indicating location of sensors and the combination wristwatch and bicycle computer,

FIG. 2 is a top plan view, and

FIG. 3 is a left side elevation view of a digital wristwatch used in the present invention,

FIG. 4 is an exploded perspective view of wristwatch, holder, and portion of a bicycle handlebar,

FIG. 5 is an end elevation view, partly in section, of the holder,

FIG. 6 is a side elevation view in section, taken along lines VI—VI of FIG. 5,

FIG. 7 is a circuit diagram illustrating in schematic form the integrated circuit and discrete electronic components used in the wristwatch,

FIG. 8 is a plan view of the electrooptical display illustrating the layout of segments,

FIG. 9 is a state diagram of the various modes and functions provided by the circuit of FIG. 7,

FIG. 10 is a state diagram of the speed/distance calibration mode for the circuit of FIG. 7, and

FIGS. 11 and 12 are state diagrams of automatically altered displays commenced from the timer and alarm states respectively.

SUMMARY OF THE INVENTION

An improved bicycle watch with a dual mode circuit has a case with manual actuators, electrooptical display and microcomputer connected to the display and responsive to the actuators. The watch receives external signals from bicycle sensors in response to operation of the bicycle and also generates timekeeping signals. The microcomputer is programmed to provide a watch mode with a plurality of states such as time of day, elapsed time, alarm and chronograph. It is also programmed to provide a bike mode with a plurality of states computed from external signals, such as speed, trip distance, and pedaling cadence. The circuit will switch between bike mode and watch mode in response to actuation of a manual actuator in a predetermined manner, preferably when the actuator is depressed for more than a preselected time. Other aspects of the invention include a calibration mode for a constant to be entered and used to perform computations, means to alter the constant with a manual actuator, and means to alter the computations in accordance with the numerical value of the constant. Additional aspects include automatic alteration of states in response to the "on" or "off" condition of an annunciator or alarm.

DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring now to FIG. 1 of the drawing, a bicycle 1 has the usual front and rear wheels 2, 3, handlebar 4 for steering, and drive sprocket 5 operated by pedal 6. A first sensor 7 is mounted on a stationary part of the frame and is actuated during each revolution of rear wheel 3 by a permanent magnet 8 mounted on the wheel. A second sensor 9 is actuated during each revolution of pedal sprocket 5 by a permanent magnet 10 mounted on the sprocket. Sensors 7, 9 simply provide switch closures and may be magnetically responsive reed switches. As shown in the expanded schematic 7', sensor 7 operates to close a switch 11 each time the magnet 8 passes sensor 7. Similarly, as seen in the ex-

panded schematic 9', sensor 9 closes a switch 12 each time magnet 10 passes sensor 9.

Mounted on handlebar 4 is a holder 13 for temporarily holding a digital wristwatch 14. In the expanded schematic 14', digital wristwatch 14 is seen to comprise a case 15, an electrooptic display 16, manually actuated push buttons S1, S2, S3 mounted in the side of case 15, and push buttons S4, S5 mounted in the top of case 15.

Lastly, an insulated electrical cable 17 carries electrical signals from the sensors 7, 9 to the wristwatch 14 by way of a common conductor 17a and switching conductors 17b, 17c. These are connected to a circuit (not shown) inside the wristwatch case 15 in a manner to be described.

Referring now to FIGS. 2 and 3 of the drawing, a preferred embodiment of the wristwatch is shown when removed from the holder. It is adapted to be worn as an ordinary wristwatch by means of strap halves 18, 19 attached by spring bars to the lugs 20, 21 of case 15. Strap half 18 terminates in a widened tongue portion 22 and includes a strip of VELCRO (Registered Trademark of Velcro Corporation) on its under side indicated at 23. The strap half 19 terminates in an expended rigid loop 24 with an opening 24a, and includes a mating strip of VELCRO 25 on its upper side. The type of strap attachment is immaterial to the present invention and any conventional watch strap or band could also be used.

A set of first electrical terminals are arranged externally on case 15. These comprise socket terminals E1 and E2 on the side of case 15 and a metallic caseback 15a for the ground or common connection. Alternatively an additional socket terminal could be substituted for caseback 15a. The first electrical terminals are connected to the circuit inside the case 15.

Referring now to FIG. 4 of the drawing, the holder 13 is seen in perspective view alongside a portion of bicycle handlebar 4 and watch 14. Holder 13 includes a first housing part 26 and a second housing part 27 pivotably connected thereto by means of a pin 28. Members 26, 27 together define inner semi-cylindrical surfaces 29, 30 which are dimensioned to grip and attach holder 13 to handlebar 4 when members 26, 27 are pivoted and secured together.

Housing 13 includes a second pivotable member 31, which, together with the upper surface of housing member 26 define a receptacle for temporarily holding watch 14 when it is inserted and when member 31 pivoted to a closed position. Member 31 defines a top window 31a arranged for viewing the electrooptical display 16 when the watch is inserted, a side window 31b for obtaining access to push button actuators S2, S3, and a recess 31c for accommodating to push button actuator S1. A metal spring clip 33 is connected to the common conductor 17a in cable 17.

Reference to the enlarged end and side elevation drawings of holder 13 in FIGS. 5 and 6 will illustrate further details of the holder 13.

As shown in FIG. 5, the pivot pin 28 permits the second housing part 27 to pivot to a closed position, where it is secured tightly around the handlebar by means of a screw 34 threaded into the first housing part 26. The pivoted member 31 is also shown in a closed position. As seen through window 31b, a gasket 35 on the opposite wall of the receptacle surrounds a pair of electrical terminal pins 37, 38.

Referring to FIG. 6, holder 13 illustrates that the upper pivotable member 31 includes an extending flap

39 with a protrusion 40 which mates in a groove 41 in the first housing part 26. The location of the watchcase 15 is illustrated by the phantom outline 15'. Gasket 35 provides an insulating socket for the electrical connection pins 37, 38. Pin 37 is properly positioned to make electrical contact with the appropriate external watch terminal E1 when the watch is properly located in the receptacle of holder 13. Similarly pin 38 makes contact with external watch terminal E2. Also, as shown by the phantom outline 15' of the watchcase, its electrically conductive caseback 15a' makes electrical contact with the spring clip 33.

The two pins 37, 38 and the spring clip 33 together comprise second electrical terminals which are disposed in the holder and connected to the cable 17 leading to the sensors 7 and 9. Spring clip 33 is connected to common conductor 17a, and pins 37, 38 are connected to switching conductors 17c and 17b respectively.

Referring now to the circuit drawing of FIG. 7, the schematic illustrates an integrated circuit 42, which is a commercially available mask-programmable microcomputer chip, having internal microprocessor, memory, input and output control circuits, LCD display drive circuits and programmed instructions sets. Such microcomputers are readily available from several sources, but a suitable microcomputer for the present application is part number JT 7598 AS, which is available from Toshiba Electric Company. Also shown on the FIG. 7 schematic drawing are the LCD display 16, magnetic reed switches 11, 12, and actuators S1 through S5 discussed previously in connection with FIG. 1. Enclosed in the watchcase 15 with the integrated circuit 42 and the LCD 16 are a number of additional circuit elements which are well-known and normally understood to be part of the operational parts of a digital wristwatch circuit. These are a quartz crystal 43 serving as a time base, external fixed capacitor 44 and variable capacitor 45 which are connected in circuit with quartz crystal 43, and are connected to pins labeled OSC and (OSC).

A battery power source 46 is provided in the form of a button-type energy cell in the watchcase. An audible annunciator serving as a watch alarm and/or cadence beeper is made up of a piezo electric crystal 47, inductance coil 48, and drive transistor 49, which is responsive to the microcomputer 42.

Two fixed external capacitors 51, 52, combined with other circuit elements inside the integrated circuit 42 serve to boost the output voltage to drive LCD 16 through a display bus 53, which represents the several parallel leads connected to the various actuatable segments of the LCD display shown in FIG. 8.

Connected between the external switch 11 and an output pin labeled IN (SPEED) on integrated circuit 42 is a filter 54 for removing noise and insuring the desired external input signal at each closure of switch 11. Similarly a filter 55 is provided between external switch 12 and an input pin labeled IN (CADENCE).

The microcomputer integrated circuit 42 is programmed at the time of manufacture to provide the various functions of a digital wristwatch, to receive periodic input signals from outside the watchcase provided by sensor switches 11, 12, to make internal computations of speed, distance, and cadence, to decode and display the results, and to provide logic for changing back and forth between watch mode and bicycle mode of operation by appropriate actuation of switches S1-S5. Conventional programming techniques are used

which are within the knowledge of those skilled in the art.

Referring now to FIG. 8 of the drawing, the layout of segments for the liquid crystal display used in the bicycle watch is shown. An upper level 61 of digits 61a-61f has individually actuatable segments arranged to indicate day/date in the manner of a digital calendar watch, as well as to provide certain messages when in other timekeeping states or bicycle speed/odometer states. A central level 62 provides for word messages and symbols. A lower level 63 of digits 63a-63f has actuatable segments providing standard hour/minute/second indication when in timekeeping state of the watch mode and provides speed/distance indication when in bike mode. Both the upper level 61 and lower level 63 further include extra segments giving capability for certain alphanumeric messages in the calibration mode, as will be apparent from the following description of these modes and by comparison with appropriate segments on the display.

Referring now to FIG. 9 of the drawing, a "state" diagram is shown in schematic form. Each of the rectangles represents a representative display shown on electrooptical display 16 when the instrument is in that "state." The instrument continues to operate under control of the particular subroutine of the program in the microcomputer chip, until the instrument is placed into another state. Change from one state to another or into another mode, respectively proceeds by actuation of a manual actuator either momentarily or by holding it for a preselected elapsed time determined by the master program. In the diagram illustrated in FIG. 9 the manual actuator switch S3, together with the length of time that switch S3 is held depressed, initiates a change to the next state. Depression of switch actuator S3 introduces a decision represented by the diamond labeled "OR". The length of time that switch S3 is depressed determines the exit path from the "OR". The preselected switch depression time, which can easily be changed by reprogramming, is 2 seconds in the example shown in FIG. 9.

Although the states can be sequenced in any desired manner, depending on how the microcomputer chip instrument is programmed, the FIG. 9 arrangement shows a "watch mode" indicated generally at 64 and a "bike mode" indicated generally at 65. The watch mode comprises 4 separate states or functions, a day/date timekeeping state 66, a chronograph state 67, a timer state 68, and an alarm state 69. The bike mode 65 comprises 4 separate states, shown as a speed state 70, a cadence state 71, a distance or trip state 72, and the aforementioned chronograph state 67, which is common to both watch mode 64 and bike mode 65. Repetitive momentary actuation of actuator S3 while in the watch mode causes the display to cycle repetitively through states 66, 67, 68, 69, 66—. Actuation of switch S3 for more than 2 seconds in any of these states causes the instrument to shift from watch mode 64 to bike mode 65, entering the bike mode in the speed state 70 of bike mode 65.

While in the bike mode, repetitive momentary actuation of switch S3 causes the device to cycle repetitively through states 70, 71, 72, 67, 70—. Depression of switch S3 for more than 2 seconds causes the device to shift from bike mode 65 back to watch mode 64.

The number of modes shown, and the number and sequence of states within the modes are purely a matter

of choice and routine programming of the microcomputer chip.

Reference to FIG. 10 of the drawing illustrates another state diagram for a "calibration mode," which is used to enter an arithmetic constant into the computer. The value of the constant is dependent upon the particular bicycle wheel size, so that the signal from sensor 7 can be utilized by the computer to properly compute speed and distance traveled. Furthermore, the calibration mode provides for calibration either in English or metric units.

As shown in FIG. 10, the calibration mode comprises a metric calibration sequence set indicated by bracket 73 and an English calibration sequence set indicated by bracket 74. The calibration mode is entered from any of the bike mode states 70-72 (see FIG. 9) indicated generally by the rectangle 75. Pressing actuator S2 for more than 2 seconds causes the metric calibration set 73 to be entered as represented by display state 76. Successive depressions of actuator S4 causes repetitive metric calibration display states 77, 78, 79, 80, 76 and so forth. A flashing numeral, indicated symbolically for example by enclosing the numeral with dotted rectangle 77a in state 77 indicates which numeral may be advanced and set. Successive actuation of actuator S5 advances numeral 77a. The numeral which may be set is similarly symbolically shown in the other state displays. When the instrument is in the first metric display state 76, depression of actuator S3 will cause the calibration mode to shift to English calibration sequence set 74 and to display successively the display states 81, 82, 83, 84, 81.

Shifting back and forth between English and metric calibration modes takes place when the display is either in state 76 or in state 81 by actuation of switch S3. The metric calibration set is indicated by the flashing letters KPH (Kilometers Per Hour) when the instrument is in state 76 and the English system is indicated by the flashing letters MPH (Miles Per Hour) when the instrument is in state 81.

An important feature of the present invention is that the numerical value of the constant introduced automatically causes a decision by the computer logic as to whether the constant represents circumference or radius of the bicycle wheel. For example, when in the metric calibration sequence set 73 (units in centimeters), an entry of greater than 76 centimeters means that the constant represents circumference of the bicycle wheel in centimeters, whereas entry of a number less than 76 centimeters means that the entry is the radius of the bicycle wheel in centimeters.

Escape from the calibration mode may take place in two ways. Either depression of actuator S2, or a time delay without changing state for more than 60 seconds, will cause the computer to exit the calibration mode and return to bike mode state 75, as indicated in FIG. 10.

FIGS. 11 and 12 of the drawing depict change of state within the watch mode, in response to a condition of an annunciator. Such an annunciator is usually an audible piezoelectric device making a beeping sound, although a visual annunciator such as a flag on the electrooptical display is included within the definition of an annunciator.

FIG. 11 depicts alternating change between the countdown timer state 68 and the time of day state 66 (see FIG. 9), in accordance with programming of microcomputer 42, such that the change of state is responsive to the "on" or "off" condition of the annunciator. The alternate states in the example of FIG. 11 show a

countdown timer state 68a which is set at 25 minutes (by a procedure similar to the setting process of FIG. 10). Actuation of actuator S4 commences countdown as illustrated at state 68b. Return to time of day state 66a is accomplished as described in FIG. 9 by successive actuation of actuator S3. At the end of the countdown period the annunciator sounds and responsive thereto the display state is automatically switched to timer display state 68c, without operator intervention. As shown in the display states 68c and 68d, the originally set countdown period of 25 minutes is displayed. The alarm annunciator is silenced by pressing any switch or by elapse of a 10 second interval. The "off" condition of the annunciator starts at a time $t=0$.

Thereafter, without operator intervention, after a preselected time period of $t=2$ seconds, the display state is changed back to time of day state 66b or 66c depending upon whether actuator S4 is pushed in this interval of 2 seconds to restart the countdown timer (state 68e).

FIG. 12 similarly illustrates a change between an alarm set state 69 and time of day state 66 (see FIG. 9), in accordance with programming of microcomputer 42, such that the change of state is responsive to the "on" or "off" condition of the annunciator. The alternate states in the example of FIG. 12 show an alarm setting of 7:30 a.m. in display state 69a, which is set by a procedure known in the art and the alarm is armed by actuating a push-button so that it appears as shown in display state 69b and returned to time of day state 66a by actuation of switch S3.

Thereafter, when the time of day and alarm setting coincide, the annunciator sounds. This is accomplished by programming means well-known in the art. In accordance with this aspect of the present invention, however, responsive to the active condition of the annunciator, an alarm set state 69c is displayed. The alarm annunciator is silenced by pressing any switch or by elapse of a 20 second delay. The "off" condition of the annunciator starts at a time $t=0$ seconds. Thereafter, without operator intervention, after a preselected time period of $t=2$ seconds, the display state is changed back to time of day state 66b or 66c. By pushing actuator S5 within this 2 second interval the alarm may be disarmed.

OPERATION

Operation of the invention should be apparent from the following description. When not bicycling, watch 14 is worn as a normal digital wristwatch on the wrist using the strap attachments shown in FIGS. 2 and 3. It may be operated in watch mode 64 to provide normal timekeeping, timed delay, alarm or chronograph. Since there are no external signals, it will not be operated in the bike mode.

When utilizing the watch as a bicycle computer, it is removed from the wrist and inserted into the receptacle of holder 13 by lifting the pivotable member 31, inserting the case 15 and then closing the pivotable cover. The watch is further secured to holder 13 by attaching the strap halves around the housing parts 26, 27 and utilizing the normal wrist attachment to secure the wristwatch in place. When so mounted, the first external electrical terminals on the case make electrical contact with the second electrical terminals in the holder, these being electrically connected to sensors 7, 9 via cable 17.

Data obtained for calibration is obtained by measuring the radius of wheel 3 with the rider seated on bicy-

cle 1, or by making a mark on the tire and determining the circumference by one or more revolutions of the wheel, again preferably with the rider mounted.

The calibration mode is entered by pressing actuator S2 (FIG. 10). The decision between English or metric units by depressing switch S3 is made. Then the radius (or circumference) is entered by successively pressing actuator S4 to determine the numeral to be set and setting it by successively pressing actuator S5. Side mounted actuators S2 and S3 can be operated through the openings provided in the holder 13. The top mounted actuators S4, S5 are not affected by the holder.

While cycling, the readings of speed, pedaling cadence, or distance covered may be displayed by successive actuation of switch S3.

While there has been described what is considered to be the preferred embodiment of the invention other modifications will occur to those skilled in the art, and it is desired to secure in the appended claims all such modifications as fall within the true spirit and scope of the invention.

I claim:

1. An improved bicycle watch with dual mode circuit, comprising;
 - a case having a plurality of manual actuators, an electrooptical display and a microcomputer in said case connected to said electrooptical display and responsive to said manual actuators,
 - means supplying periodic external signals to said microcomputer in response to operation of a bicycle,
 - said microcomputer being programmed to provide a watch mode having a first plurality of states and adapted to generate timekeeping signals and to display at least one timekeeping signal in one of said first states, and a bike mode having a second plurality of states and adapted to perform computations on said external signals and to display at least one operative condition of the bicycle in one of said second states, and
 - said circuit being programmed to switch between watch mode and bike mode in response to actuation of at least a first manual actuator in a predetermined manner.
2. The combination according to claim 1, wherein successive actuation of a second manual actuator repetitively changes said states within said respective modes.
3. The combination according to claim 2, wherein the first and second manual actuators are one and the same, and wherein actuation thereof for longer than a preselected time causes change in mode.
4. The combination according to claim 1, wherein said watch mode and said bike mode include at least one common state.
5. The combination according to claim 1, wherein said bike mode includes states selected from the group comprising: bicycle speed, trip distance, pedaling cadence, and chronograph.
6. The combination according to claim 1 wherein said watch mode includes states selected from the group comprising: time of day, countdown timer, alarm, and chronograph.
7. The combination according to claim 1, wherein said microcomputer is programmed to provide a calibration mode adapted to display at least one constant to be used in said computation in response to actuation of a third manual actuator, and also responsive to actua-

tion of fourth manual actuator to alter the numerical value of said displayed constant.

8. The combination according to claim 7, wherein said microcomputer is further programmed to return from said calibration mode to one of said other modes after lapse of a preselected time period without further actuation of said third manual actuator.

9. The combination according to claim 7, wherein said calibration mode includes a plurality of calibration sequence sets, and wherein successive actuation of a fifth manual actuator repetitively changes calibration states within said sequence.

10. The combination according to claim 7, wherein said microcomputer is programmed to alter said computations in accordance with the numerical value of said displayed constant.

11. The combination according to claim 7 wherein said calibration mode comprises a first sequence set of calibration states in a first system of units for the displayed constant and a second sequence set of calibration states in a second system of units for the displayed constant.

12. The combination according to claim 1, wherein said bicycle watch includes an annunciator responsive to said microcomputer and wherein at least one change of state within one of said modes is responsive to a change in condition of said annunciator.

13. The combination according to claim 12, wherein a time of day state is arranged to change to an alarm set state in response to activation of sound on said annunciator.

14. The combination according to claim 12, wherein an alarm set state is arranged to change to a time of day state in response to a predetermined elapse of time after cessation of sound on said annunciator.

15. The combination according to claim 12, wherein a time of day state is arranged to change to a countdown timer state in response to activation of sound on said annunciator.

16. The combination according to claim 12, wherein countdown timer state is arranged to change to a time of day state in response to a predetermined elapse of time after cessation of sound over said annunciator.

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