



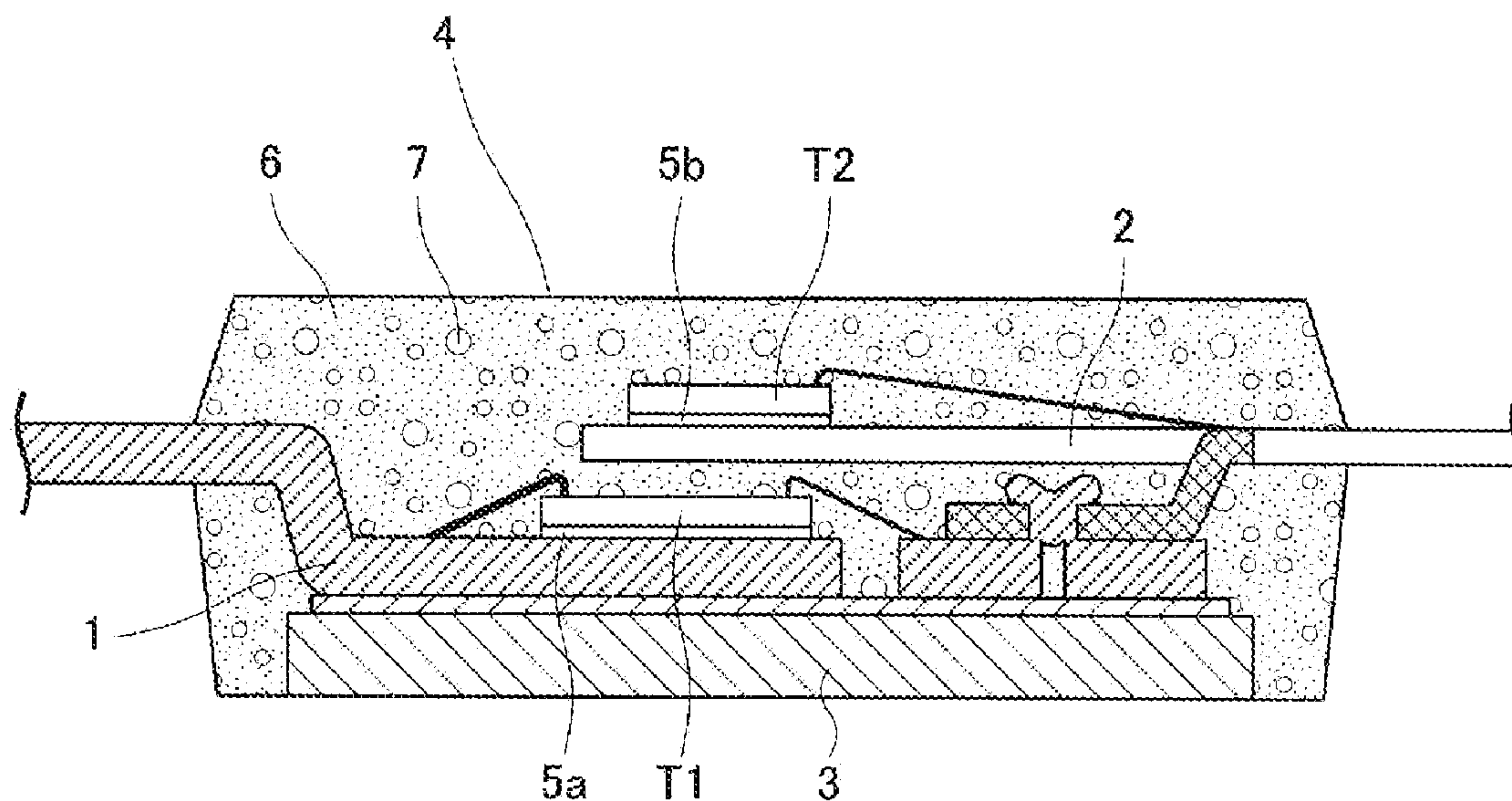
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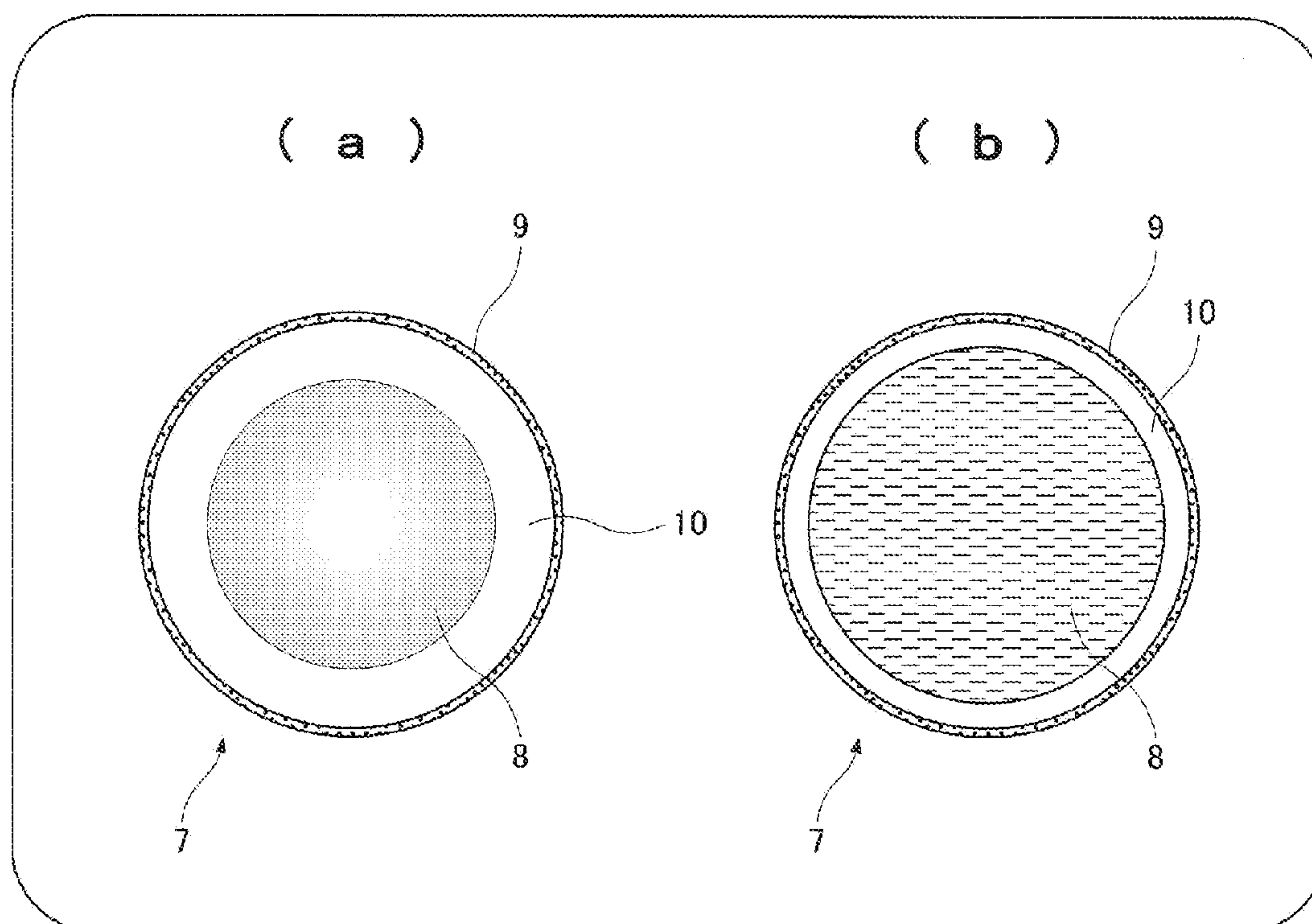
(19) **United States**(12) **Patent Application Publication**
Minamio et al.(10) **Pub. No.: US 2014/0054077 A1**(43) **Pub. Date: Feb. 27, 2014**(54) **ELECTRICAL COMPONENT RESIN,
SEMICONDUCTOR DEVICE, AND
SUBSTRATE**(30) **Foreign Application Priority Data**

Nov. 21, 2011 (JP) 2011-253383

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Osaka (JP)**Publication Classification**(72) Inventors: **Masanori Minamio**, Osaka (JP); **Hiroki
Ikeuchi**, Osaka (JP)(51) **Int. Cl.**
H05K 1/03 (2006.01)(73) Assignee: **Panasonic Corporation**, Kadoma-shi,
Osaka (JP)(52) **U.S. Cl.**
CPC **H05K 1/0306** (2013.01)
USPC **174/258; 524/387**(21) Appl. No.: **13/994,253**(22) PCT Filed: **Oct. 18, 2012**(86) PCT No.: **PCT/JP2012/006652**§ 371 (c)(1),
(2), (4) Date: **Jun. 14, 2013**(57) **ABSTRACT**

A semiconductor device is a resin package structure including a semiconductor element T1 molded with a first resin 6. The first resin 6 contains a filler 7 including an electrical insulating capsule enclosed with a phase-change-material that absorbs ambient heat and phase-changes so as to increase a dielectric-strength. The effect of the filler 7 achieves a structure with satisfactory heat dissipation and a high withstand voltage.





F I G. 3

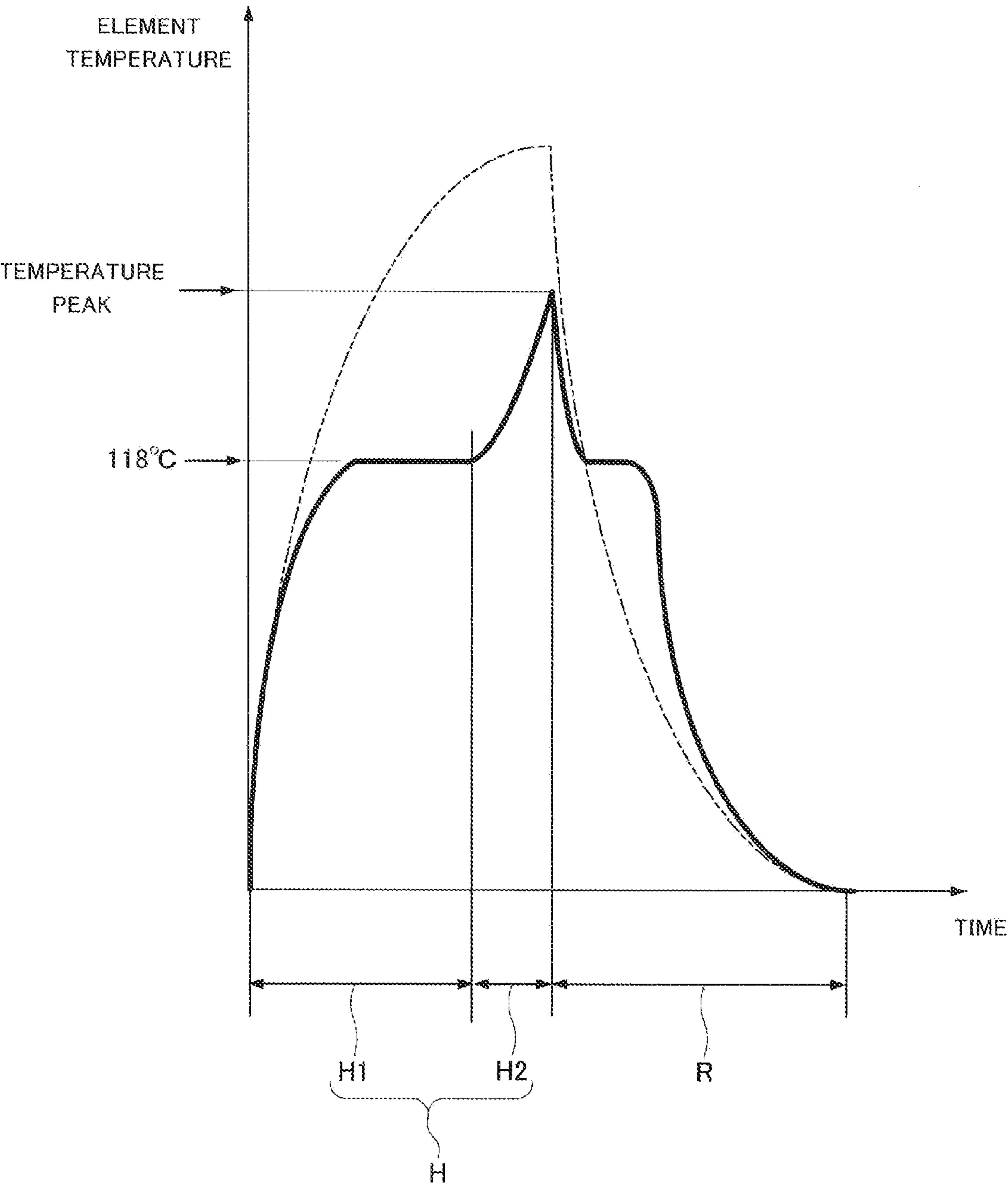


FIG. 4

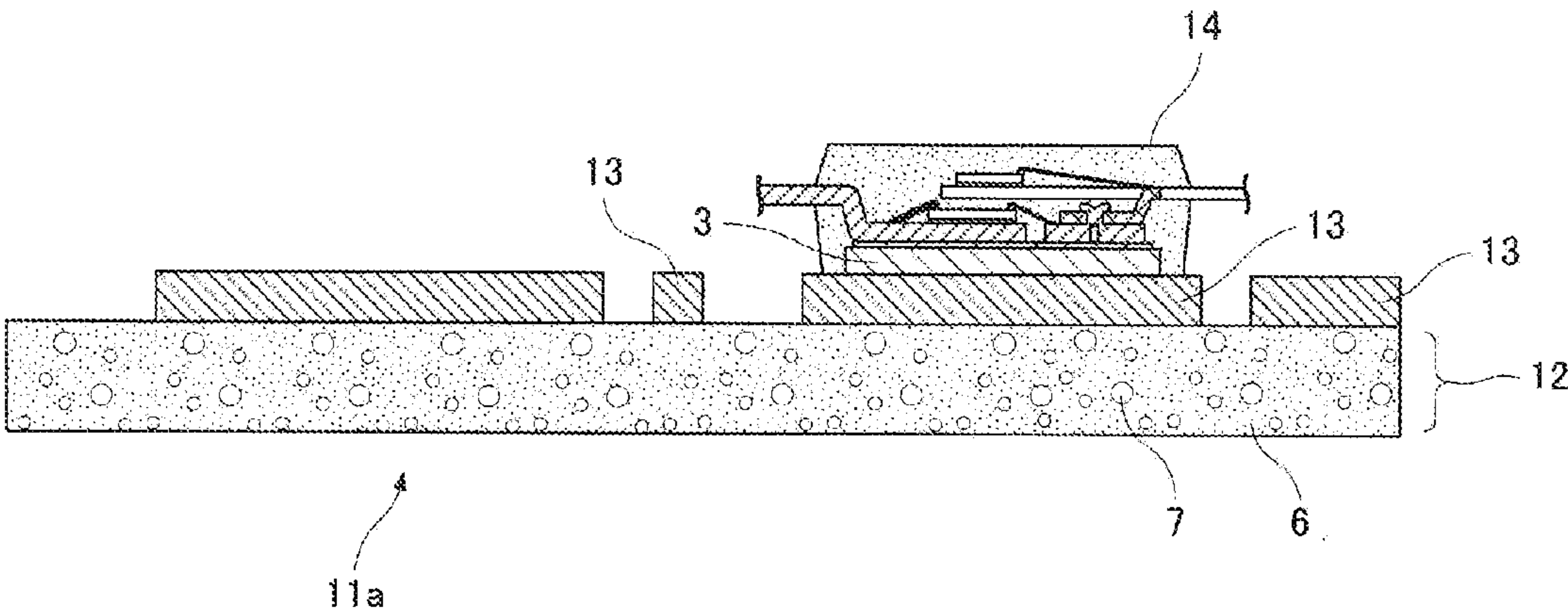


FIG. 5

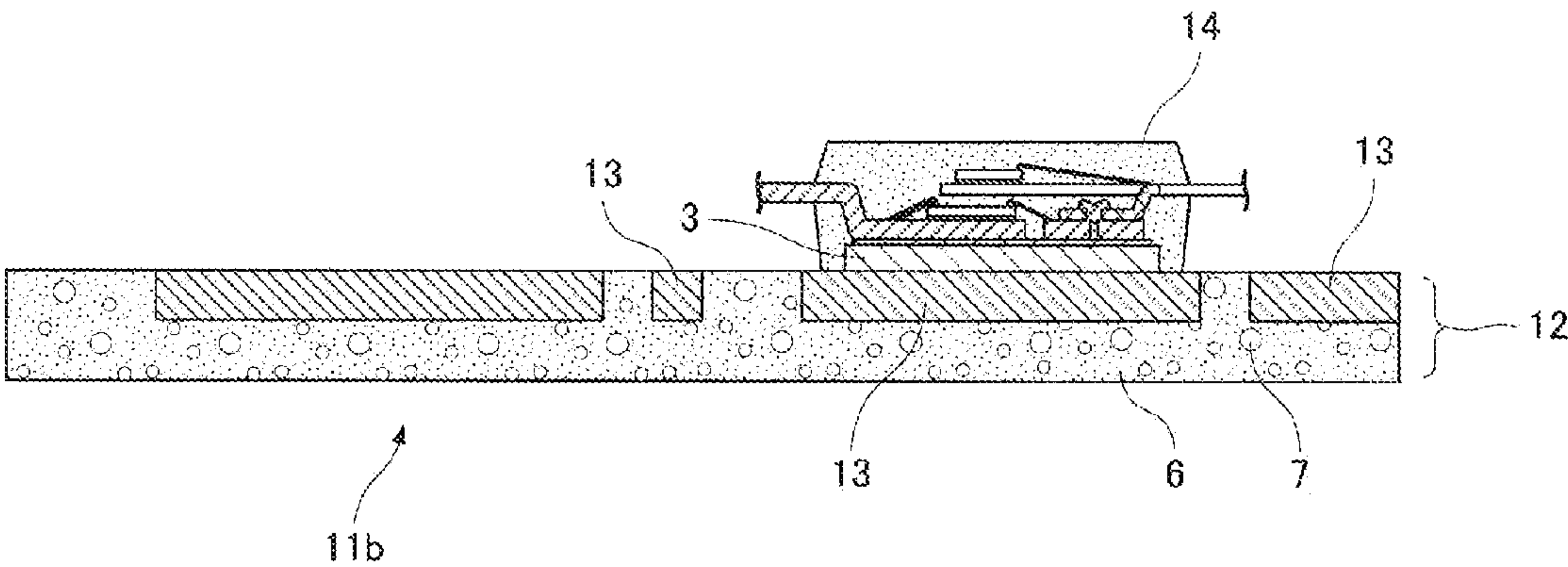


FIG. 6

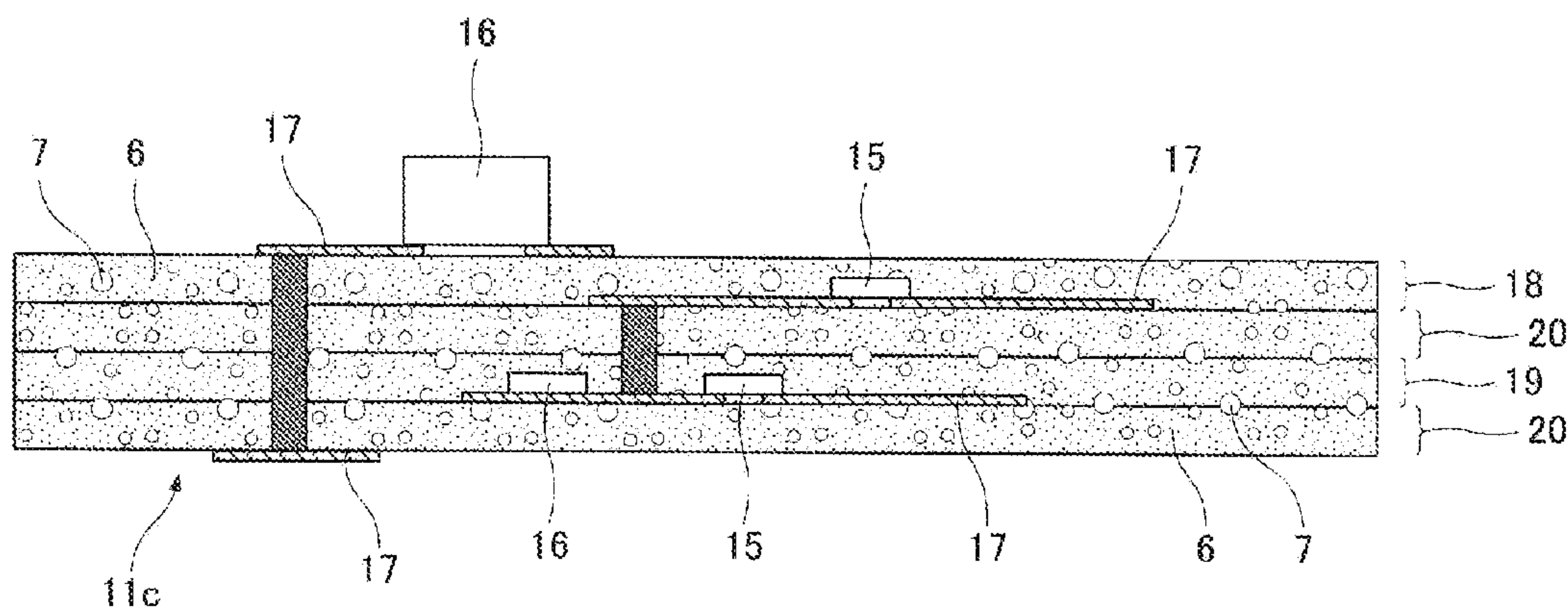


FIG. 7

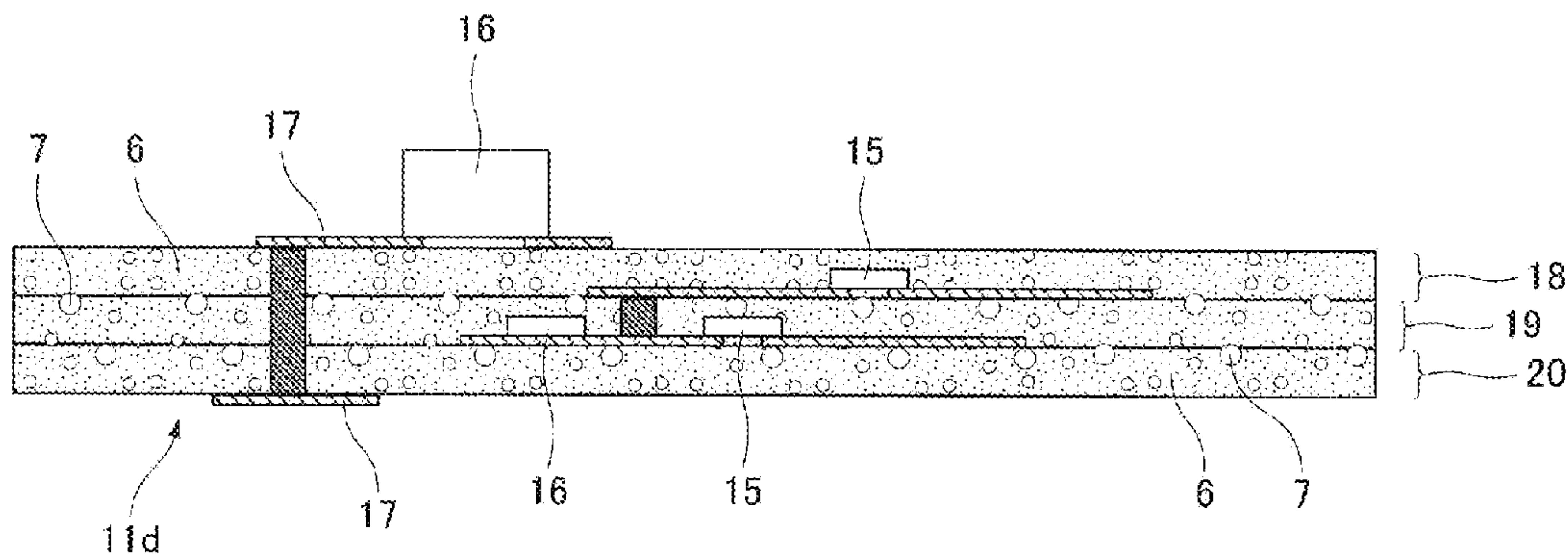


FIG. 8

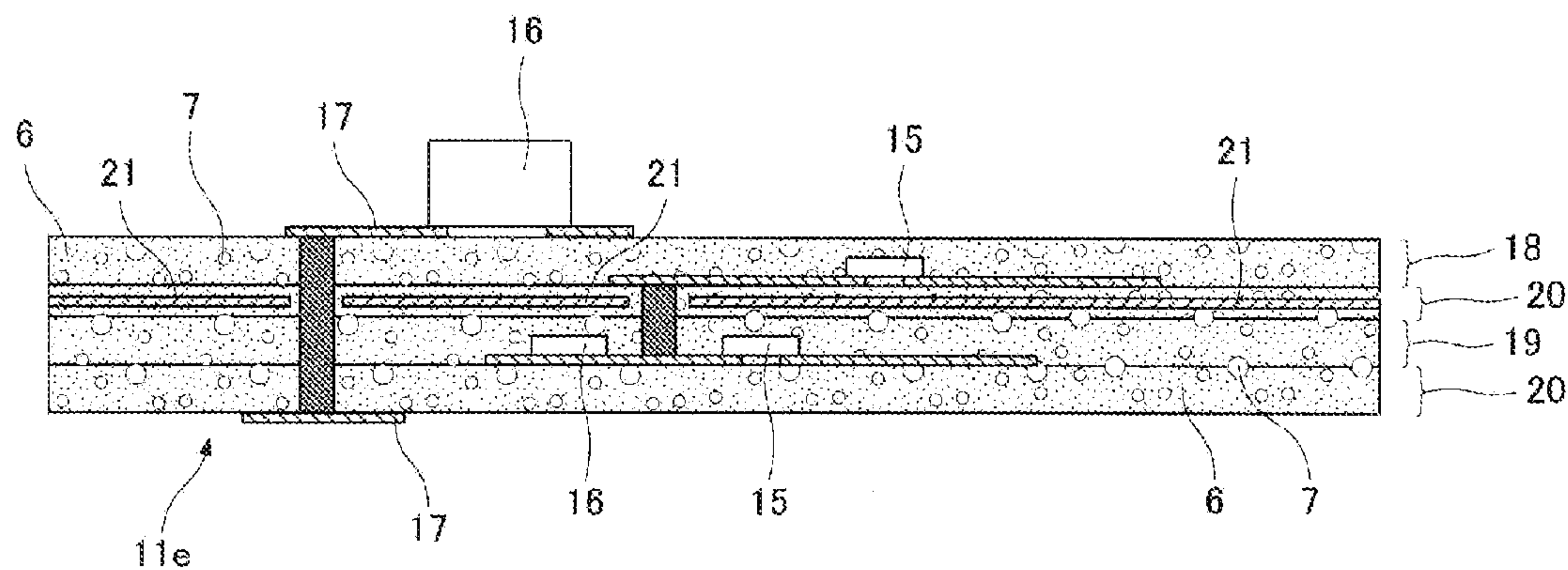


FIG. 9 PRIOR ART

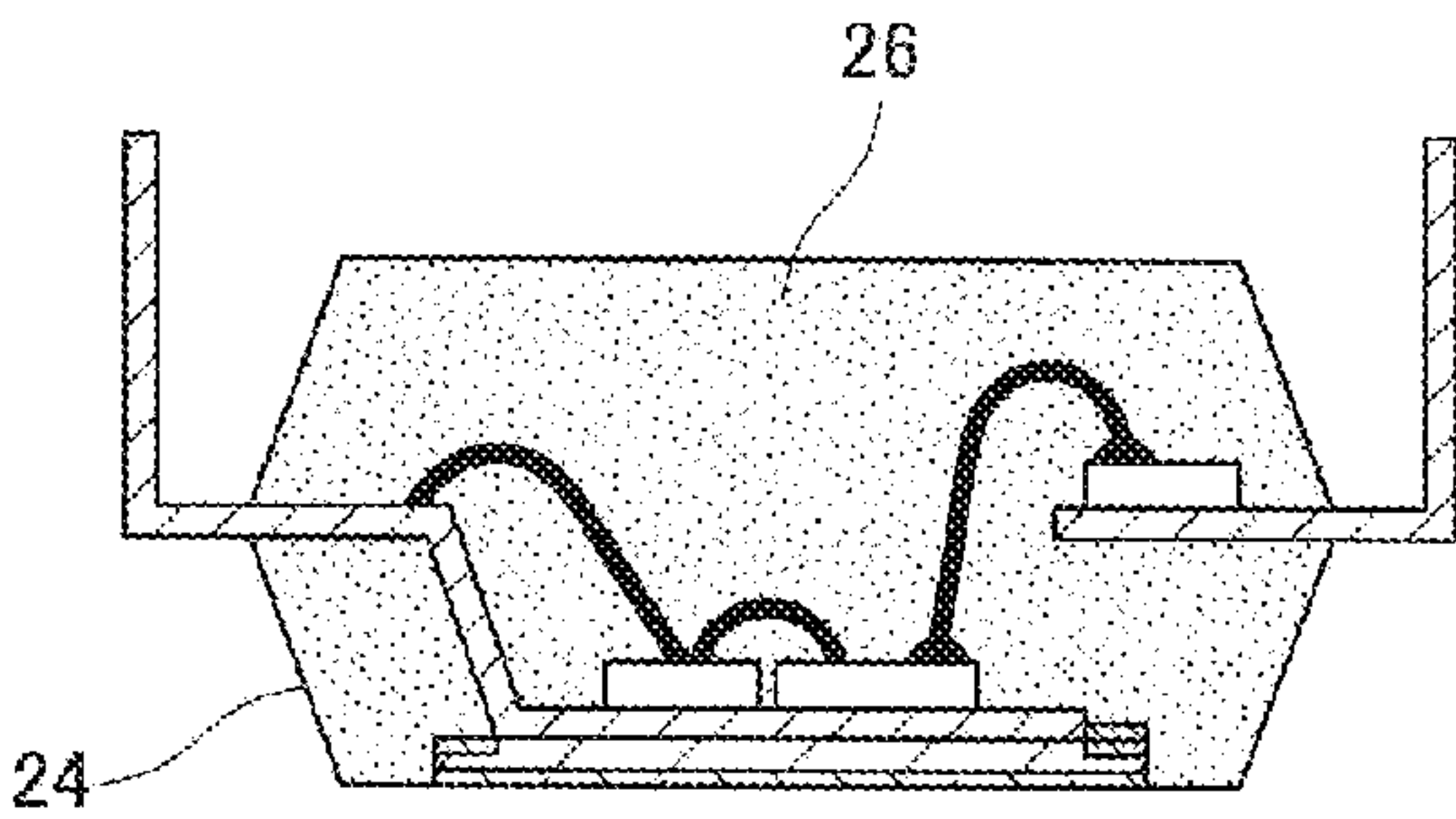


FIG. 10 PRIOR ART

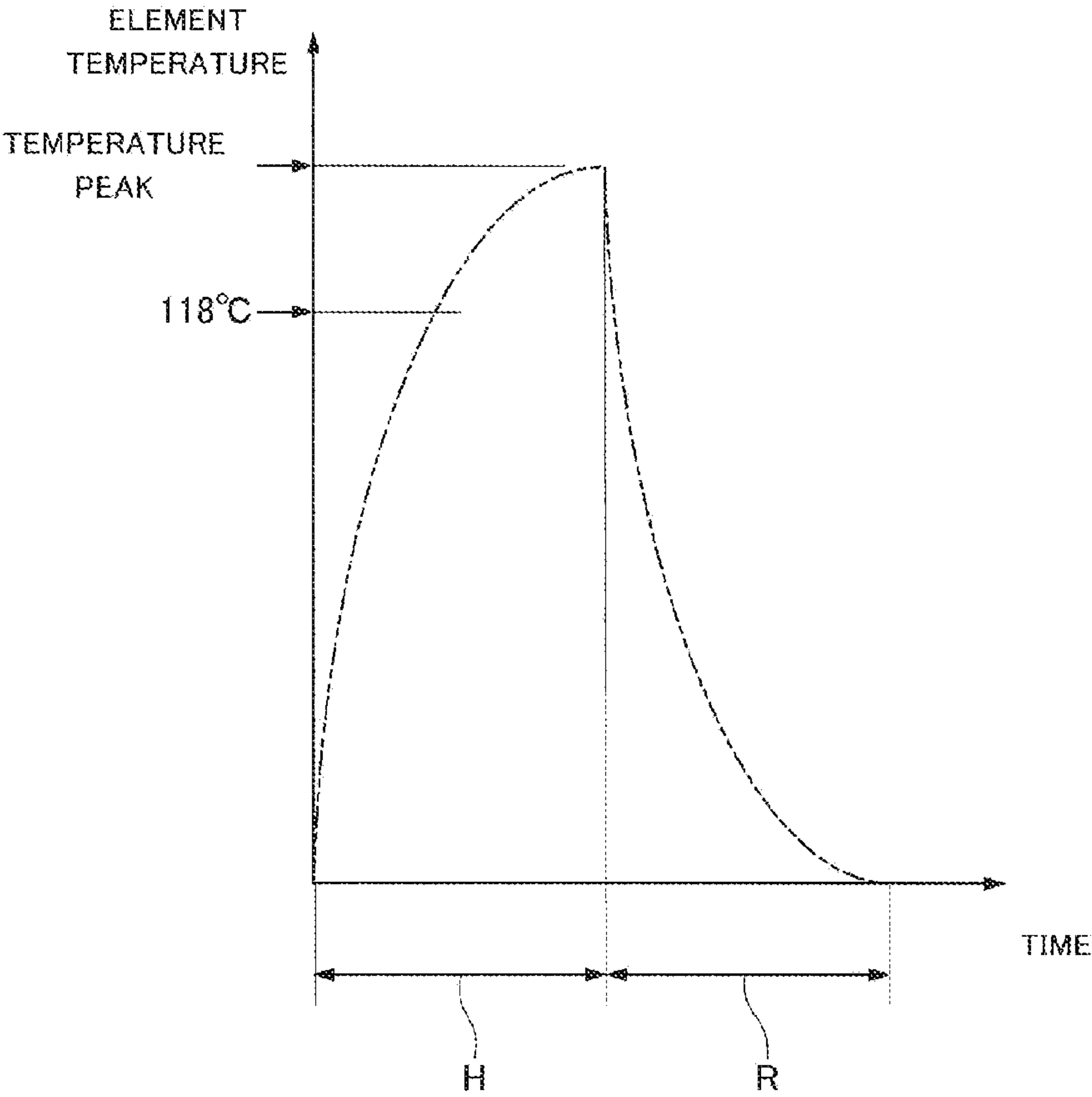


FIG. 11 PRIOR ART

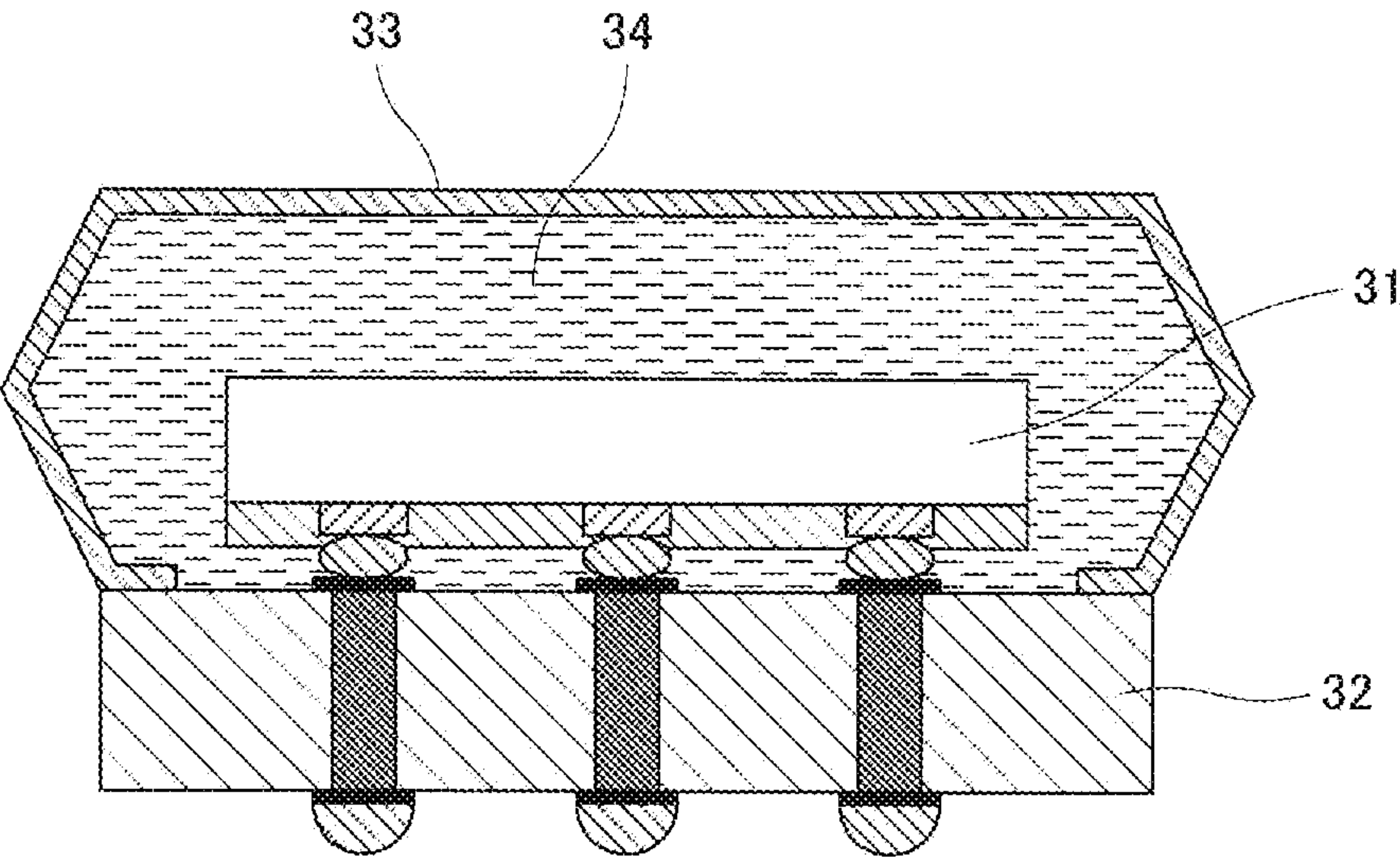
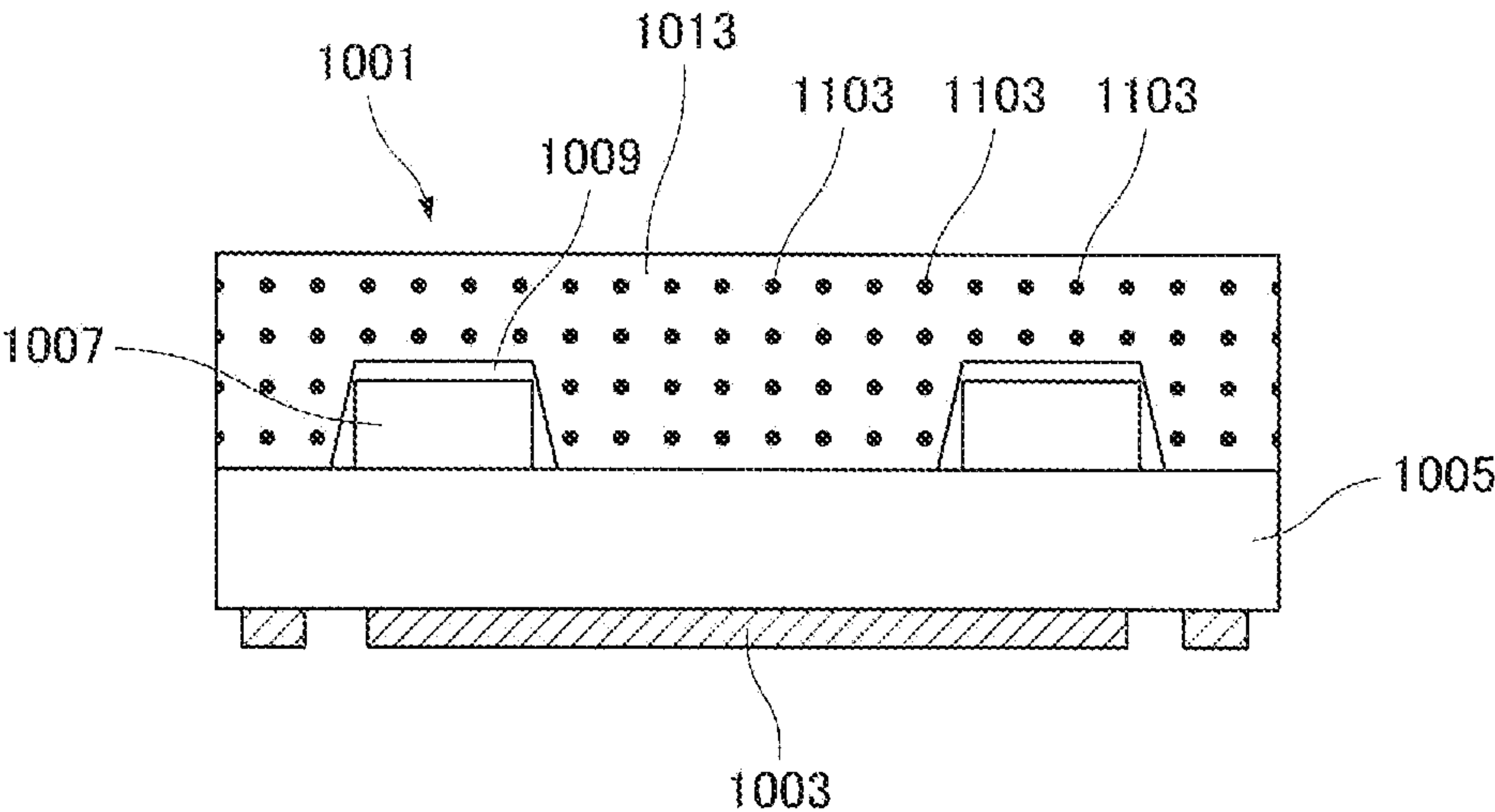


FIG. 12 PRIOR ART



ELECTRICAL COMPONENT RESIN, SEMICONDUCTOR DEVICE, AND SUBSTRATE

TECHNICAL FIELD

[0001] The present invention relates to an electrical component resin, a semiconductor device including a semiconductor element molded with a resin, and a substrate.

BACKGROUND ART

[0002] For example, inverter controllers with smaller sizes and lighter weights have been demanded. Accordingly, resin molding semiconductor devices mounted in inverter controllers have been reduced in size and weight. Moreover, for resin molding semiconductor devices including power semiconductor elements or substrates having electrical components such as semiconductor devices, a high withstand voltage between adjacent electrodes or higher heat dissipation has been demanded.

[0003] In a resin molding semiconductor device, heat dissipation from a chip to the top surface of a package is restricted by the heat conductivity of a resin used for molding. Thus, in Patent Literature 1, as shown in FIG. 11, a metallic case 33 covering a chip 31 is mounted on a substrate 32 and is filled with a coolant 34 in the metallic case 33. In the resin molding semiconductor device of Patent Literature 1, heat generated from the chip 31 is transmitted to the case 33 by the heat convection of the coolant 34, achieving higher heat conduction to the outside than in a conventional resin molding semiconductor device.

[0004] In a cooling system for a pulsed power semiconductor described in Patent Literature 2, heat is absorbed by a molding material containing microcapsules of a phase-change-material. FIG. 12 is a schematic cross-sectional view illustrating a semiconductor device 1001 disclosed in Patent Literature 2. The semiconductor device 1001 includes a radiator plate 1003, a substrate 1005, and elements 1007. The elements 1007 mounted on the substrate 1005 are molded with a molding material 1013. The molding material 1013 contains microcapsules of a phase-change-material 1103. For example, epoxy resin is disclosed as the molding material 1013. A heat conducting material 1009 is applied between the element 1007 and the molding material 1013. The elements 1007 and the molding material 1013 are thermally coupled by the heat conducting material 1009.

CITATION LIST

Patent Literature

[0005] Patent Literature 1: Japanese Patent Laid-Open No. 2008-4688

[0006] Patent Literature 2: the specification of U.S. Pat. No. 6,848,500

SUMMARY OF INVENTION

Technical Problem

[0007] However, in the resin molding semiconductor device of Patent Literature 1, the case 33 may be filled with the coolant 34 by a complicated process. Since the coolant 34 is a fluorine-based inert liquid or ethanol having a high dielectric constant, heat dissipation and a dielectric-strength may be limited.

[0008] The semiconductor device in Patent Literature 2 is configured regardless of a dielectric-strength, making it difficult to obtain a high withstand voltage.

[0009] An object of the present invention is to provide an electrical component resin, a semiconductor device, and a substrate that can obtain satisfactory heat dissipation and a high withstand voltage.

Solution to Problem

[0010] In order to solve the problem, an electrical component resin according to the present invention is composed of an electrical insulating resin containing a filler, the filler is formed by enclosing a phase-change-material that changes a dielectric-strength by phase-change by heat absorption, in a capsule of electrical insulation.

[0011] Moreover, in order to solve the problem, a semiconductor device according to the present invention includes a semiconductor element molded with the electrical component resin.

[0012] Furthermore, in order to solve the problem, a substrate according to the present invention has a conductor pattern on a base substrate composed of the electrical component resin.

[0013] Moreover, in order to solve the problem, another substrate according to the present invention includes an electric circuit composed of layers of the electrical component resin containing one of an electrical component and a conductor pattern, the layers being stacked sequentially or with an intermediate layer interposed between the layers.

Advantageous Effect of Invention

[0014] The present invention can provide an electrical component resin, a semiconductor device, and a substrate that can achieve satisfactory heat dissipation and a high withstand voltage.

BRIEF DESCRIPTION OF DRAWINGS

[0015] FIG. 1 is an enlarged cross-sectional view illustrating a semiconductor device according to a first embodiment of the present invention.

[0016] FIG. 2 is an enlarged cross-sectional view illustrating a filler according to the first embodiment of the present invention.

[0017] FIG. 3 is a temperature characteristic diagram of heat absorption and heat dissipation when heat is generated from a semiconductor element according to the first embodiment of the present invention.

[0018] FIG. 4 is an enlarged cross-sectional view illustrating a first example of a substrate according to a second embodiment of the present invention.

[0019] FIG. 5 is an enlarged cross-sectional view illustrating a substrate of a second example.

[0020] FIG. 6 is an enlarged cross-sectional view illustrating a substrate of a third example.

[0021] FIG. 7 is an enlarged cross-sectional view illustrating a substrate of a fourth example.

[0022] FIG. 8 is an enlarged cross-sectional view illustrating a substrate of a fifth example.

[0023] FIG. 9 is an enlarged cross-sectional view illustrating a semiconductor device according to the related art.

[0024] FIG. 10 is a temperature characteristic diagram of heat absorption and heat dissipation when heat is generated from a semiconductor element according to the related art.

[0025] FIG. 11 is an enlarged cross-sectional view illustrating a semiconductor device of Patent Literature 1.

[0026] FIG. 12 is an enlarged cross-sectional view illustrating a semiconductor device of Patent Literature 2.

DESCRIPTION OF EMBODIMENTS

First Embodiment

[0027] FIG. 1 illustrates a first semiconductor device according to a first embodiment. The first semiconductor device in FIG. 1 is a resin molding semiconductor device that is an example of a structure molded with an electrical insulating resin for an electrical component.

[0028] The first semiconductor device includes a power element T1 fixed on a first lead frame 1, and a control element T2 fixed on a second lead frame 2. The first and second lead frames 1 and 2 are fixed on insulating sheets 5a and 5b above the top surface of a radiator plate 3. The power element T1 and the control element T2 are examples of a semiconductor element. The insulating sheets 5a and 5b are made of, for example, electrical insulating materials with heat conduction. The insulating sheets 5a and 5b each have a three-layer structure including an electrical insulating layer sandwiched between adhesive layers. A package 4 joins the first lead frame 1 and the second lead frame 2 and protects the power element T1 and the control element T2. The package 4 contains a first resin 6 that is an example of a thermosetting resin such as epoxy. The first resin 6 contains a filler 7 that is a feature of the present invention. The filling factor of the filler 7 in the first resin 6 of the first embodiment is set at 20% to 80% depending upon a heat absorbing or insulating characteristic and the material characteristic of the first resin 6, which will be described later.

[0029] According to the present invention, the resin for molding (e.g., the first resin 6) desirably has moldability and a higher dielectric constant than the filler 7. The use of the resin and the filler 7 can achieve a dielectric-strength characteristic according to an instant voltage increase in the semiconductor device.

[0030] As shown in FIG. 2(a), the filler 7 is configured such that an electrical insulating capsule 9 contains a phase-change-material 8 that phase-changes when absorbing ambient heat. FIG. 2(a) is an enlarged cross-sectional view illustrating the filler at room temperature. At room temperature in FIG. 2(a), the phase-change-material 8 is solid. FIG. 2(b) illustrates the filler 7 that absorbs heat generated from the power element T1 and so on. When heat is absorbed as shown in FIG. 2(b), the phase-change-material 8 is liquid.

[0031] Specifically, the filler 7 includes the silica (SiO₂) capsule 9 enclosed with erythritol serving as the phase-change-material 8. In the first embodiment, the capsule 9 has a diameter of 60 μm. According to the present invention, the capsule 9 can be 2 μm to 100 μm in diameter. Furthermore, the capsule 9 has a thickness of 0.1 μm to 20 μm. In the first embodiment, the phase change material 8 in the capsule 9 is erythritol whose volume is 60% of the capacity of the capsule 9. The volume of erythritol serving as the phase change material 8 is desirably 30% to 70% relative to the capacity of the capsule 9. A gas layer 10 is provided between the capsule 9 and the phase change material 8. The gas layer 10 may contain various kinds of inert gas or air. In the first embodiment, the gas layer 10 contains air having an excellent insulating property.

[0032] As shown in FIG. 2(a), erythritol is solid at room temperature and phase-changes into a liquid state at a melting point of 118° C. The melting point of erythritol, which is an example of the phase change material 8, is lower than 125° C. that is the heatproof temperature of the power element T1. In other words, in the first embodiment, the melting point of the phase change material 8 is lower than the heatproof temperature of the power element T1. In the present invention, the melting point of the phase change material 8 is lower than the heatproof temperature of the power element T1 and thus heat is absorbed by the phase change material 8, reducing the possibility that the temperature of the first semiconductor device may exceed the heatproof temperature of the power element T1. The capsule 9 is resistant to the expansion of the molten phase change material 8, preventing the phase change material 8 from flowing to the outside.

[0033] Referring to FIGS. 3, 9, and 10, the temperature of the first semiconductor device according to the present invention is less likely to increase than in the related art.

[0034] FIG. 3 is a temperature characteristic diagram of heat absorption and heat dissipation when heat is generated from the semiconductor element according to the first embodiment of the present invention. FIG. 9 is an enlarged cross-sectional view illustrating a semiconductor device according to the related art. FIG. 10 is a temperature characteristic diagram showing heat absorption and heat dissipation when heat is generated from a semiconductor element according to the related art.

[0035] In the first semiconductor device according to the first embodiment, the first resin 6 containing the filler 7 is used. Thus, when heat is generated from the power element T1 so as to increase the temperature of the first resin 6 in the first semiconductor device according to the first embodiment, the temperature rises as indicated by a solid line in FIG. 3 and then reaches 118° C., the melting point of erythritol. At this point, the temperature stops rising. This is because the phase change material 8 (erythritol) melts and phase-changes from a solid state to a liquid state, and then the phase change material 8 absorbs the heat of fusion according to the phase change so as to rapidly reduce the inclination of the temperature increase around the melting point of erythritol. Thus, the first semiconductor device according to the first embodiment has characteristics shown in FIG. 3. Furthermore, the time of arrival at a temperature peak exceeding the heat proof temperature of the power element T1 can be delayed.

[0036] In other words, in the first semiconductor device according to the present invention, microcapsules containing the phase change material are mixed in a desired resin, achieving satisfactory heat dissipation and an improved dielectric-strength. Specifically, the satisfactory heat dissipation is heat absorption when the first semiconductor device rapidly increases in temperature. To improve a dielectric-strength is to prevent a concentration of an electric field in a space and reduce a dielectric constant in response to a voltage increase, which is disadvantageous when the first semiconductor device is momentarily driven.

[0037] On the solid line in FIG. 3, “period H” is a heat generation period having a power loss and “period R” is a heat dissipation period during which a temperature stops rising and then decreases in response to heat generation. Moreover, in “period H1”, a part of the period H, heat is absorbed according to a phase change of the phase change material 8 so as to stop a temperature increase. In “period H2”, a part of the period H, heat cannot be absorbed by the phase change mate-

rial 8 and thus the temperature of the first semiconductor device increases. The relationship between “period H1” and “period H2” depends upon the amount of the phase change material 8 in the filler 7. As the amount of the phase change material 8 increases, “period H1” is extended.

[0038] A virtual line (chain double-dashed line) in FIG. 3 indicates a temperature increase in the related art of FIG. 10. Specifically, the virtual line in FIG. 3 indicates a temperature increase of a comparative example in which a first resin does not contain the filler 7.

[0039] The use of the first resin 6 in the first embodiment can prevent the package 4 from reaching the temperature peak, improving reliability according to a temperature increase of the semiconductor device. For example, in the case of a semiconductor device for a driver of a motor driving inverter, the first resin 6 of the first embodiment can reduce the influence of a temperature when a system counter electromotive force is applied. This can improve the performance of a motor even in the case of an inverter having the same outside dimensions as in the related art.

[0040] Furthermore, the first resin 6 containing the filler 7 of the present invention has a high dielectric-strength during heat generation. The dielectric-strength will be compared with that in a comparative example of the semiconductor device according to the related art in FIG. 9. The semiconductor device according to the related art in FIG. 9 includes a package 24 that contains a thermosetting resin 26 only composed of epoxy.

[0041] While epoxy has a dielectric constant of “4”, the capsule 9 (silica) of the filler 7 has a dielectric constant of “2.5” and the phase change material 8 (erythritol) has a dielectric constant of “1.5”. Thus, in the semiconductor device according to the first embodiment, the dielectric constant of the filler 7 is lower than that of epoxy, thereby improving a dielectric-strength between electrodes in the semiconductor device.

[0042] Comparing with ethanol used as a coolant 34 in the semiconductor device of Patent Literature 1, ethanol having a dielectric constant of “24” allows the semiconductor device of the first embodiment to considerably increase a dielectric-strength between the electrodes.

Second Embodiment

[0043] FIGS. 4 to 8 illustrate substrates as examples of a structure that contains the electrical component resin according to the first embodiment as an insulating resin. Structures in FIGS. 4 to 8 will be described as first to fifth examples. The same constituent elements as in the first embodiment are indicated by the same reference numerals.

First Example

[0044] A substrate 11a according to a first example in FIG. 4 has a conductor pattern 13 on the top surface of a base substrate 12. An electrical insulating first resin 6 constituting the base substrate 12 contains a filler 7. As in the first embodiment, the filler 7 includes an insulating capsule 9 containing a phase change material 8 that phase-changes when absorbing heat. A semiconductor device 14 includes a radiator plate 3 thermally coupled to the conductor pattern 13 to dissipate heat through the conductor pattern 13.

[0045] An electrical component resin constituting the base substrate 12 is the insulating first resin 6 containing the filler 7. Thus, heat from the semiconductor device 14 can be dissi-

pated through the base substrate 12 by the absorption of heat of fusion by the phase change material 8. This can delay the time of arrival at a temperature peak exceeding the heatproof temperature of the semiconductor device 14, protecting the semiconductor device 14 from thermal destruction.

Second Example

[0046] A substrate 11b according to a second example in FIG. 5 is different from the first example only in that the conductor pattern 13 is embedded in the base substrate 12.

Third Example

[0047] A substrate 11c according to a third example in FIG. 6 is a multi-layer substrate containing an inner layer circuit. The substrate 11c includes layers 18 and 19 stacked with an intermediate layer 20 interposed between the layers 18 and 19, which constitutes an electric circuit. The layers 18 and 19 each contain an electrical component of an active element 15 or a passive element 16 or a conductor pattern 17. In the third example, the first resin 6 constituting at least one of the layers 18 and 19 and the intermediate layer 20 contains the filler 7.

[0048] The electrical component resin constituting the layers 18 and 19 and the intermediate layer 20 contains the filler 7. Thus, heat of fusion is absorbed by the phase change material 8 in the filler 7 so as to protect the electrical component from thermal destruction and obtain a promising shielding effect in a high-frequency band. The shielding effect in the high-frequency band is a lower dielectric constant in the filler 7 than in the first resin 6.

Fourth Example

[0049] A substrate 11d according to a fourth example in FIG. 7 is different from the third example only in that the layers 18 and 19 are stacked without the intermediate layer 20 interposed therebetween.

Fifth Example

[0050] A substrate 11e according to a fifth example in FIG. 8 is different from the third example only in that the intermediate layer 20 contains a shield layer 21 that blocks the passage of an electromagnetic wave.

[0051] In the foregoing embodiments and examples, the capsule 9 of the filler 7 is made of silica (SiO₂). The capsule 9 of the filler 7 may be made of organic materials such as electrical insulating melamine and silicone.

[0052] In the foregoing embodiments, the phase change material 8 of the filler 7 is erythritol that phase-changes at a temperature lower than the heatproof temperature of an electrical component serving as an element to be used. Other sugar alcohols may be used with a dielectric constant smaller than that of the contained resin. For example, the other sugar alcohols may be sorbitol or xylitol, or the phase change material 8 of the filler 7 may be phase change materials such as paraffin having a dielectric constant of about “2” and a melting point of about “70° C.” or polyethylene having a dielectric constant of about “2.3” and a melting point of about “125° C”. In any cases, the heat absorbing property and the heat dissipating property can be adjusted by an adjustment on an additive and the amount of the additive.

[0053] In the substrate or the multilayer substrate according to the foregoing embodiments, the filler 7 is contained over the substrate. The electrical insulating first resin containing

the filler 7 may be similarly used in preimpregnation as a matrix resin for impregnation of a base material, e.g., glass fibers or carbon fibers.

[0054] In the foregoing embodiments, the heat of fusion of the phase change material 8 is latent heat necessary for a phase change from solid to liquid. In the case of the phase change material 8 having an intermediate phase condition of solid and liquid, the heat of fusion may be only latent heat necessary for a phase change from solid to the intermediate phase condition or only latent heat necessary for a phase change from the intermediate phase condition to liquid.

[0055] In the foregoing embodiments, the filler 7 has the gas layer 10 between the phase change material 8 and the capsule 9. A liquid layer or a gel layer may be provided between the phase change material 8 and the capsule 9. In this case, the layer between the phase change material 8 and the capsule 9 requires a dielectric constant equal to or lower than that of the first resin 6, which is an example of resin.

INDUSTRIAL APPLICABILITY

[0056] The present invention contributes to a size reduction of various inverters for air conditioners requiring high power control, and an improvement of reliability of electrical structures under poor service conditions.

1. An electrical component resin composed of an electrical insulating resin containing a filler,

the filler is formed by enclosing a phase-change-material that changes a dielectric-strength by phase-change by heat absorption, in a capsule of electrical insulation.

2. The electrical component resin according to claim 1, wherein the phase-change-material that increases the dielectric-strength by phase-change by heat absorption.

3. The electrical component resin according to claim 1, wherein a gap between the phase-change-material and the

capsule in the filler is reduced when the phase-change-material phase-changes by heat absorption.

4. The electrical component resin according to claim 1, wherein the phase-change-material has a smaller dielectric constant than the resin.

5. The electrical component resin according to claim 1, wherein the phase-change-material phase-changes a phase change at a temperature lower than a heatproof temperature of an electrical component to be used.

6. The electrical component resin according to claim 1, wherein the capsule is made of silica, and the phase-change-material is a sugar alcohol.

7. The electrical component resin according to claim 1, wherein the phase-change-material is erythritol.

8. The electrical component resin according to claim 1, wherein the phase-change-material is one of sorbitol, xylitol, paraffin, and polyethylene.

9. A semiconductor device comprising a semiconductor element molded with the electrical component resin according to claim 1.

10. A substrate having a conductor pattern on a base substrate composed of the electrical component resin according to claim 1.

11. A substrate comprising an electric circuit composed of layers of the electrical component resin according to claim 1, the electrical component resin containing one of an electrical component and a conductor pattern, the layers being stacked sequentially or with an intermediate layer interposed between the layers.

12. The electrical component resin according to claim 2, wherein a gap between the phase-change-material and the capsule in the filler is reduced when the phase-change-material phase-changes by heat absorption.

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