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(19) **United States**(12) **Patent Application Publication**  
**Vidu et al.**(10) **Pub. No.: US 2013/0014799 A1**(43) **Pub. Date: Jan. 17, 2013**(54) **NANOSTRUCTURE AND PHOTOVOLTAIC  
CELL IMPLEMENTING SAME**(76) Inventors: **Ruxandra Vidu**, Citrus Heights, CA  
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**Jie-Ren Ku**, Davis, CA (US)(21) Appl. No.: **11/466,416**(22) Filed: **Aug. 22, 2006****Related U.S. Application Data**(60) Provisional application No. 60/710,097, filed on Aug.  
22, 2005, provisional application No. 60/710,262,  
filed on Aug. 22, 2005.**Publication Classification**(51) **Int. Cl.**  
**H01L 31/042** (2006.01)  
**H01L 31/0264** (2006.01)**H01L 31/18** (2006.01)**H01L 51/42** (2006.01)**H01L 31/048** (2006.01)**H01L 31/0352** (2006.01)(52) **U.S. Cl.** ..... **136/244**; 136/251; 136/252; 438/80;  
257/E31.032(57) **ABSTRACT**

Nanostructures and photovoltaic structures are disclosed. A nanostructure according to one embodiment includes an array of nanocables extending from a substrate, the nanocables in the array being characterized as having a spacing and surface texture defined by inner surfaces of voids of a template; an electrically insulating layer extending along the substrate; and at least one layer overlaying the nanocables. A nanostructure according to another embodiment includes a substrate; a portion of a template extending along the substrate, the template being electrically insulative; an array of nanocables extending from the template, portions of the nanocables protruding from the template being characterized as having a spacing, shape and surface texture defined by previously-present inner surfaces of voids of the template; and at least one layer overlaying the nanocables.

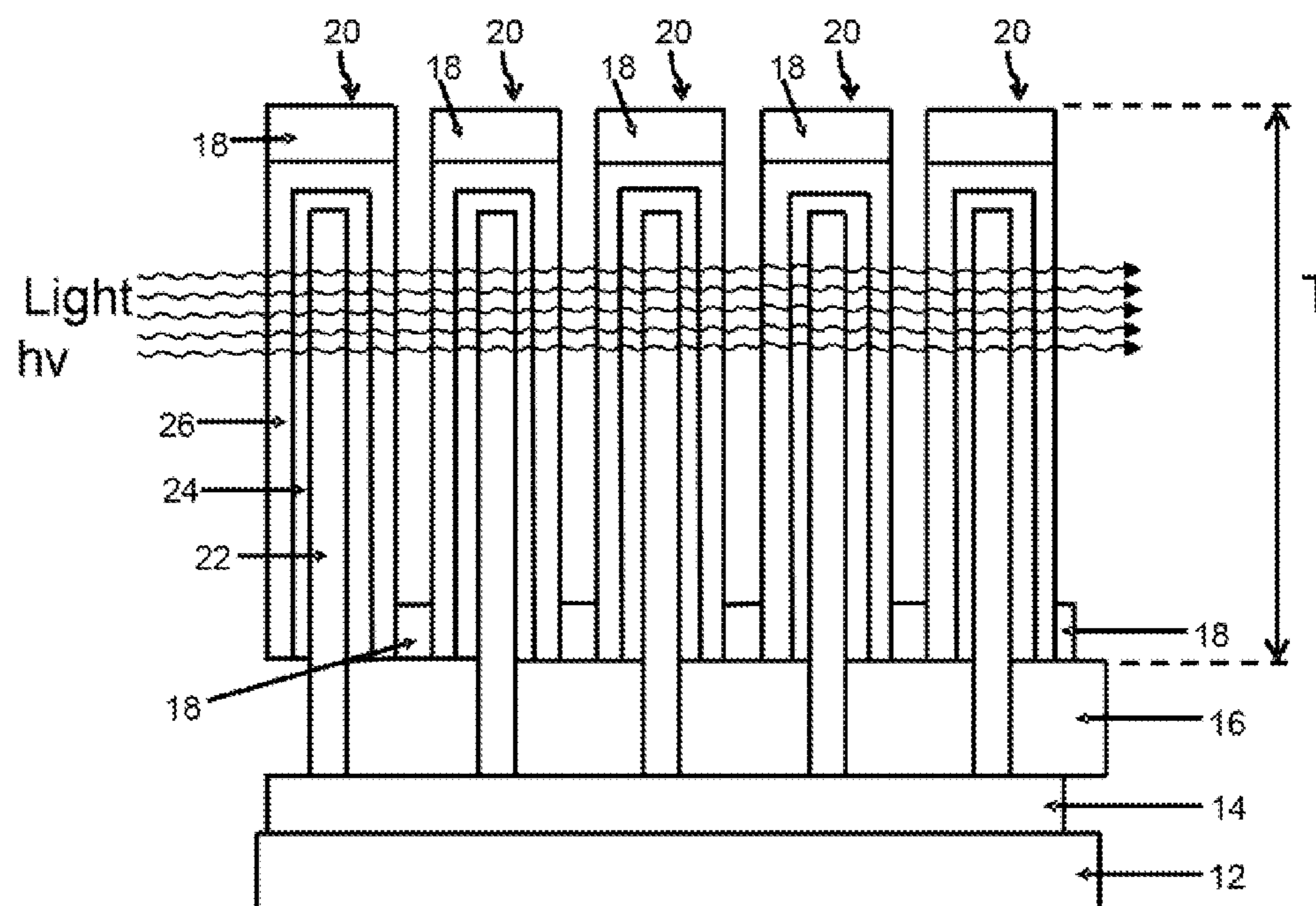


FIG. 1

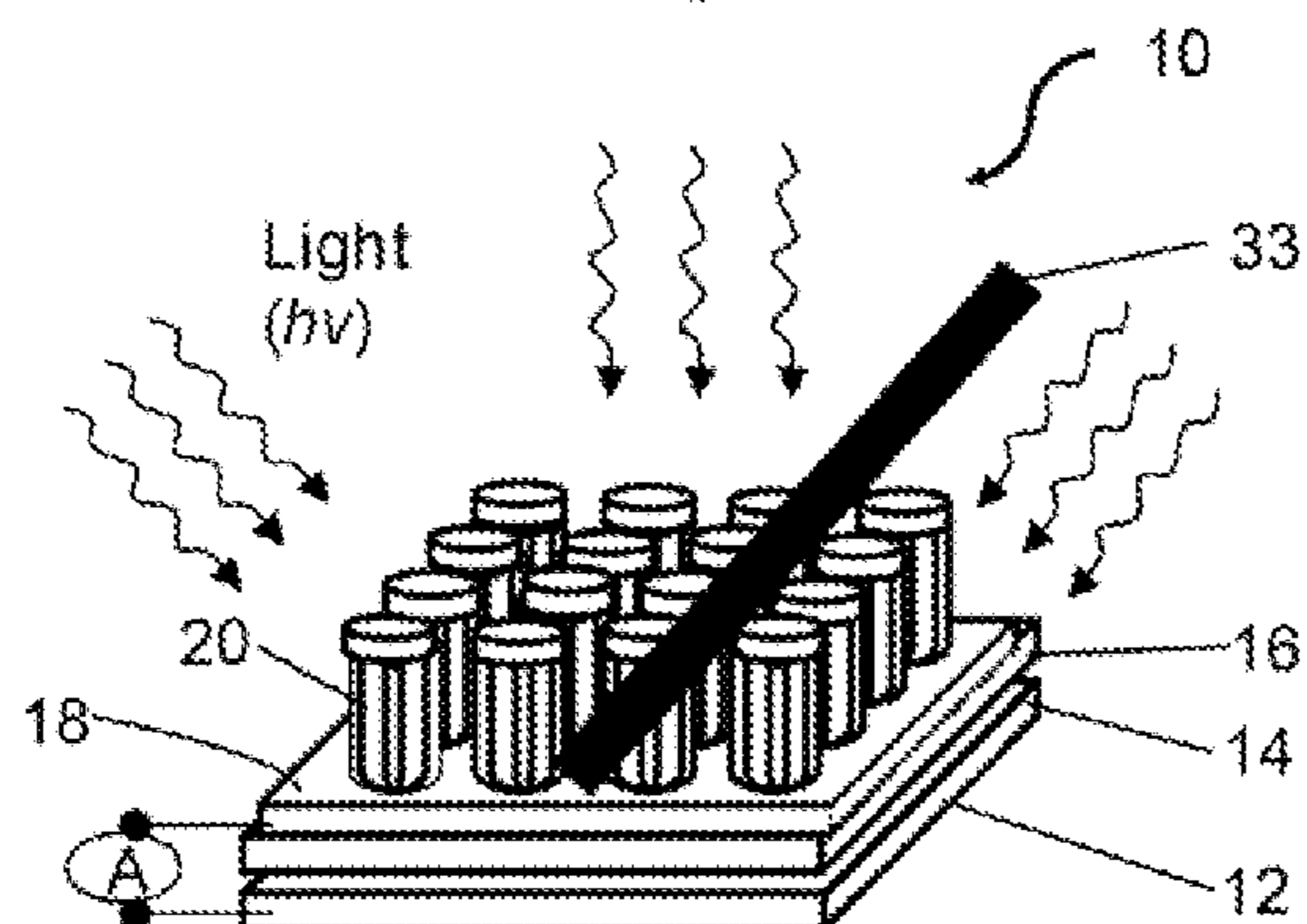
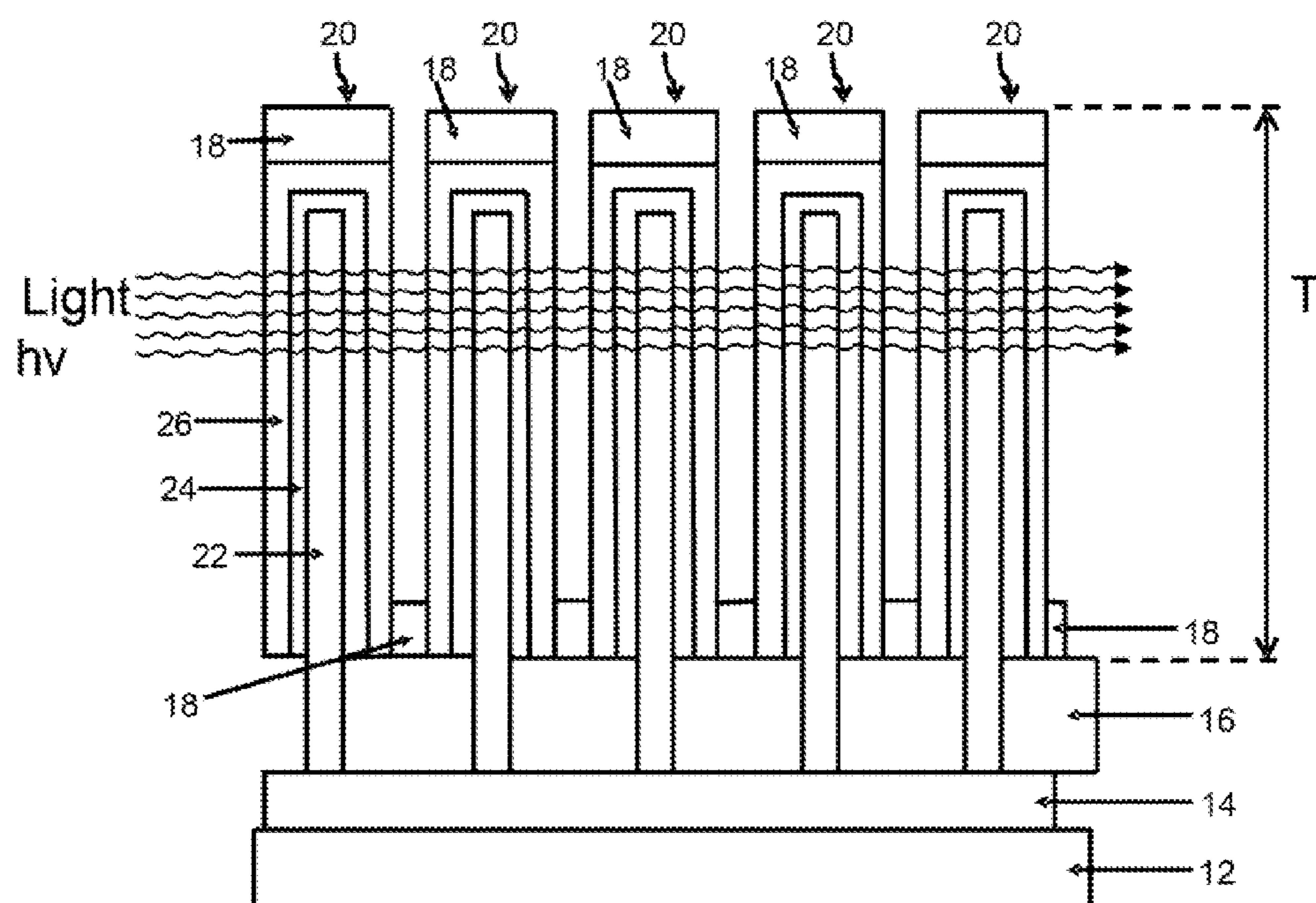


FIG. 2



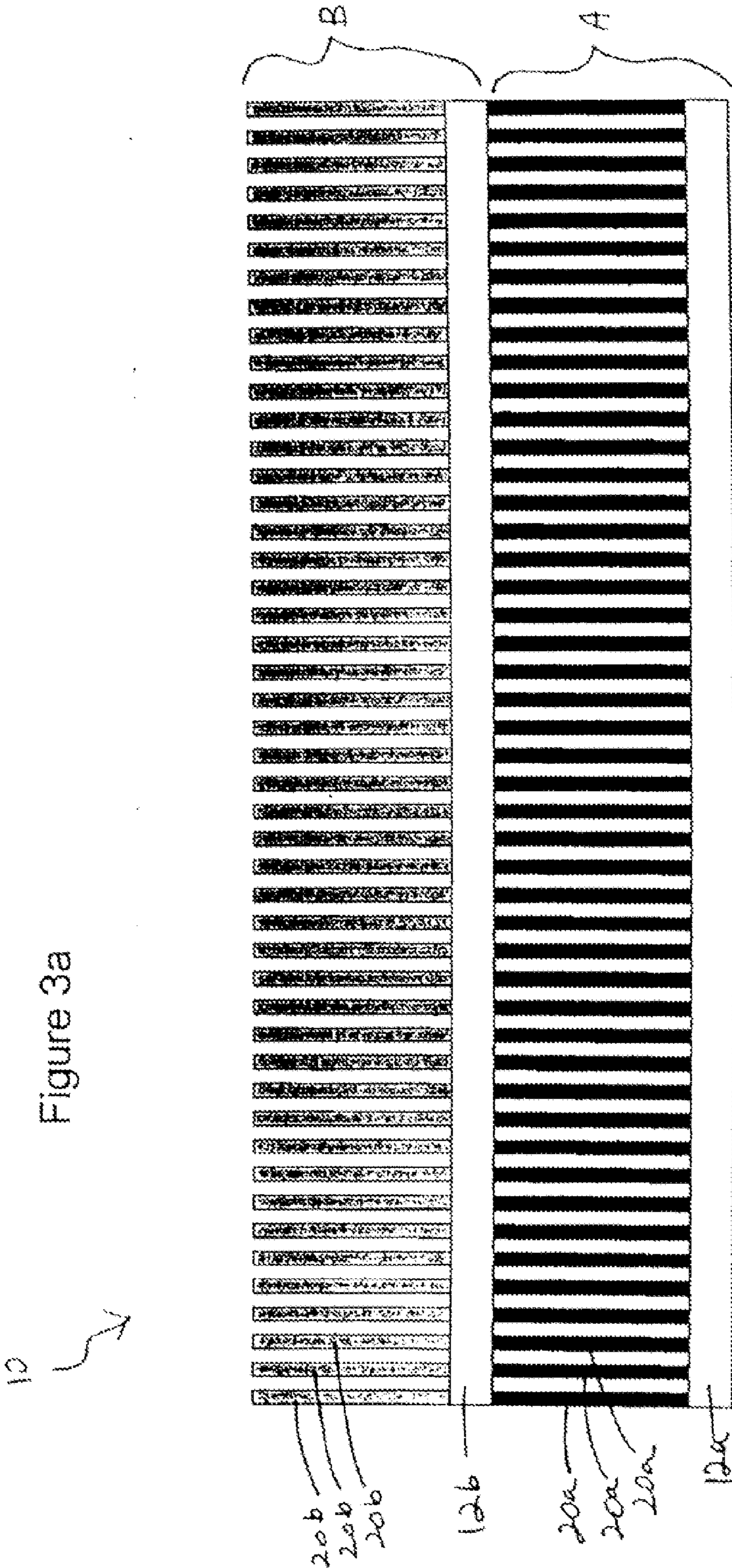
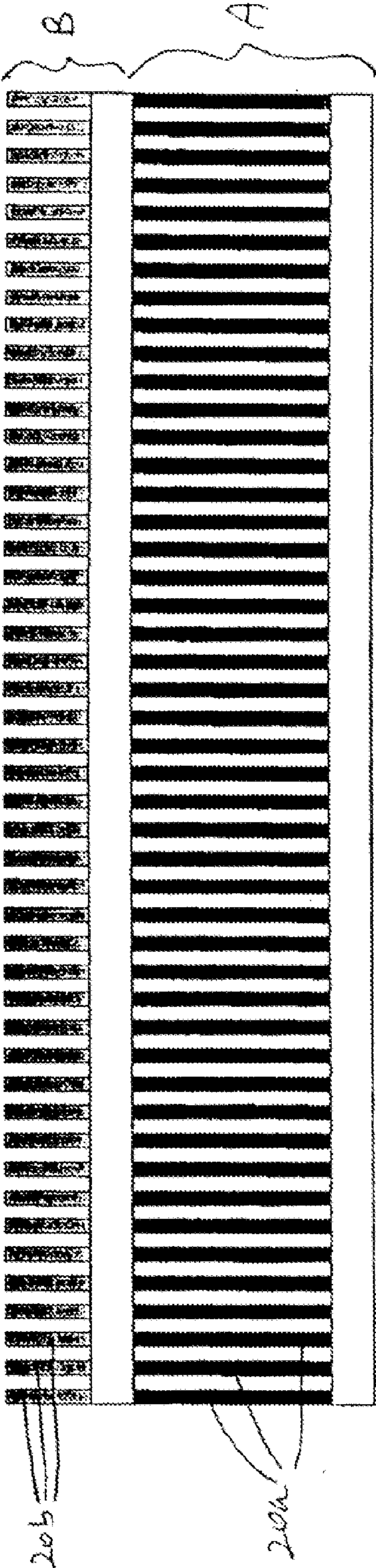
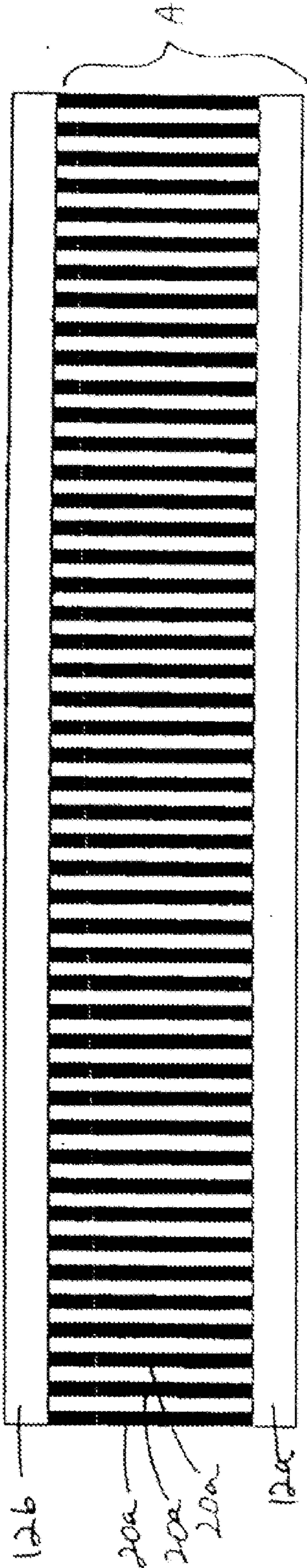


Figure 3b



10 ↗

Figure 3C



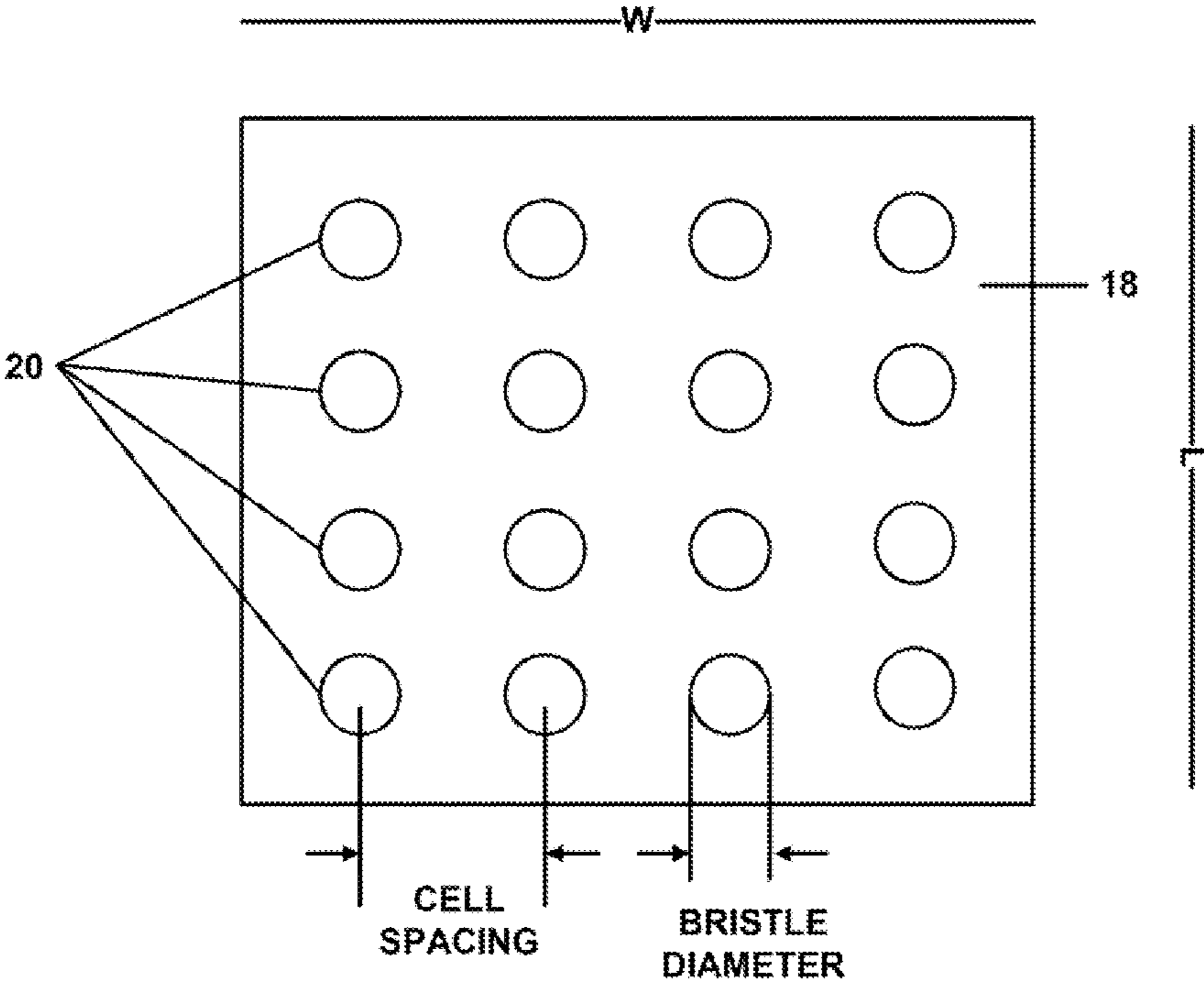


FIG. 4



FIG. 5A



FIG. 5B

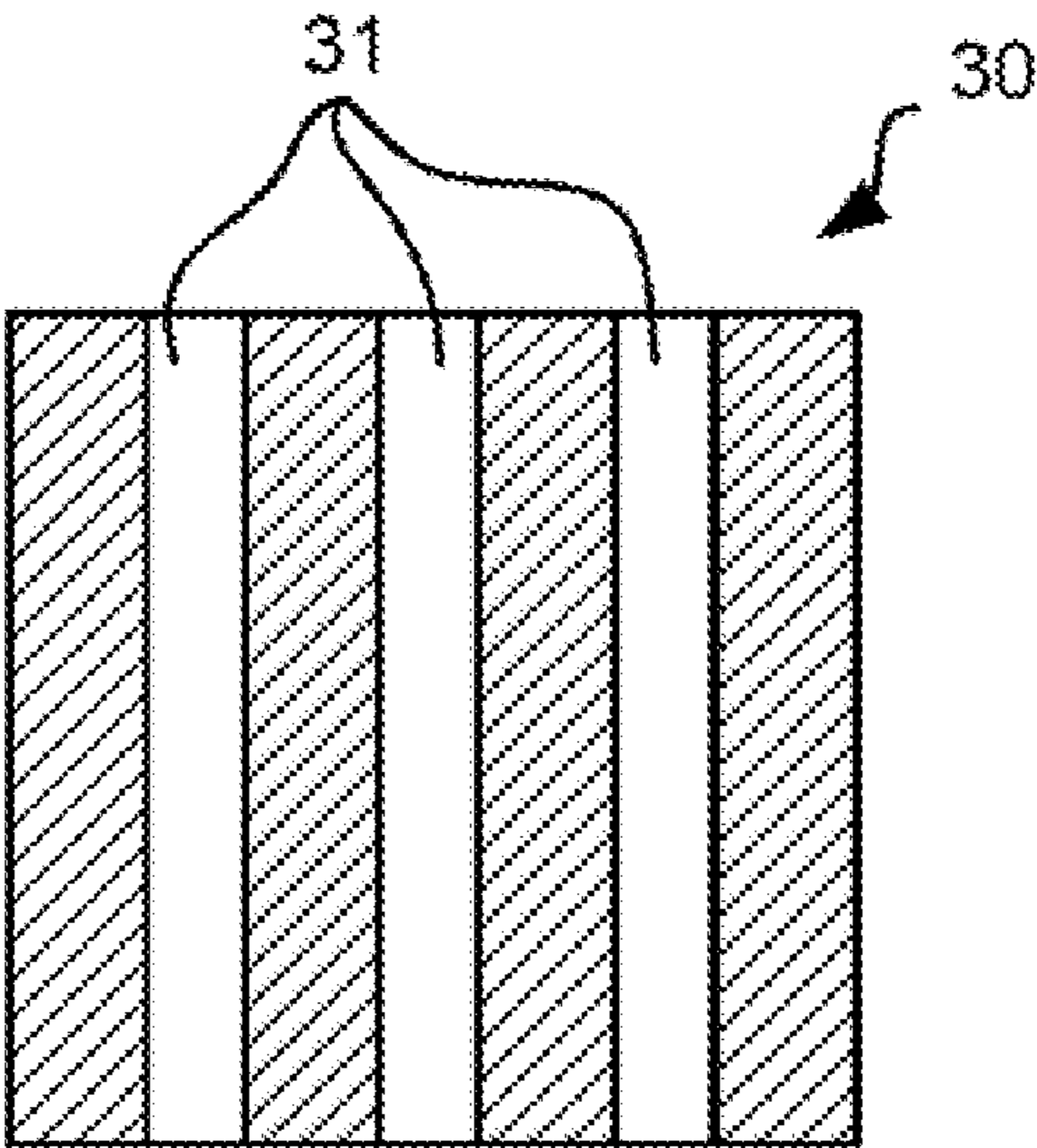


FIG. 5C

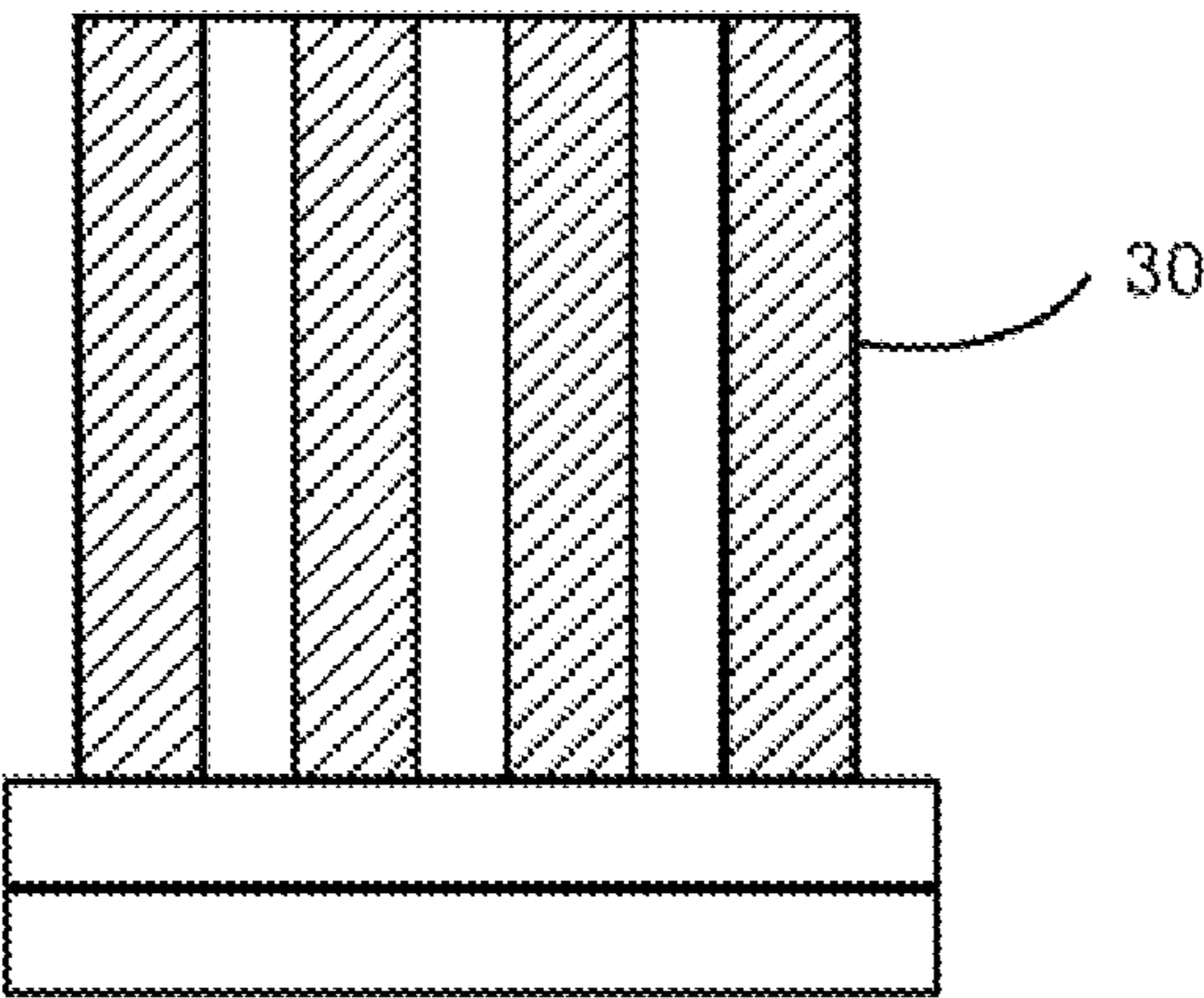


FIG. 5D

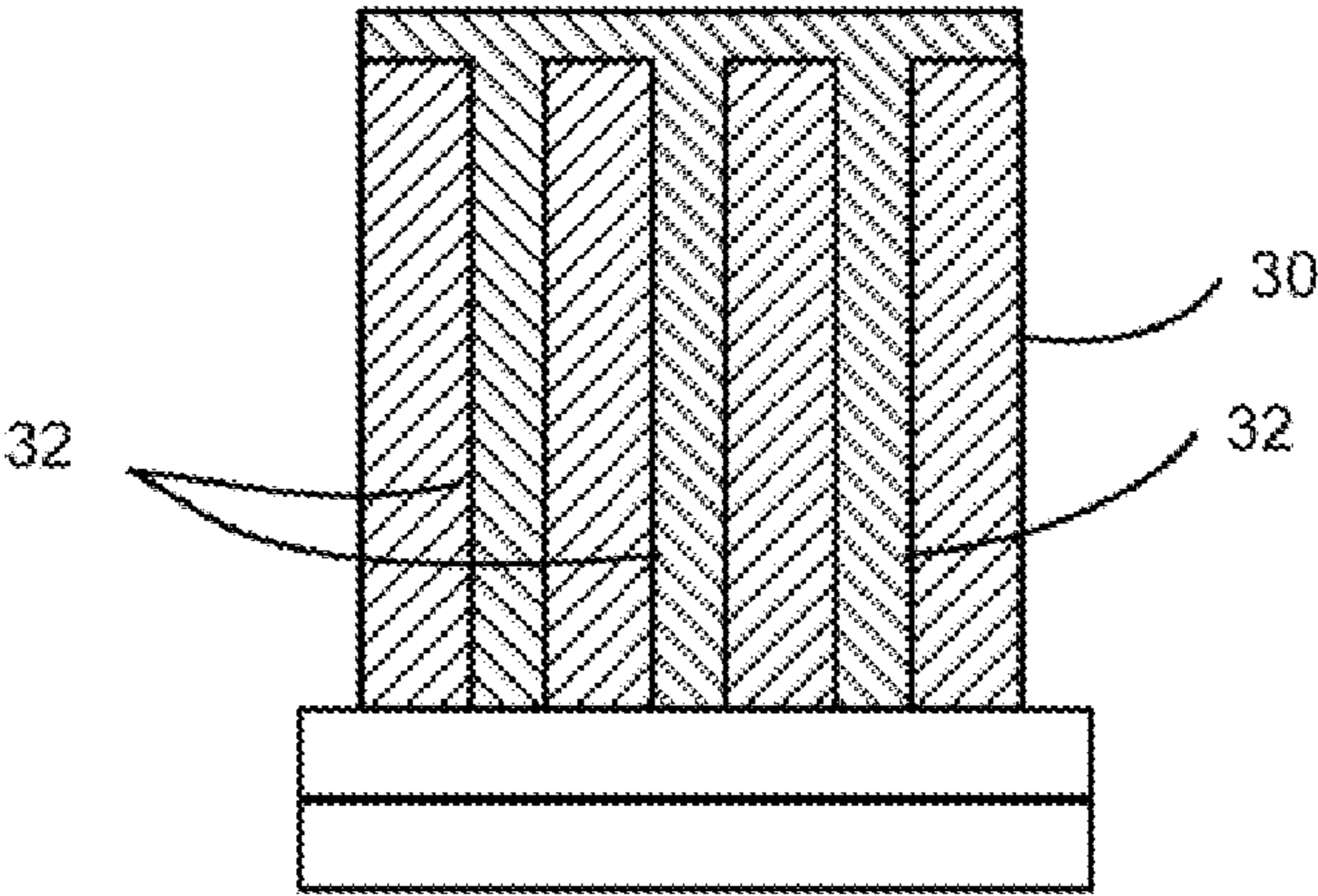


FIG. 5E

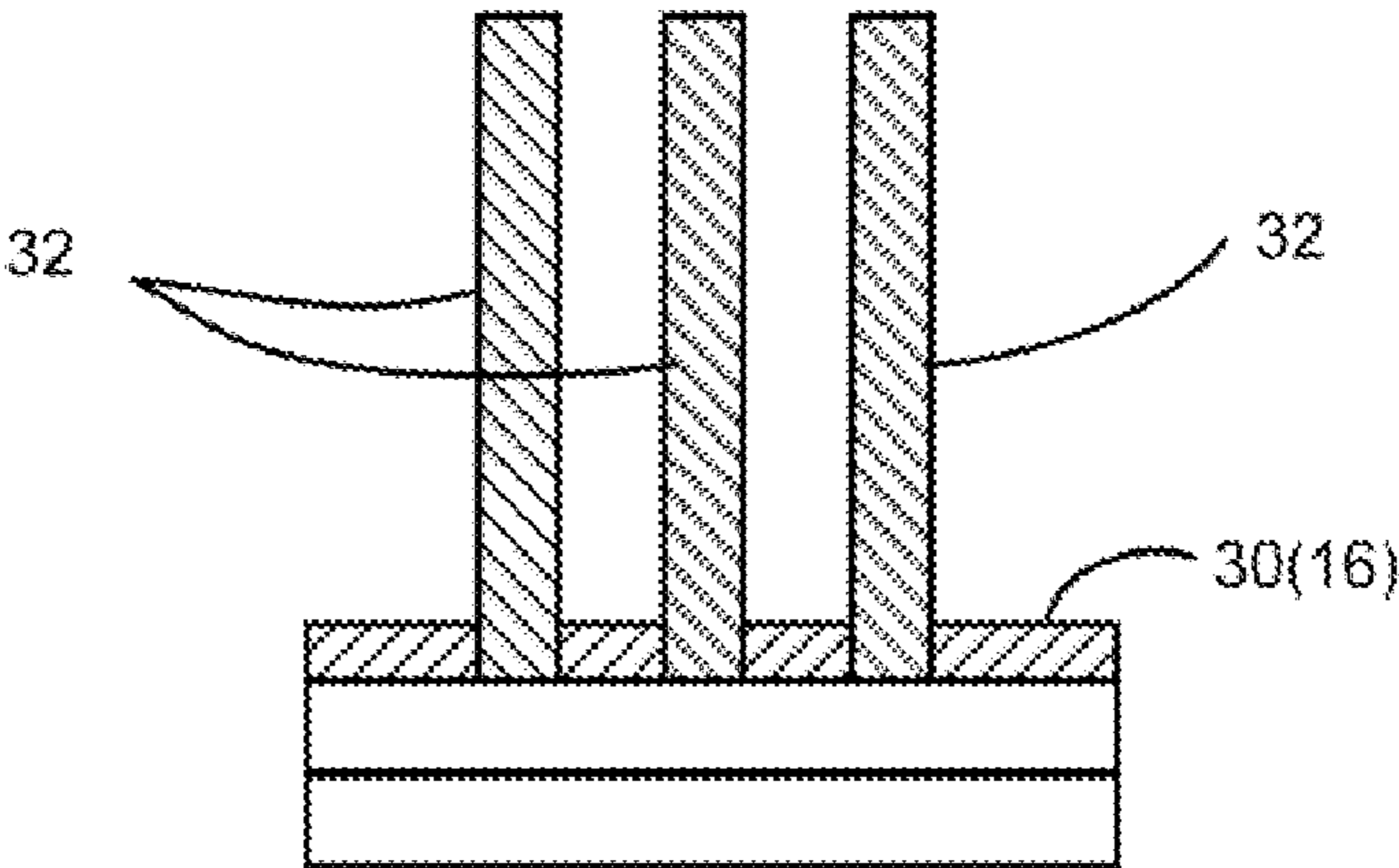


FIG. 5F

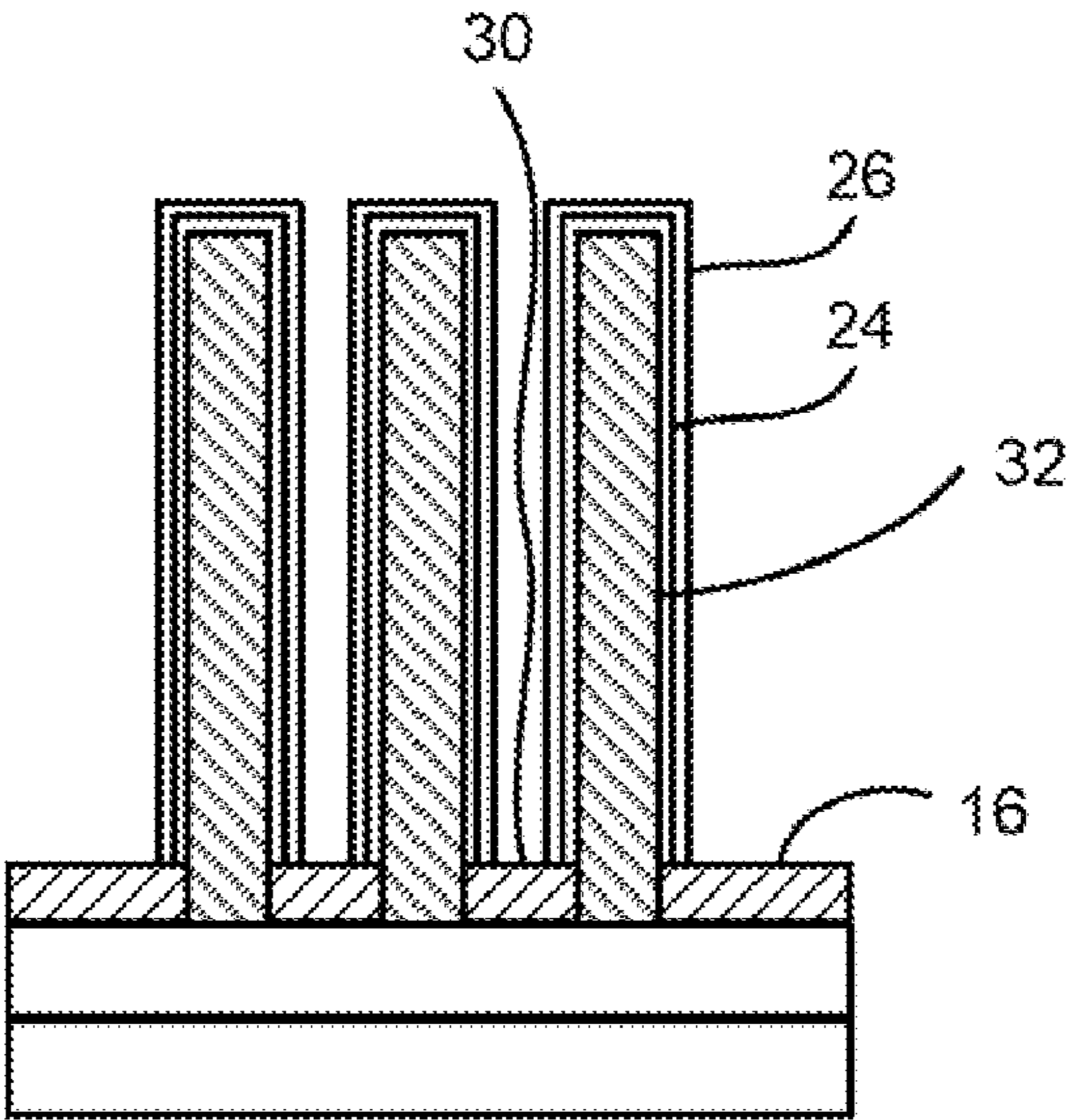


FIG. 5G

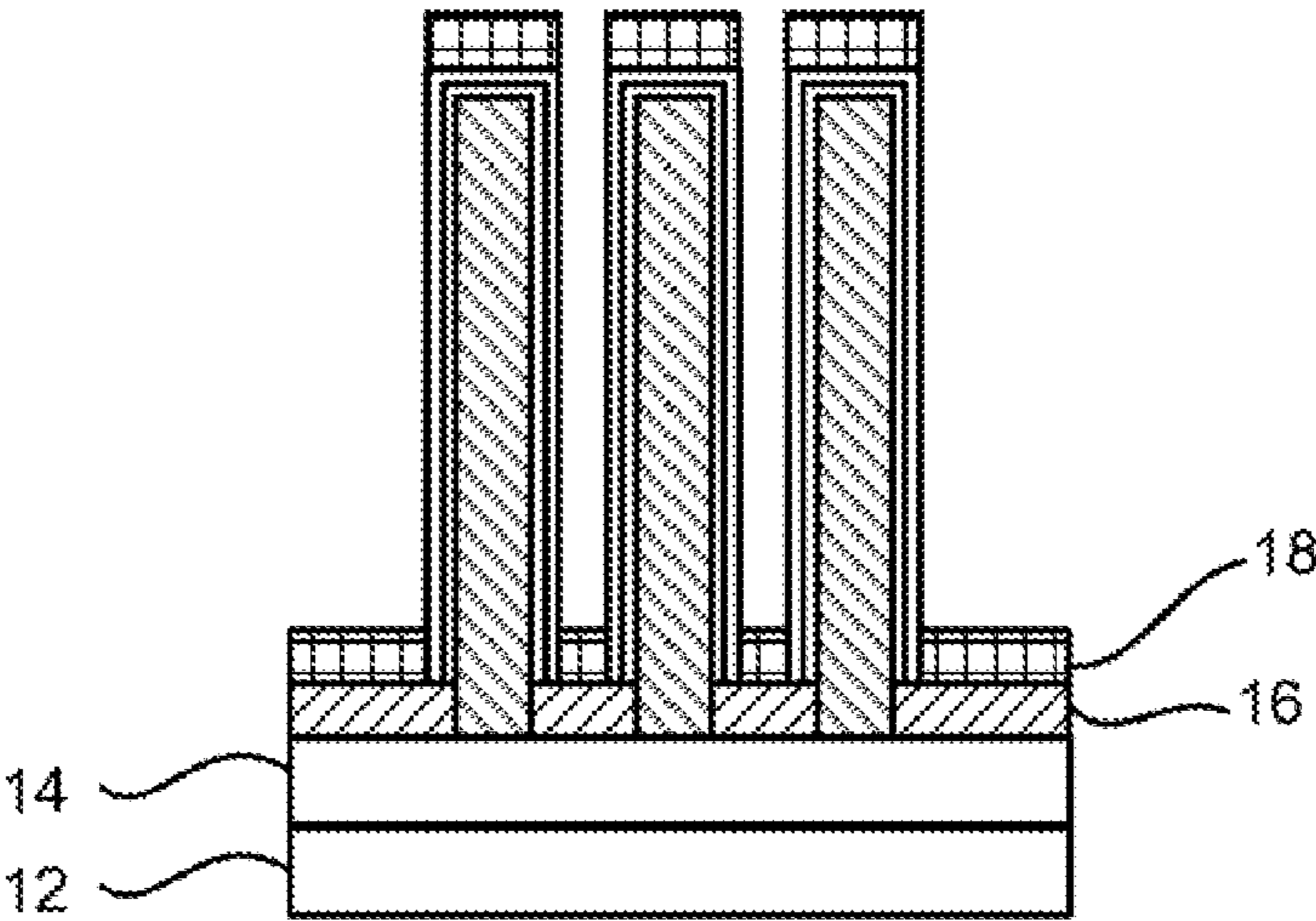


FIG. 5H

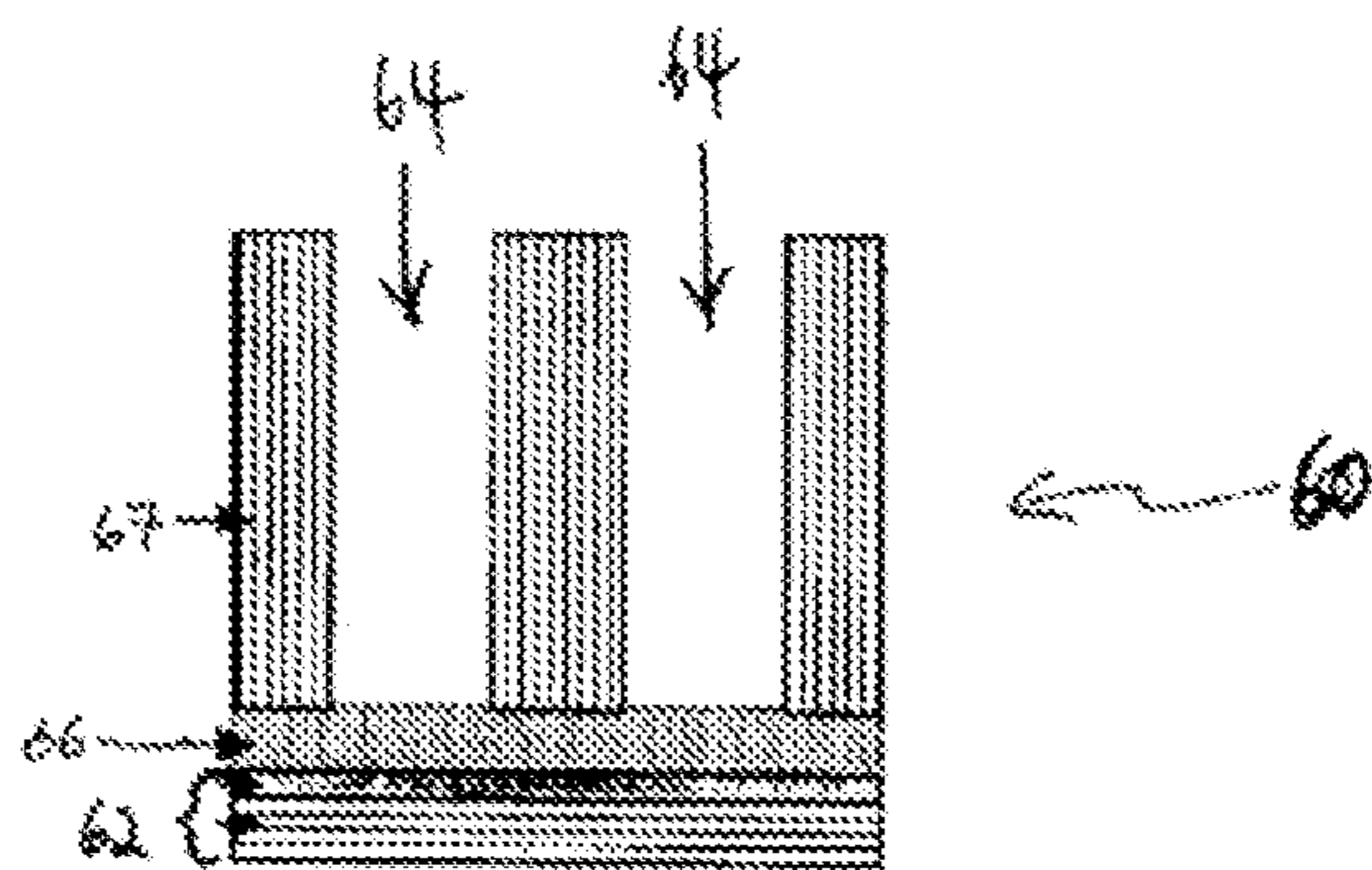


FIG. 6A

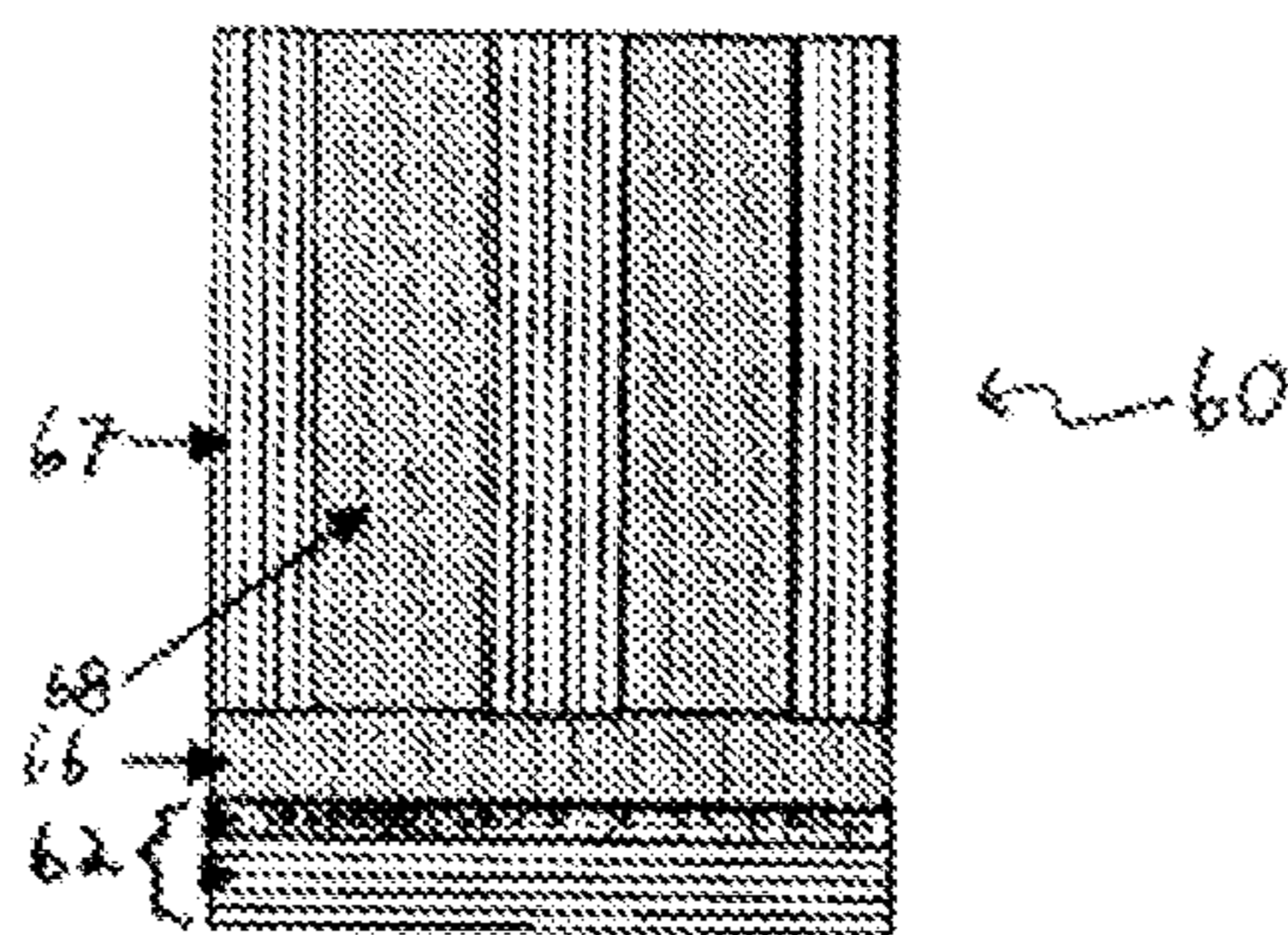


FIG. 6B

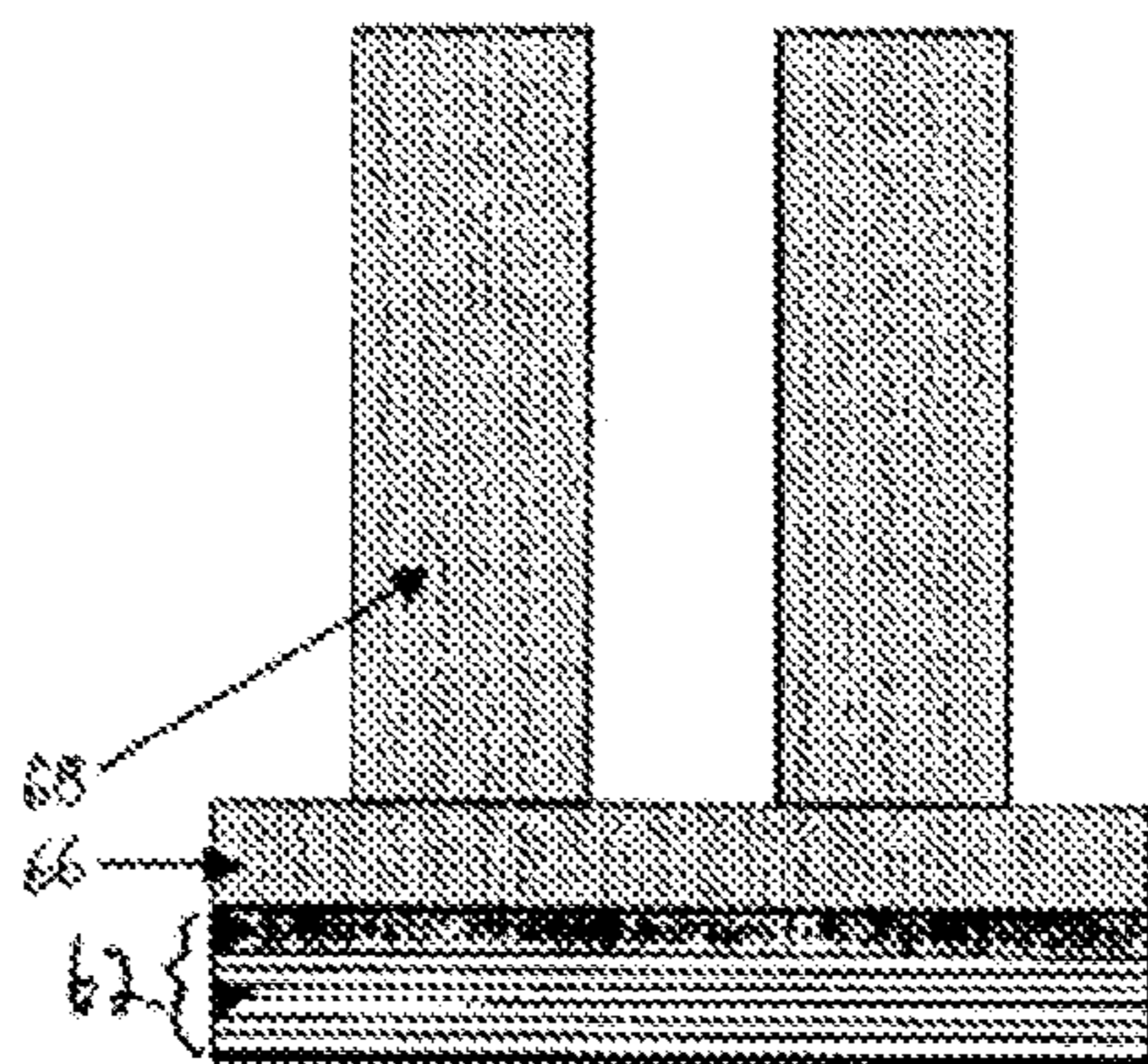


FIG. 6C

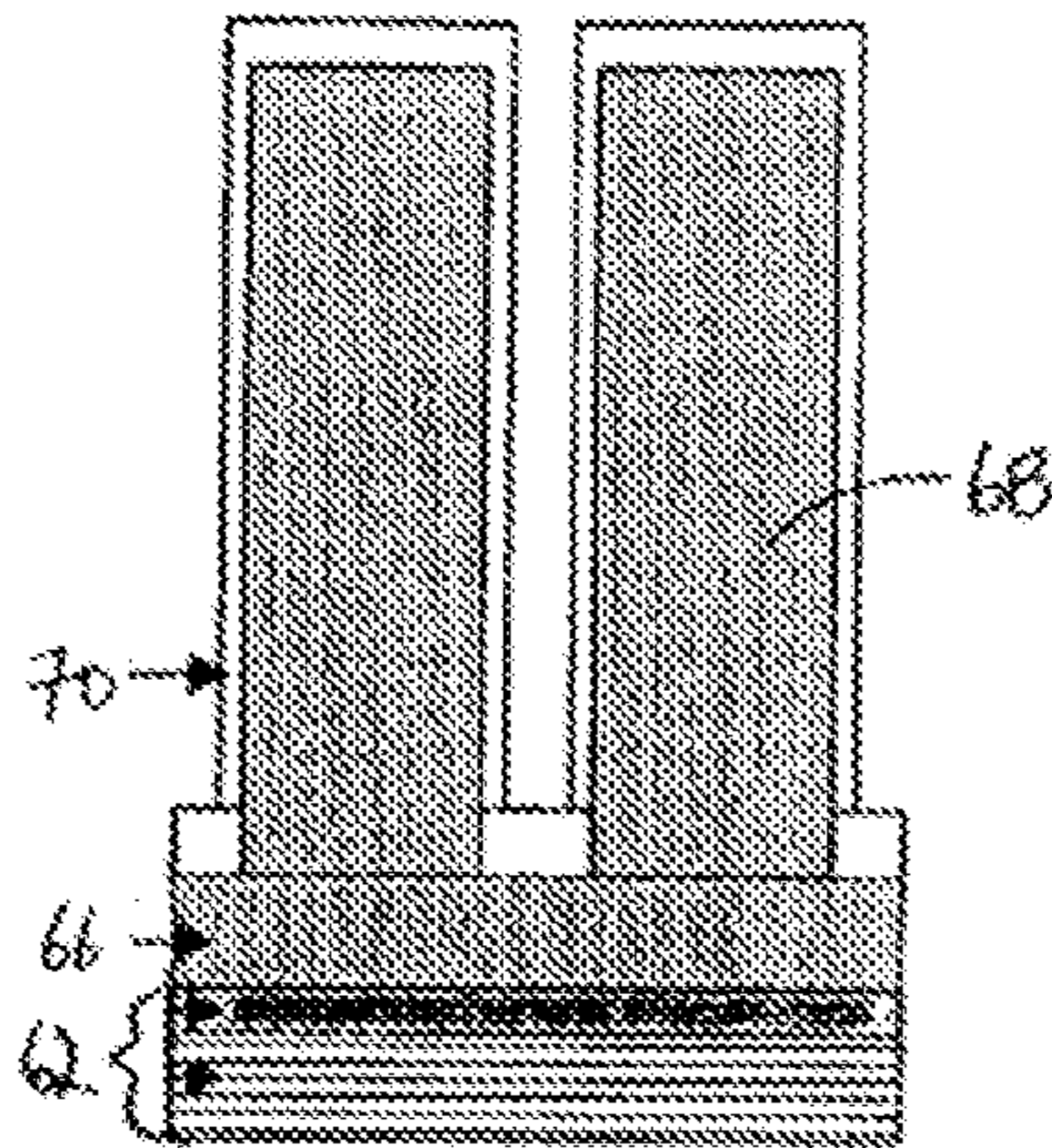


FIG. 6D

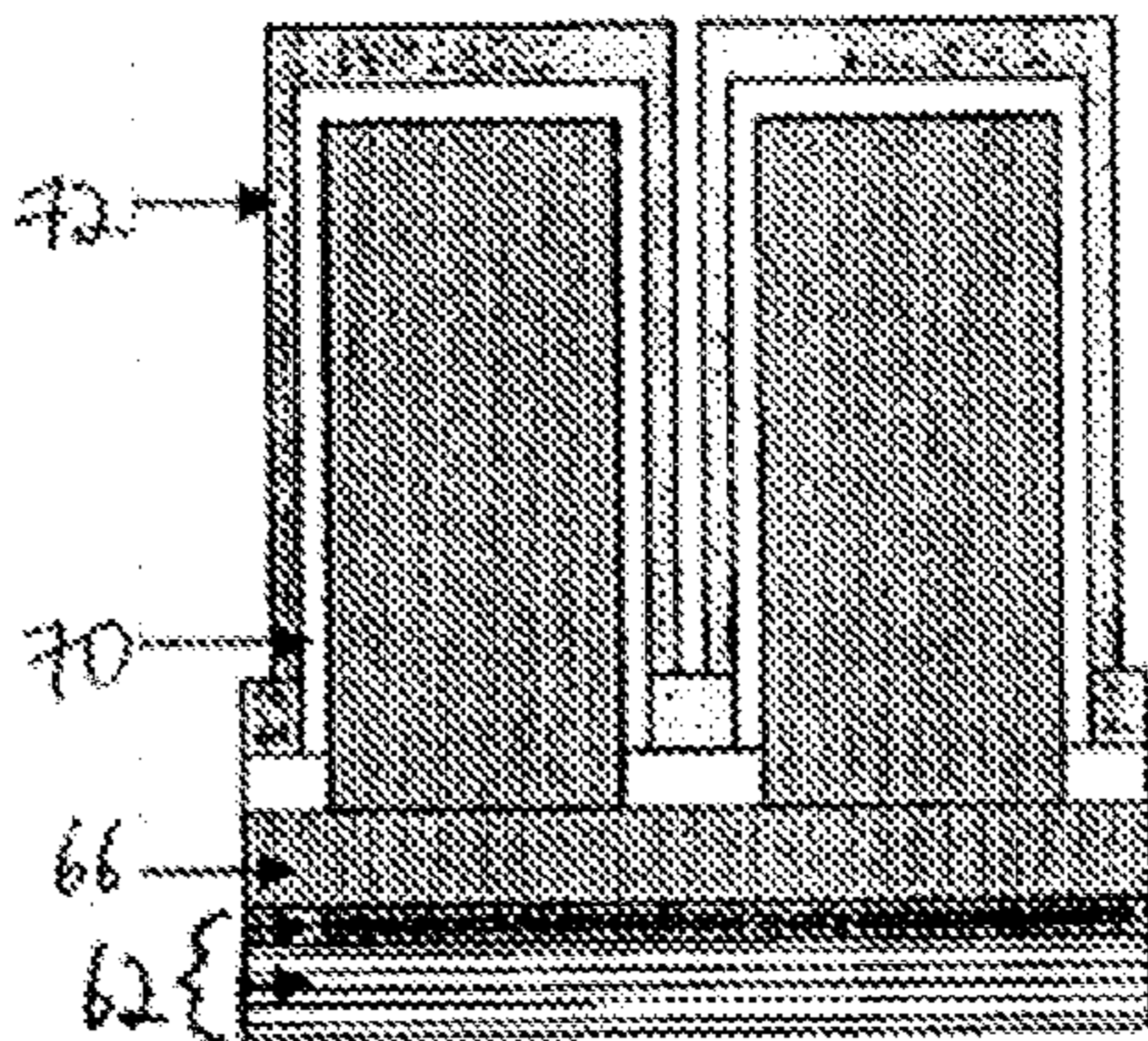


FIG. 6E

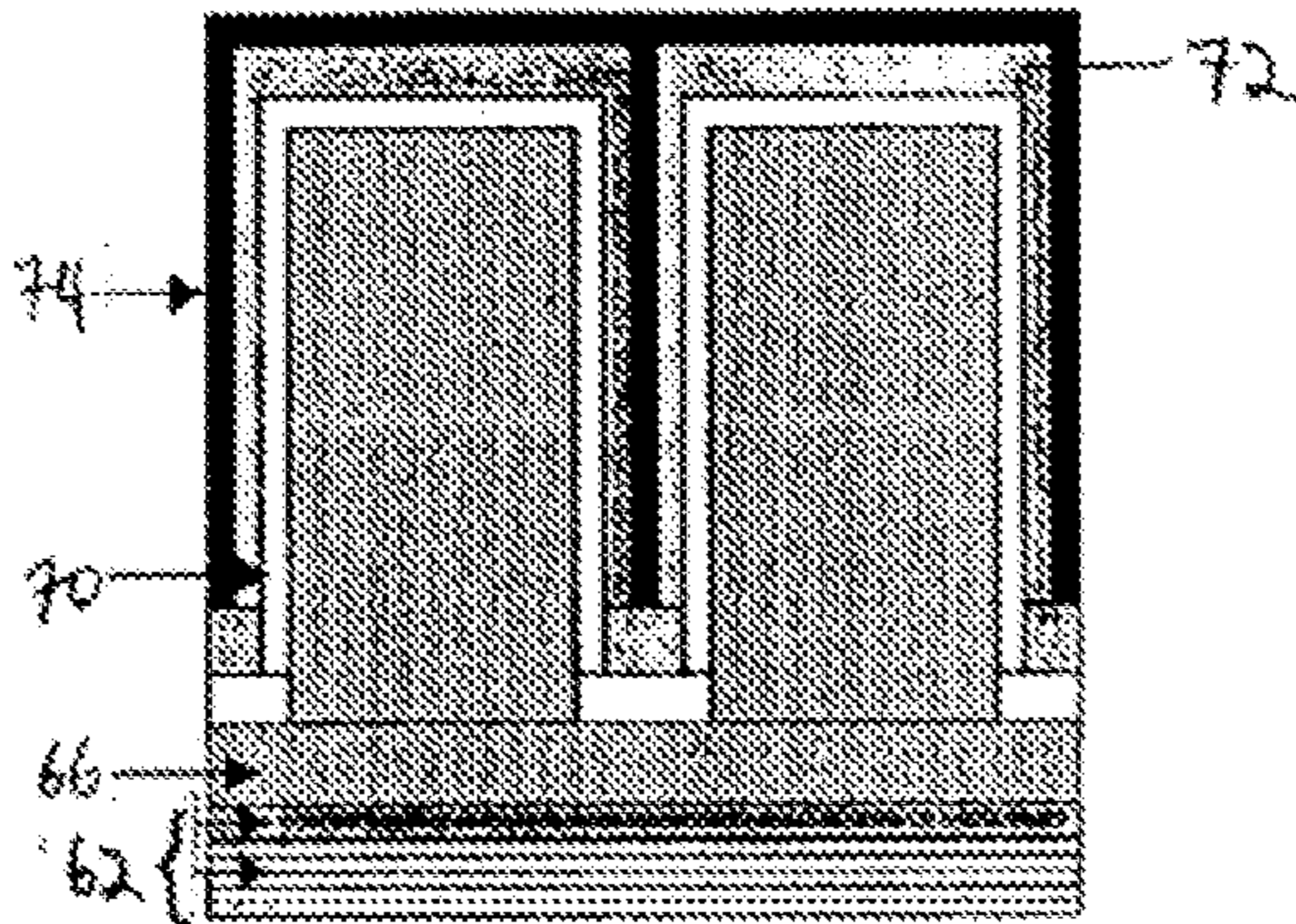


FIG. 6F

Figure 7A



Figure 7B

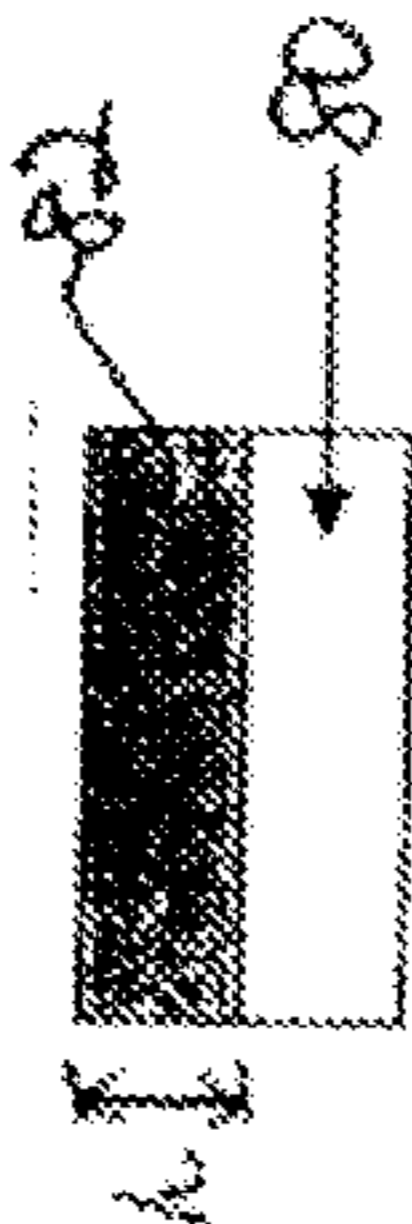


Figure 7C

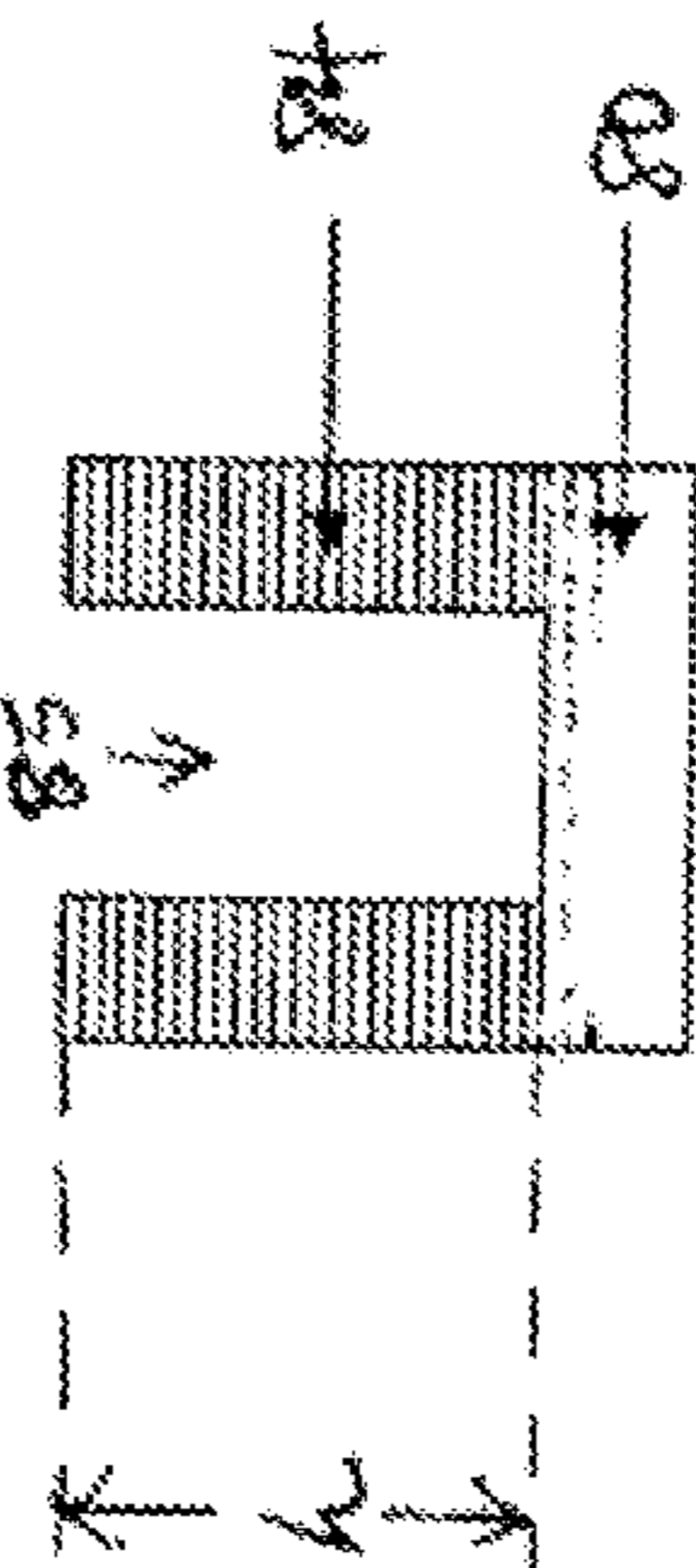


Figure 7D

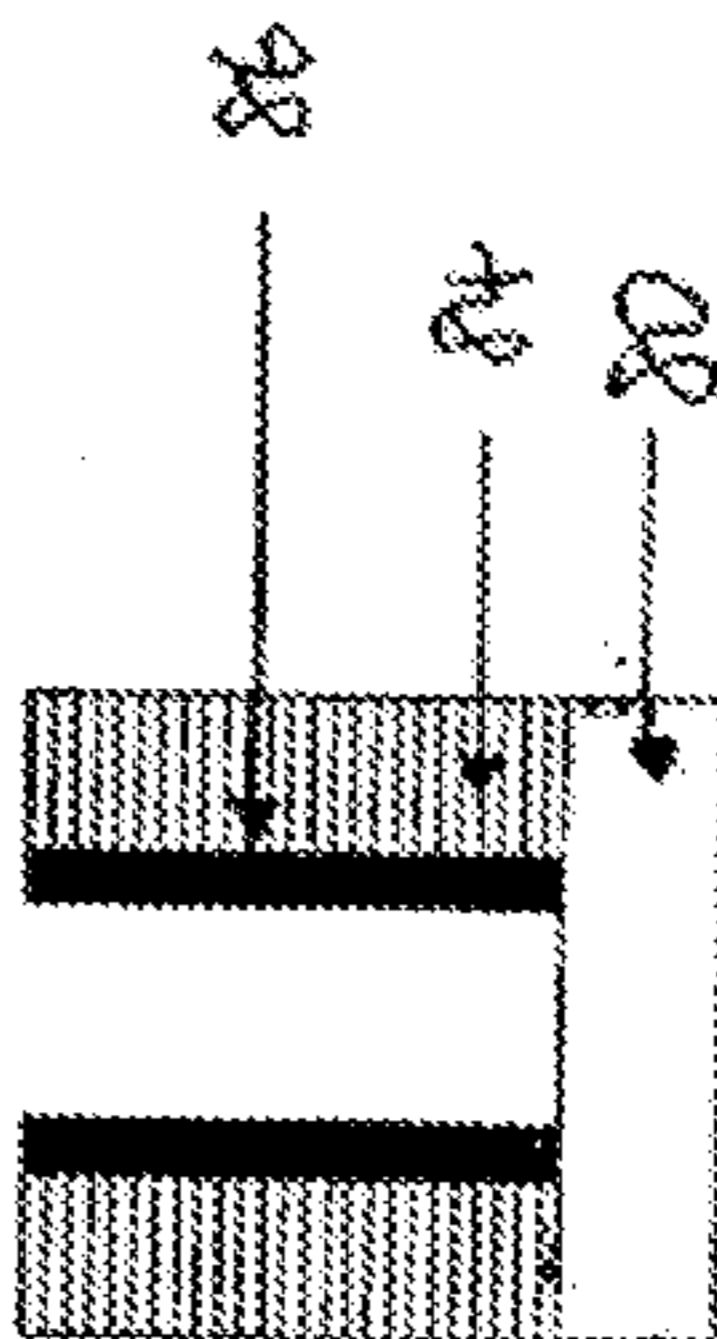


Figure 7E

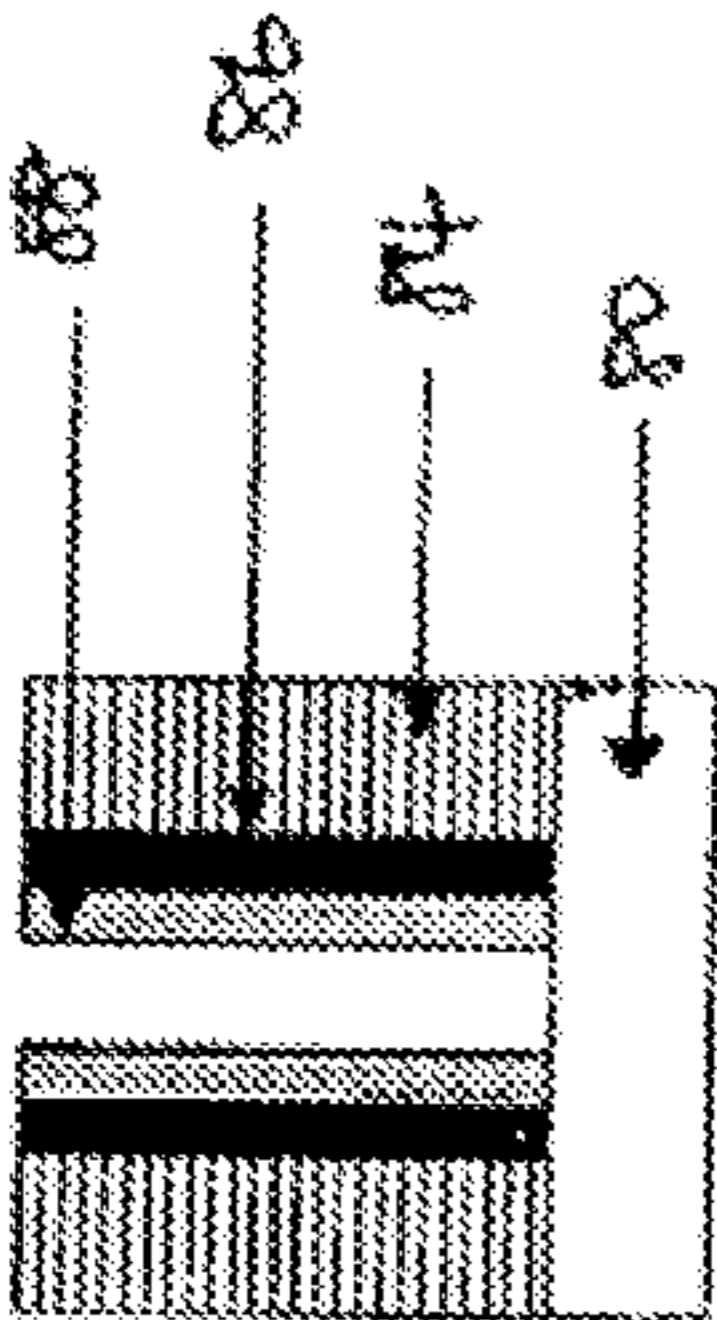


Figure 7F

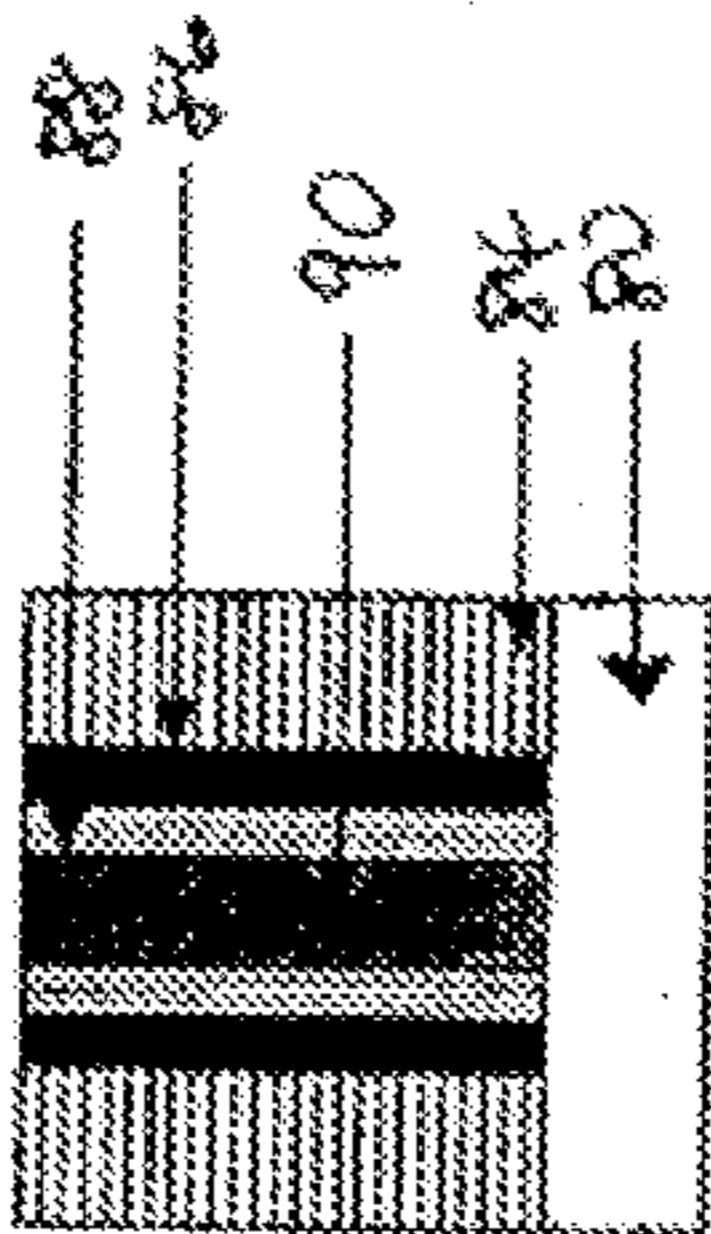


Figure 7G

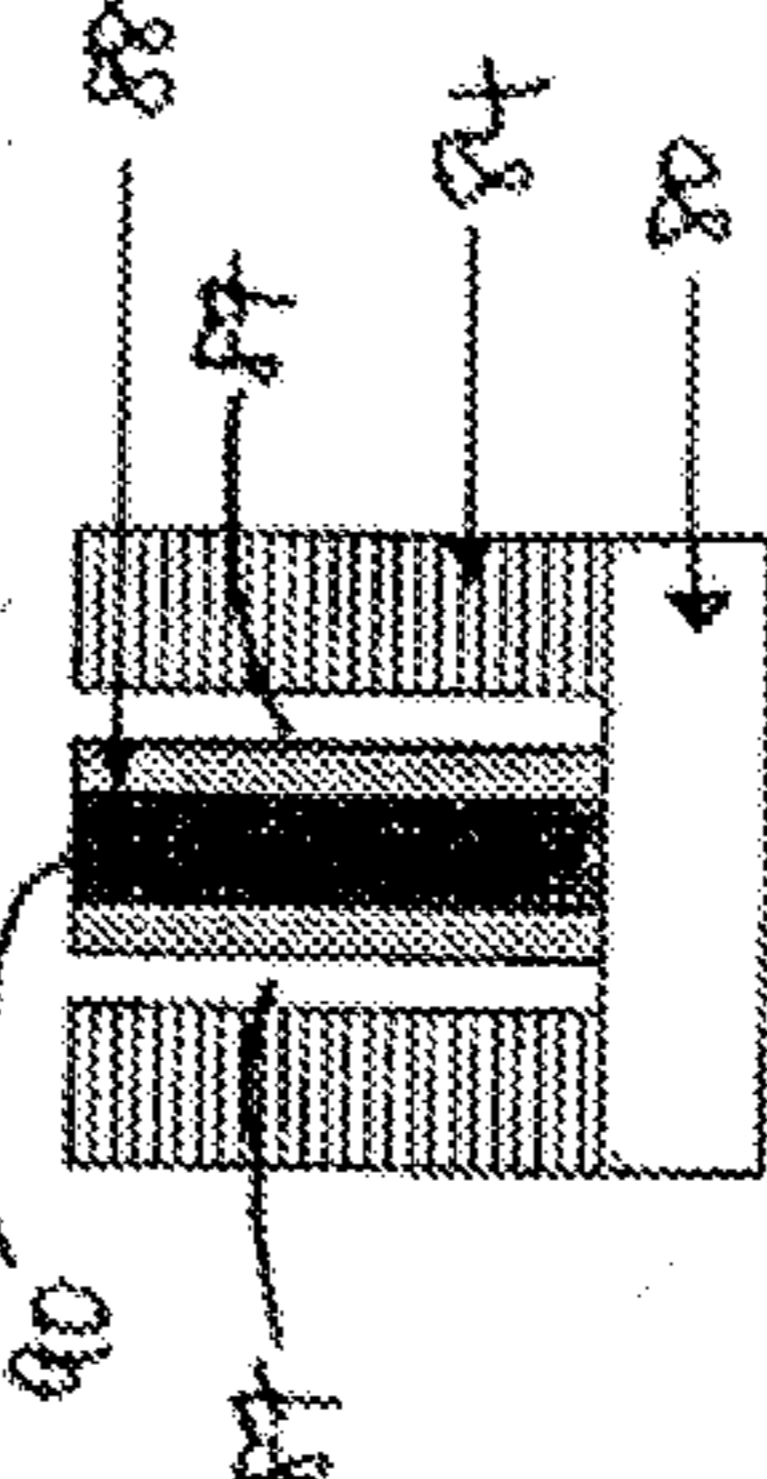
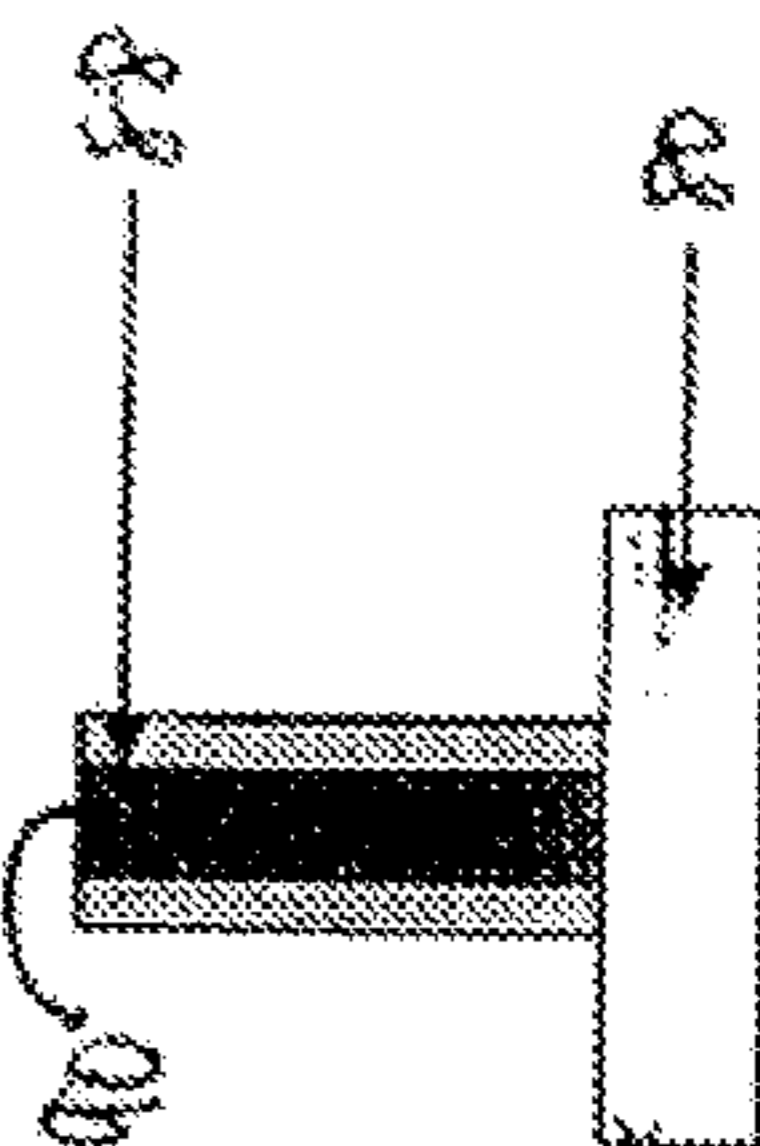
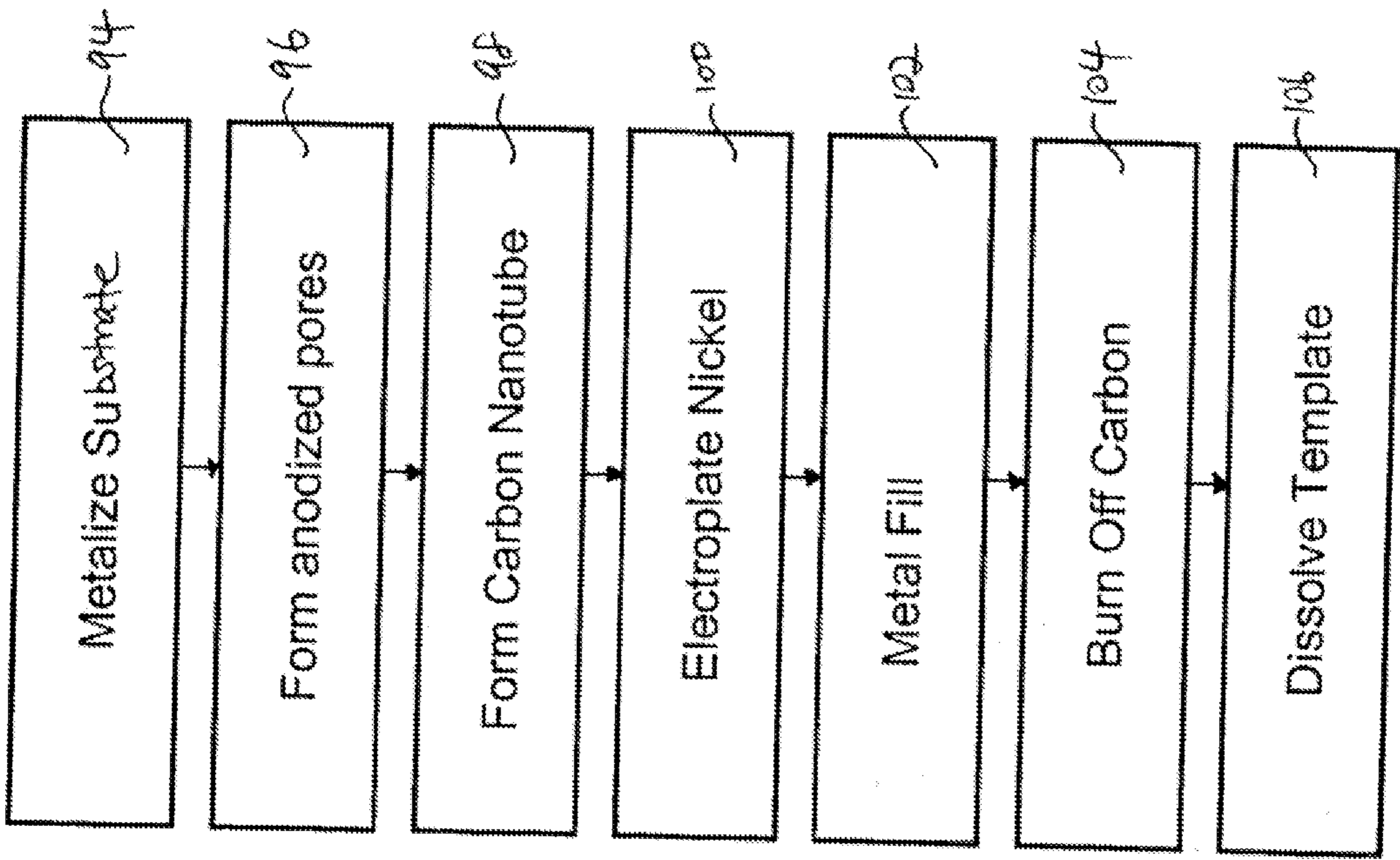


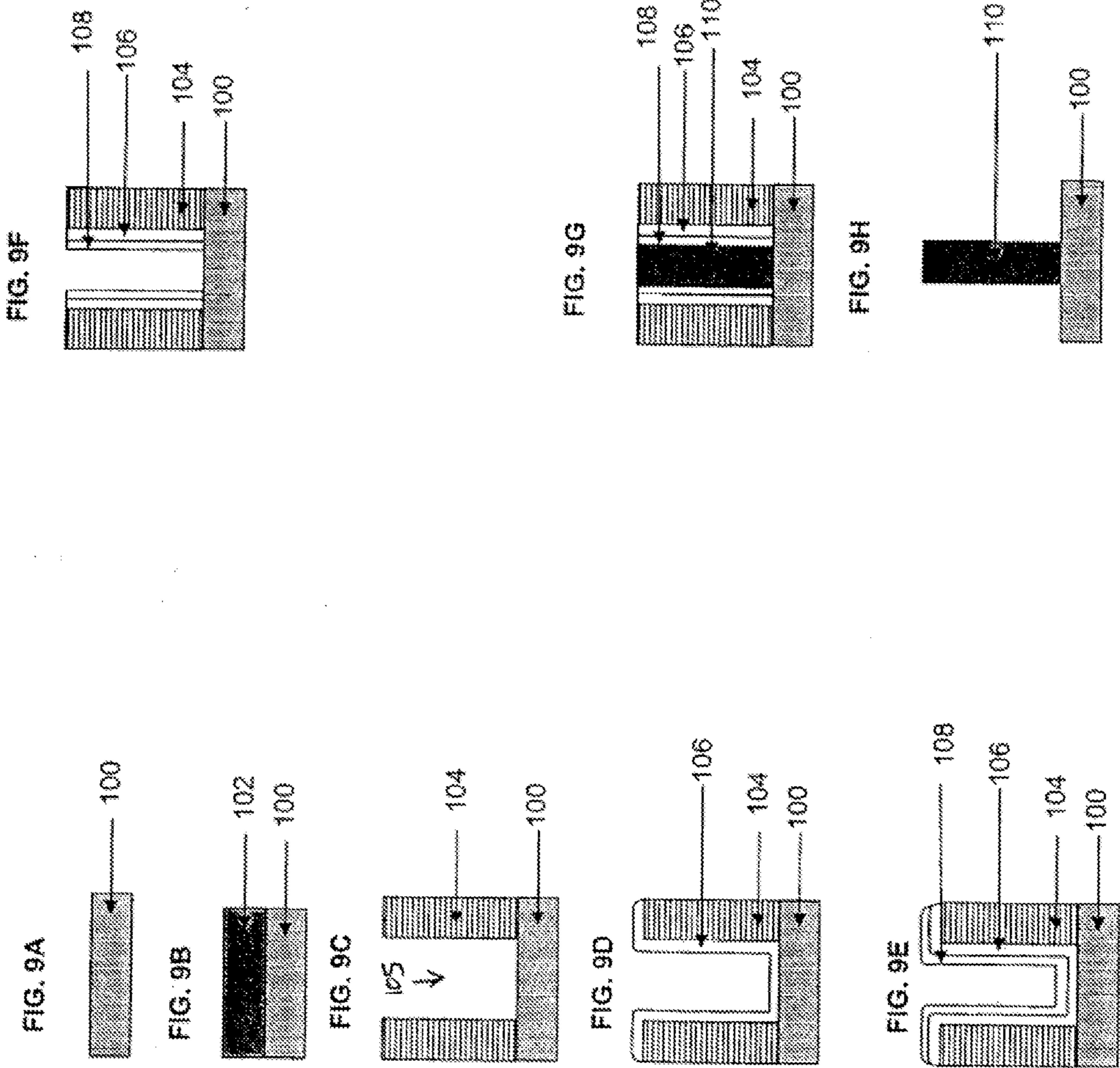
Figure 7H





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FIG. 8



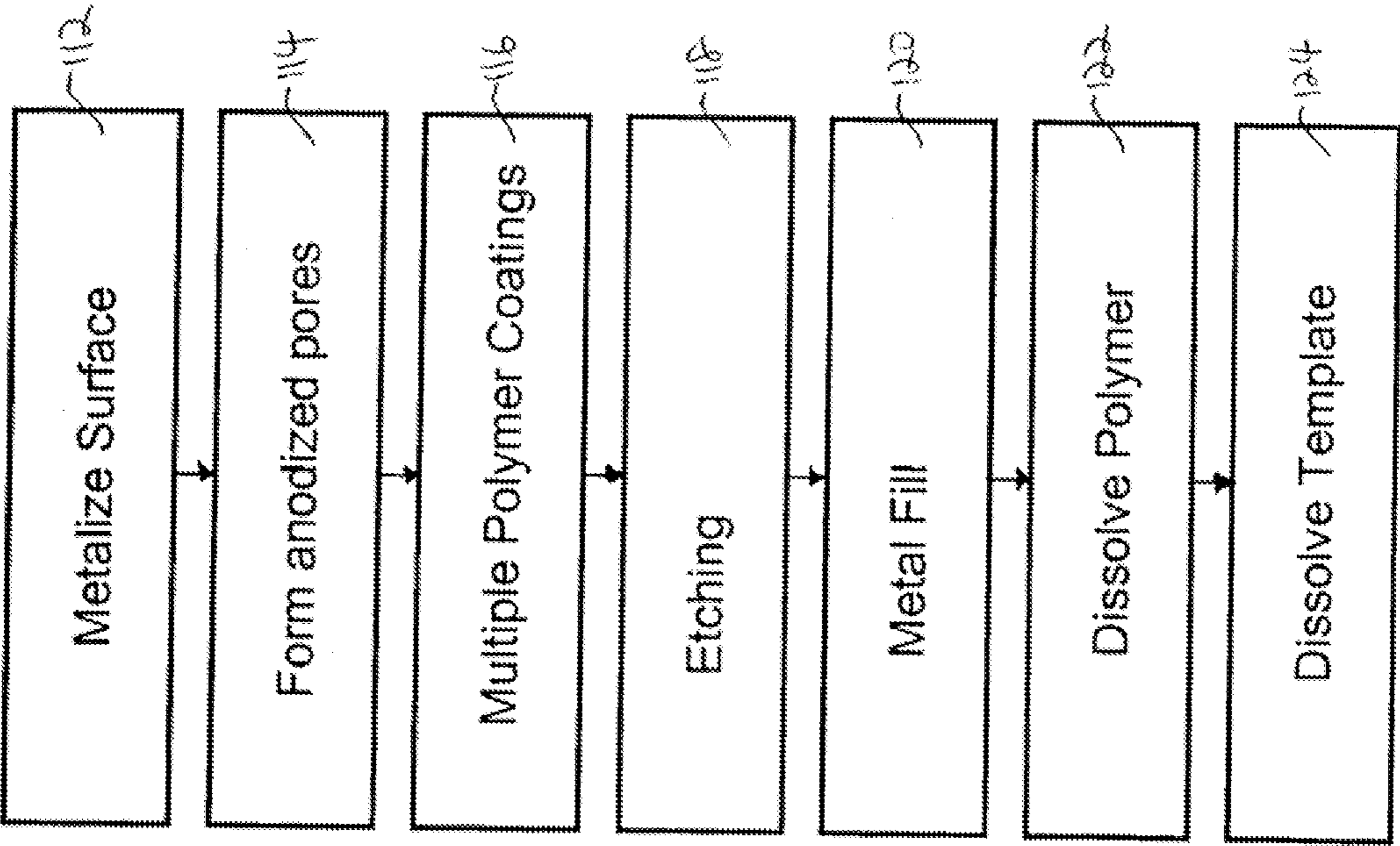


FIG. 10

FIG. 11A

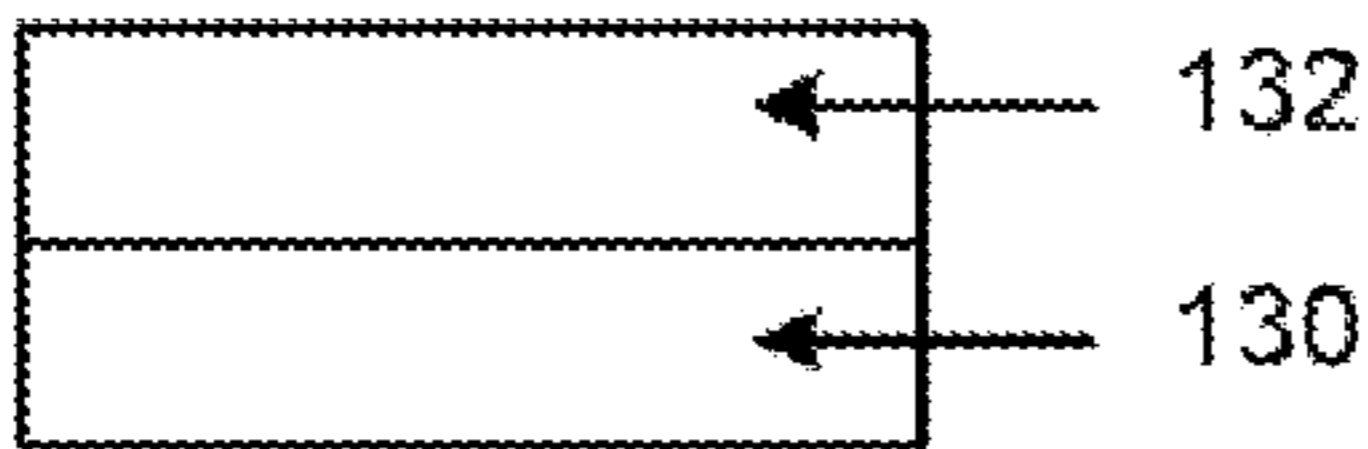


FIG. 11B

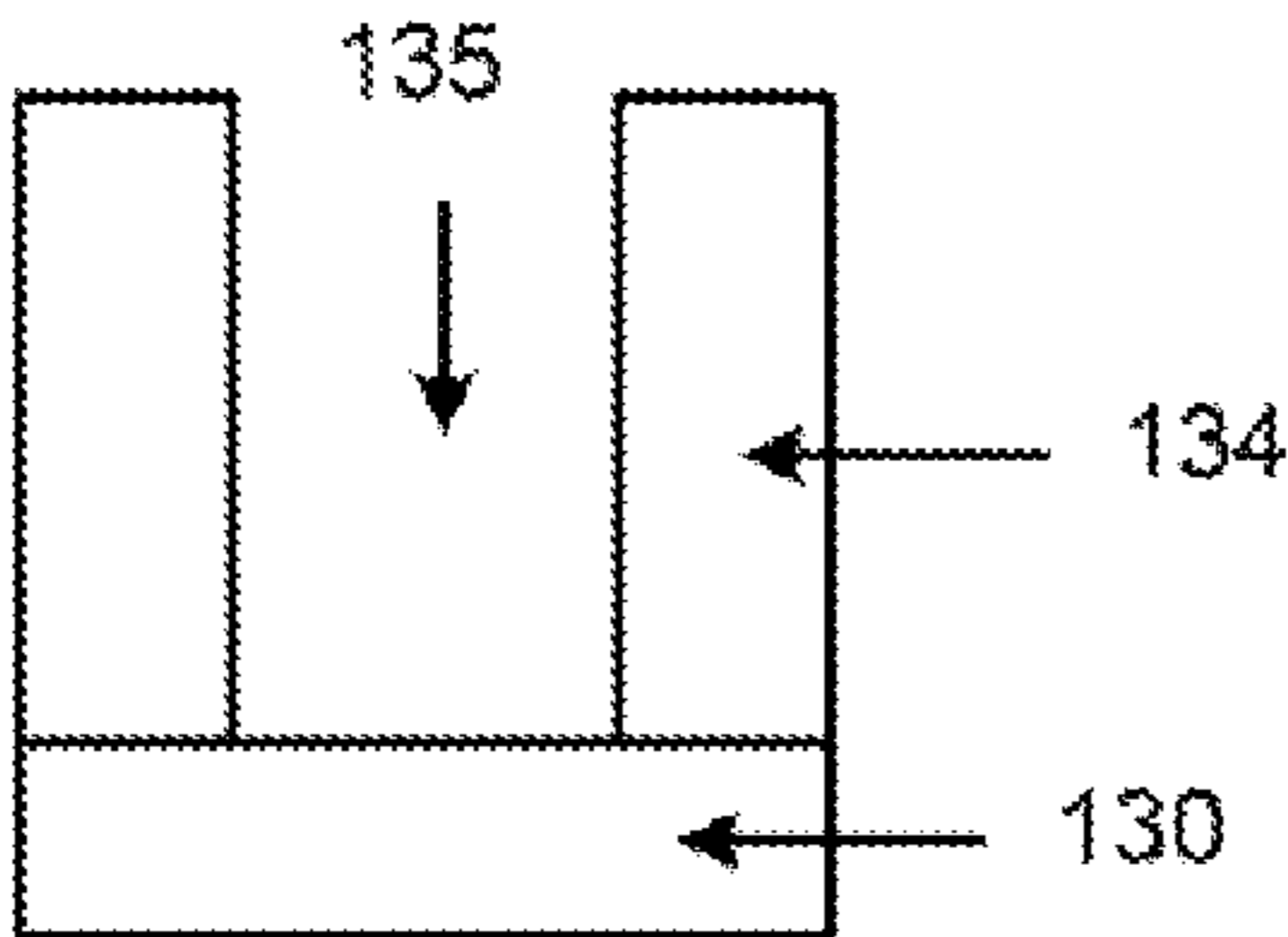


FIG. 11C

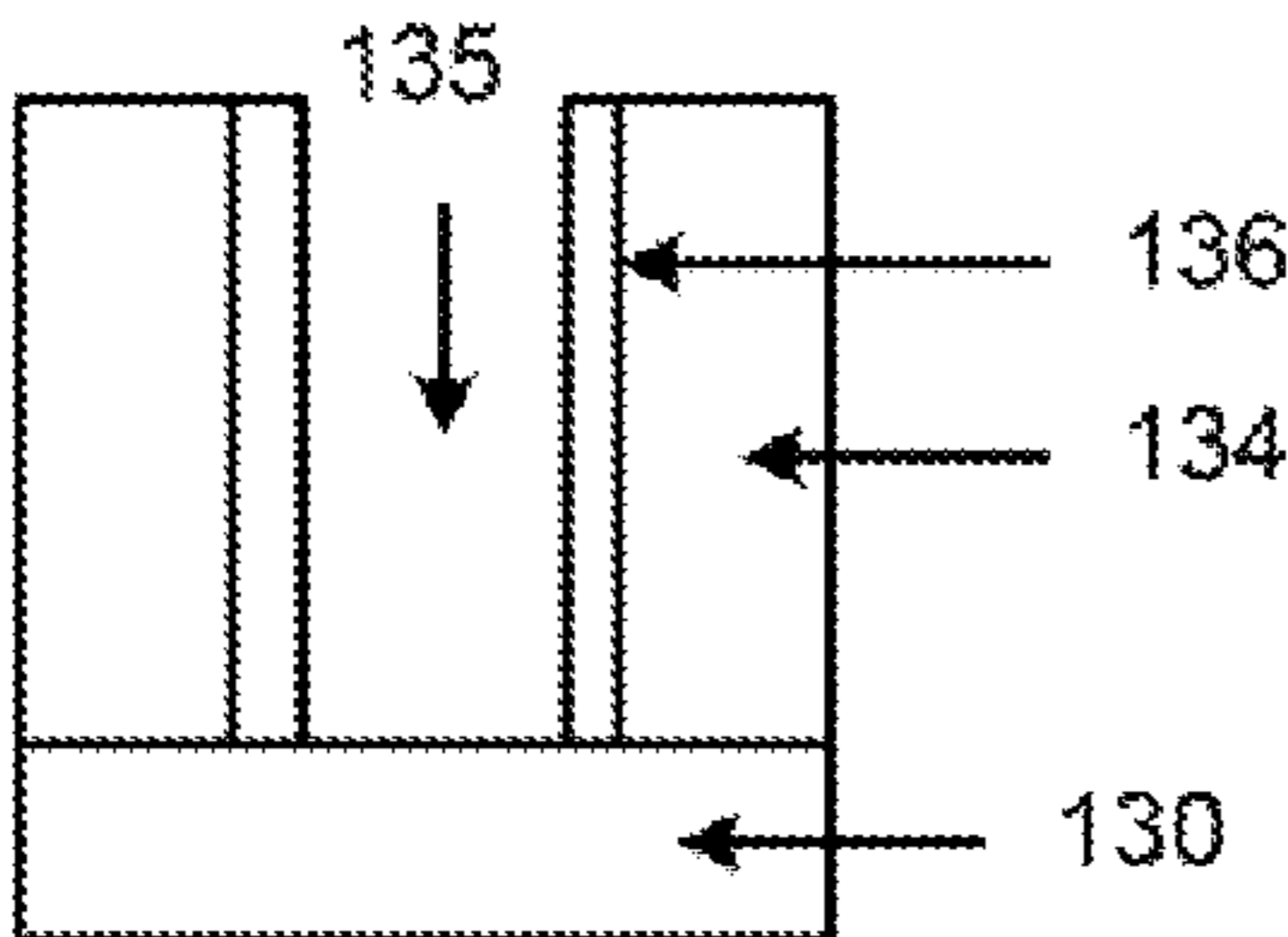


FIG. 11D

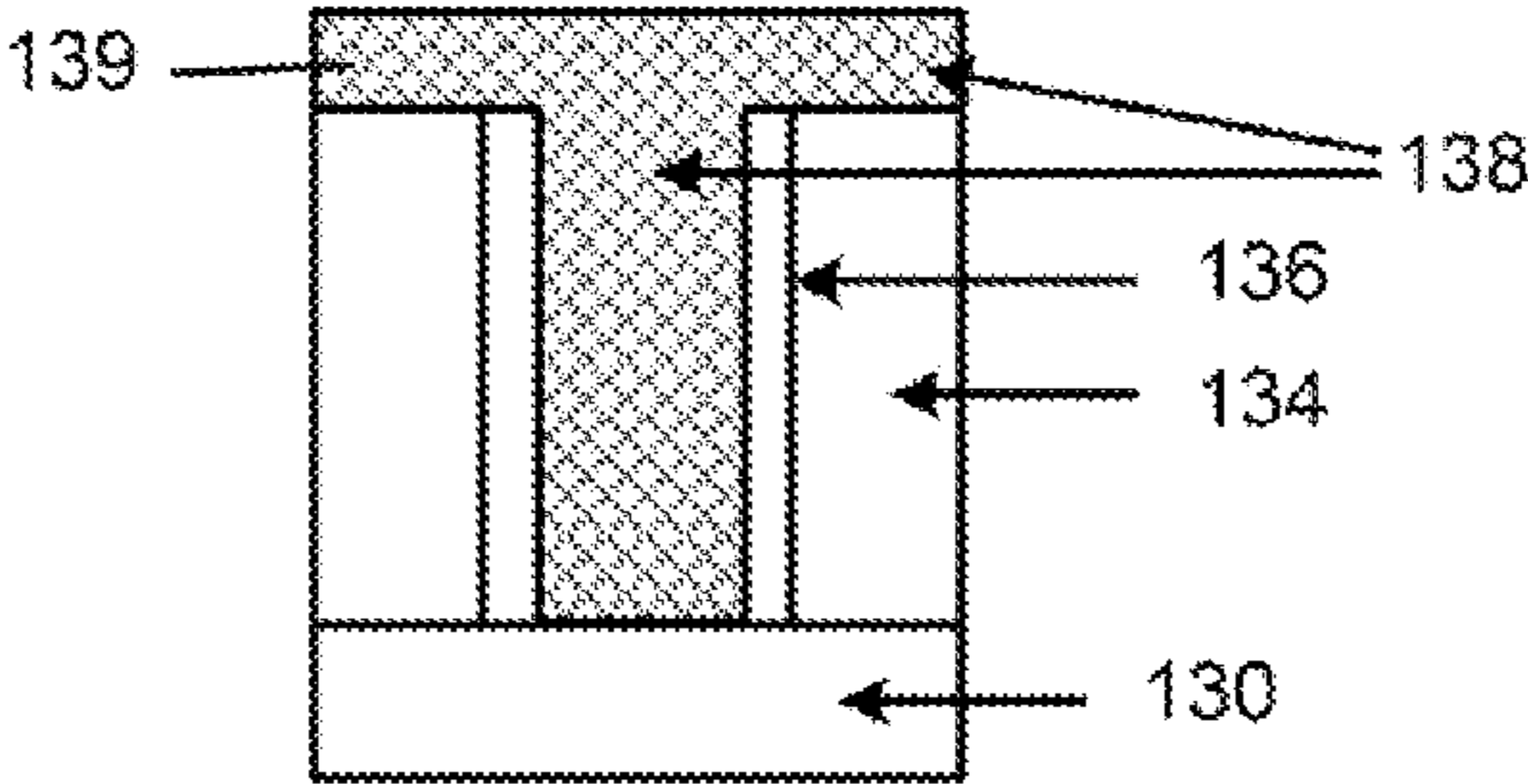


FIG. 11E

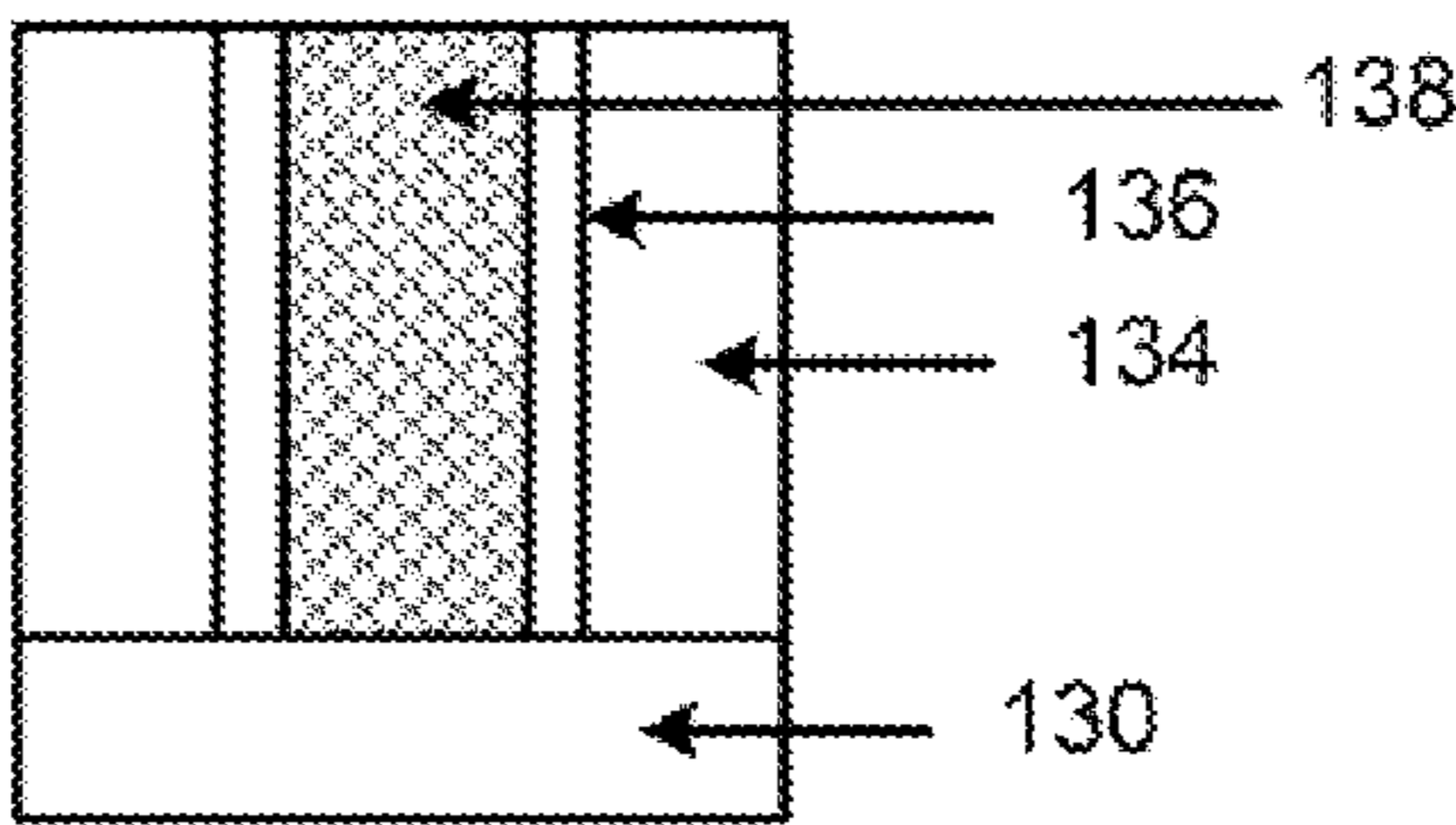


FIG. 11F

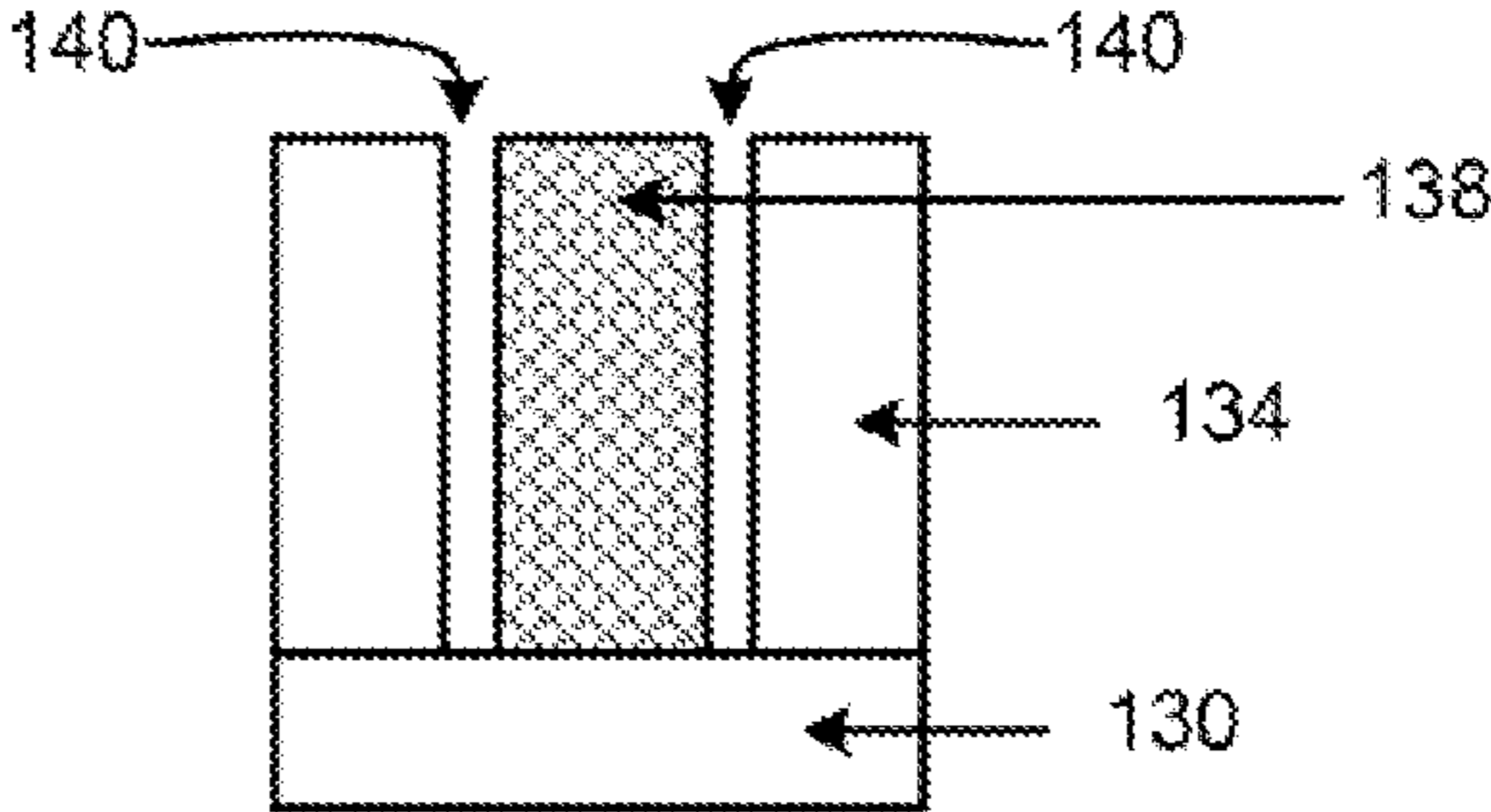


FIG. 11G

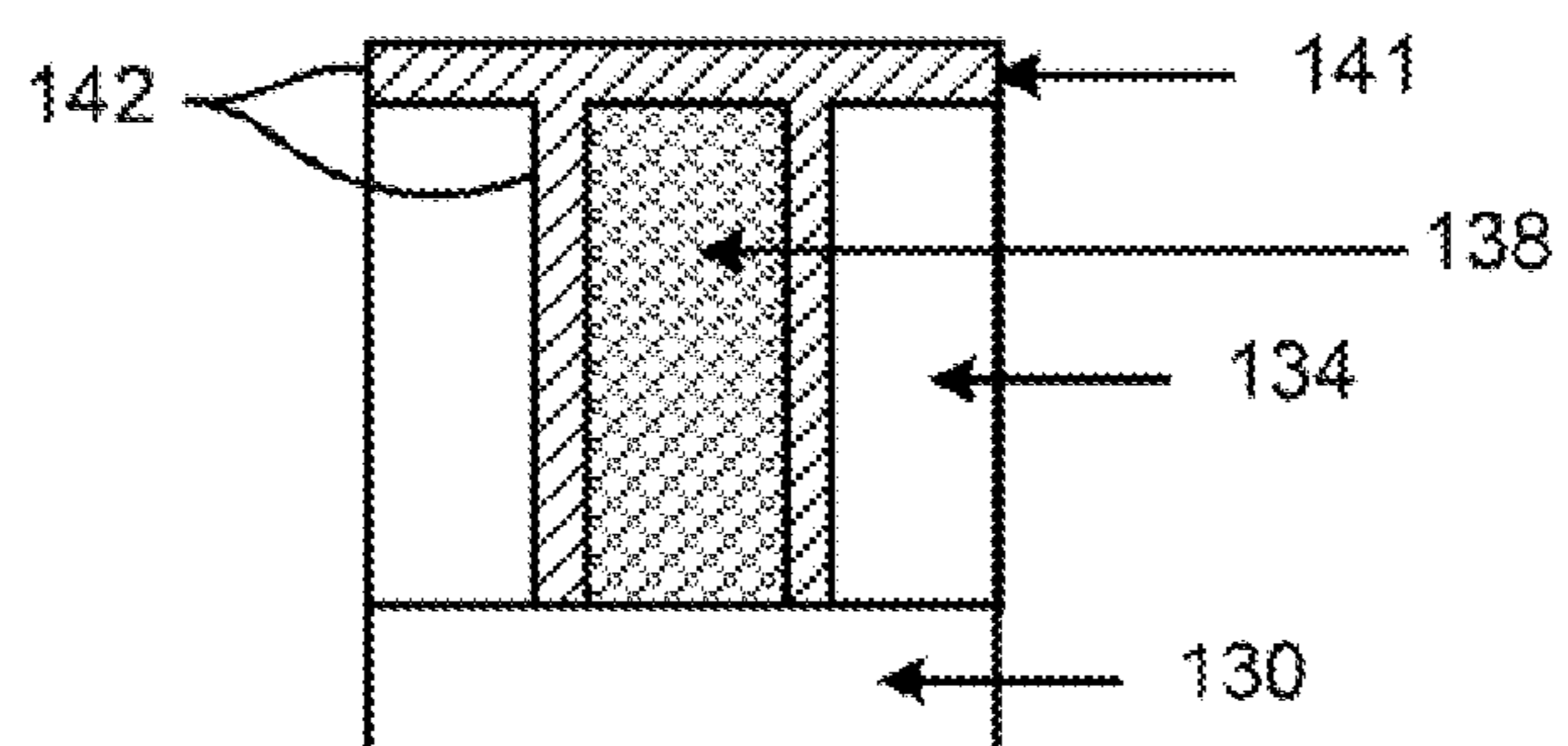


FIG. 11H

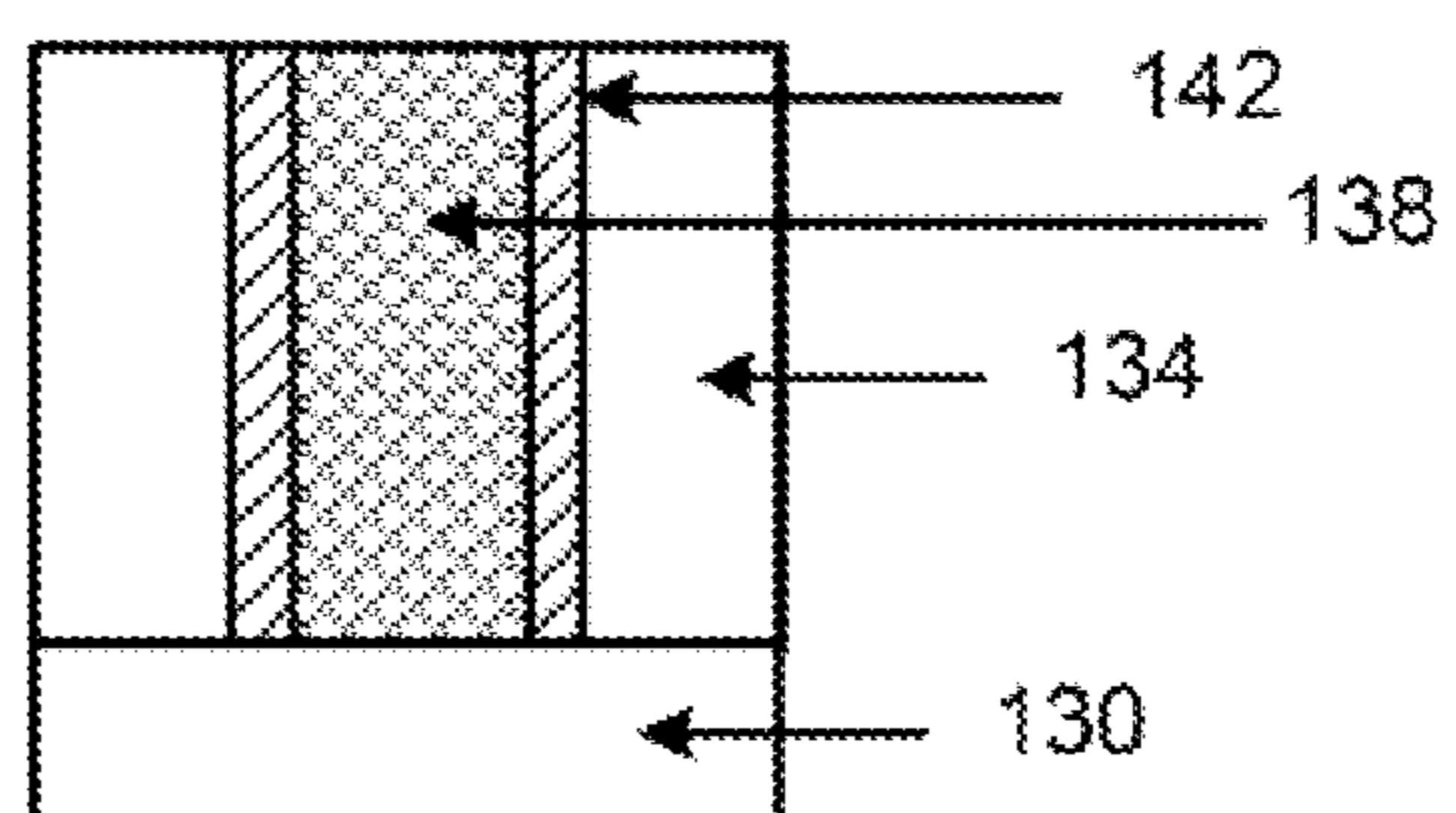
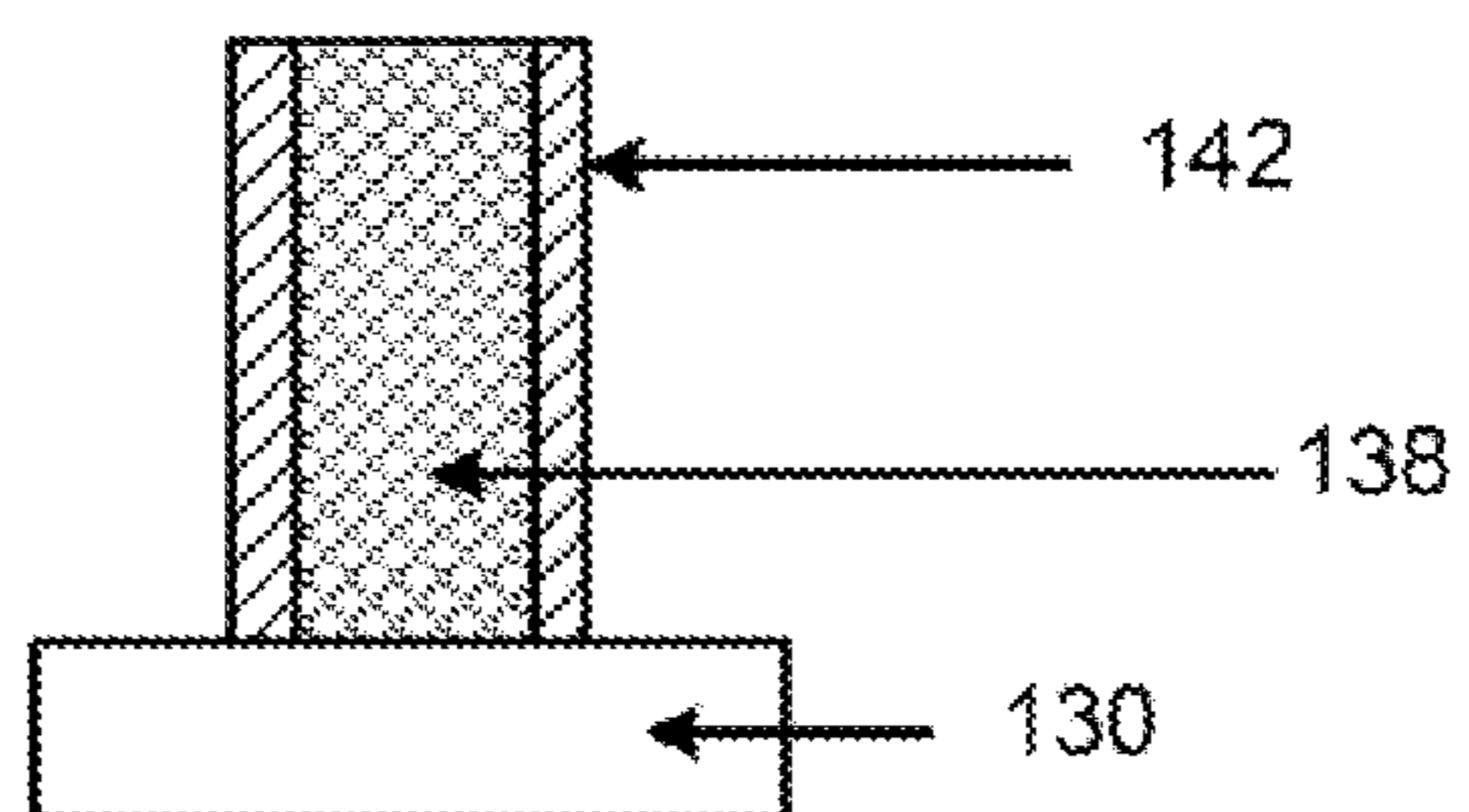


FIG. 11I



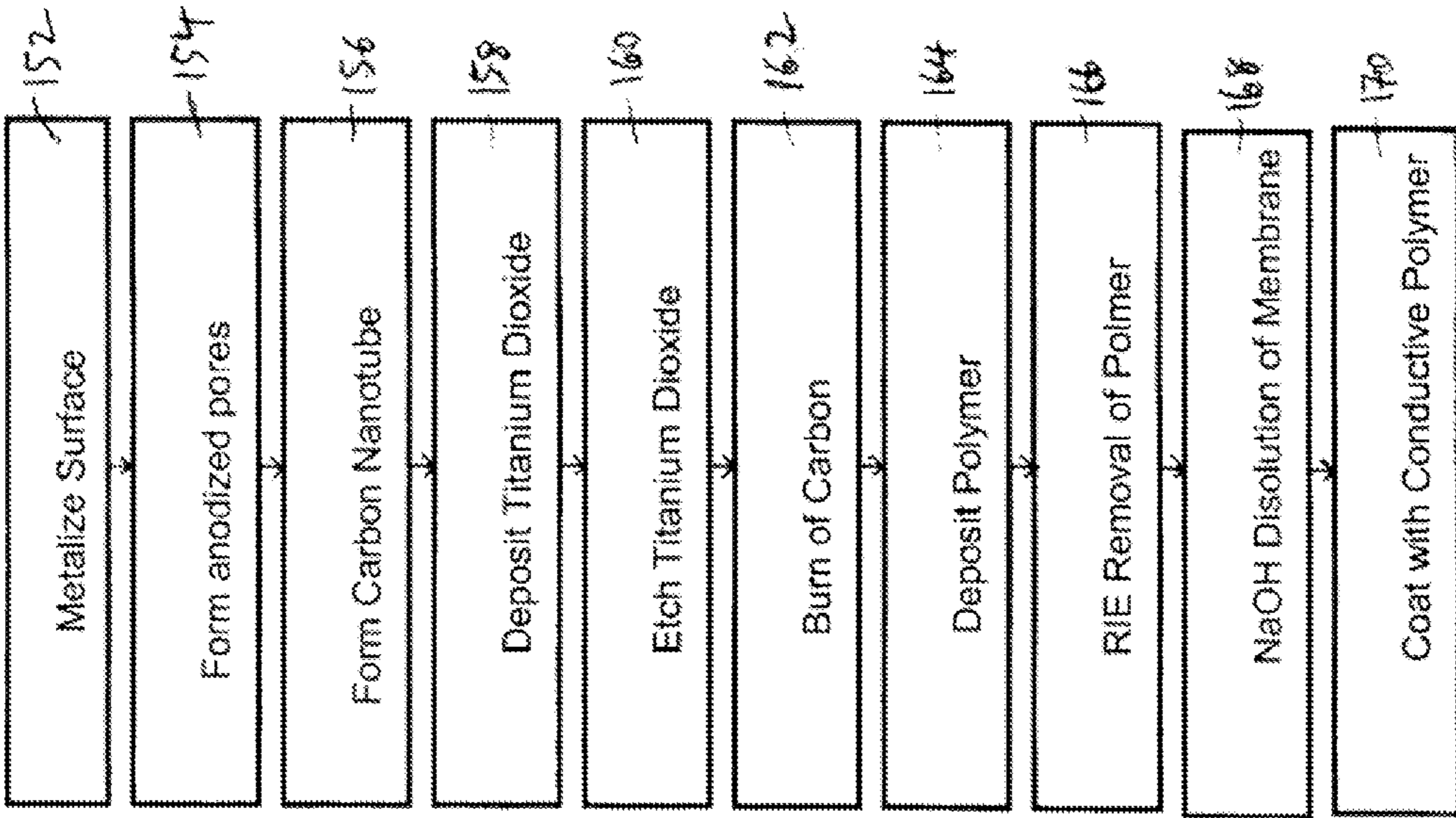


FIG. 12

FIG. 13A

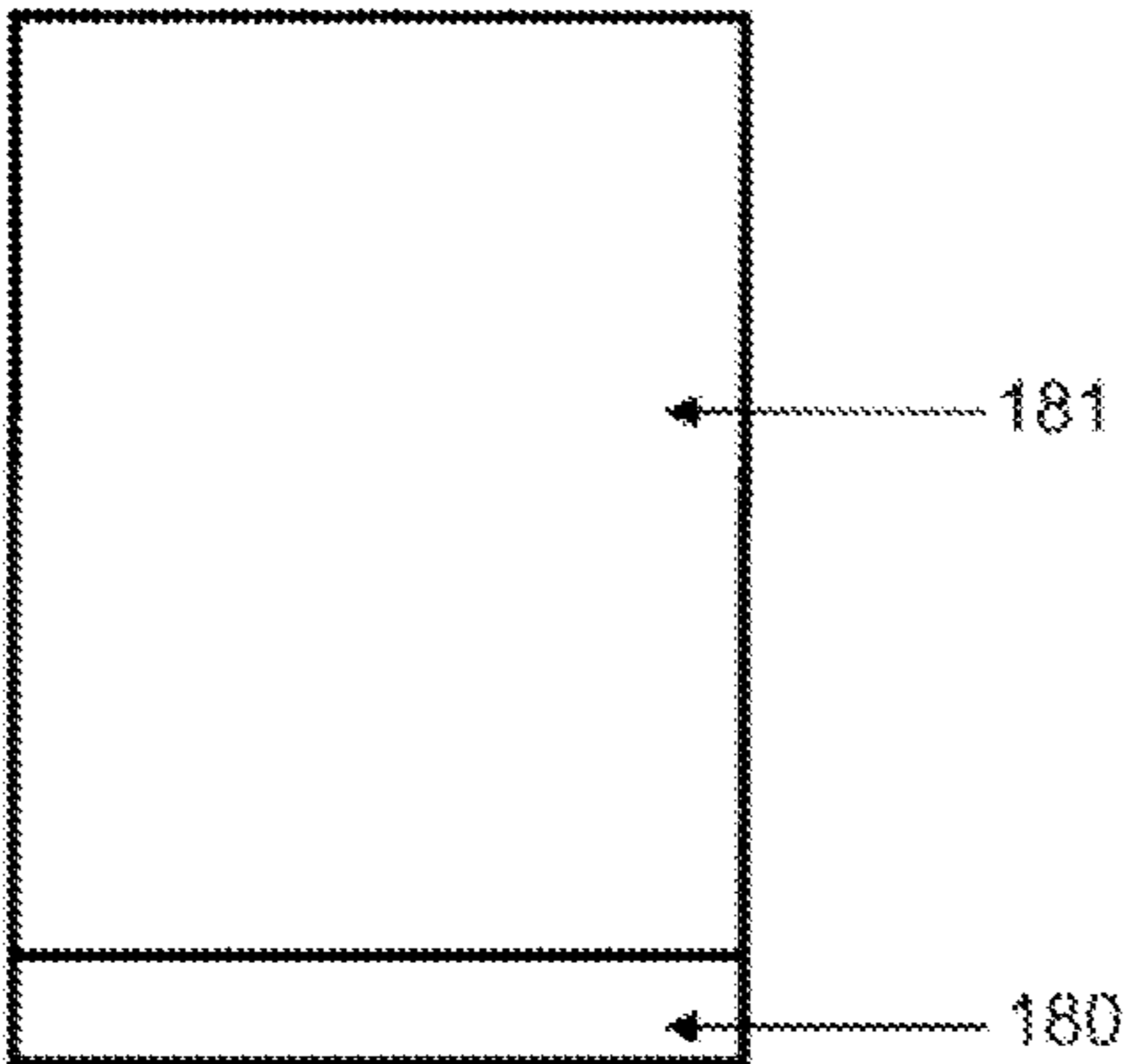


FIG. 13D

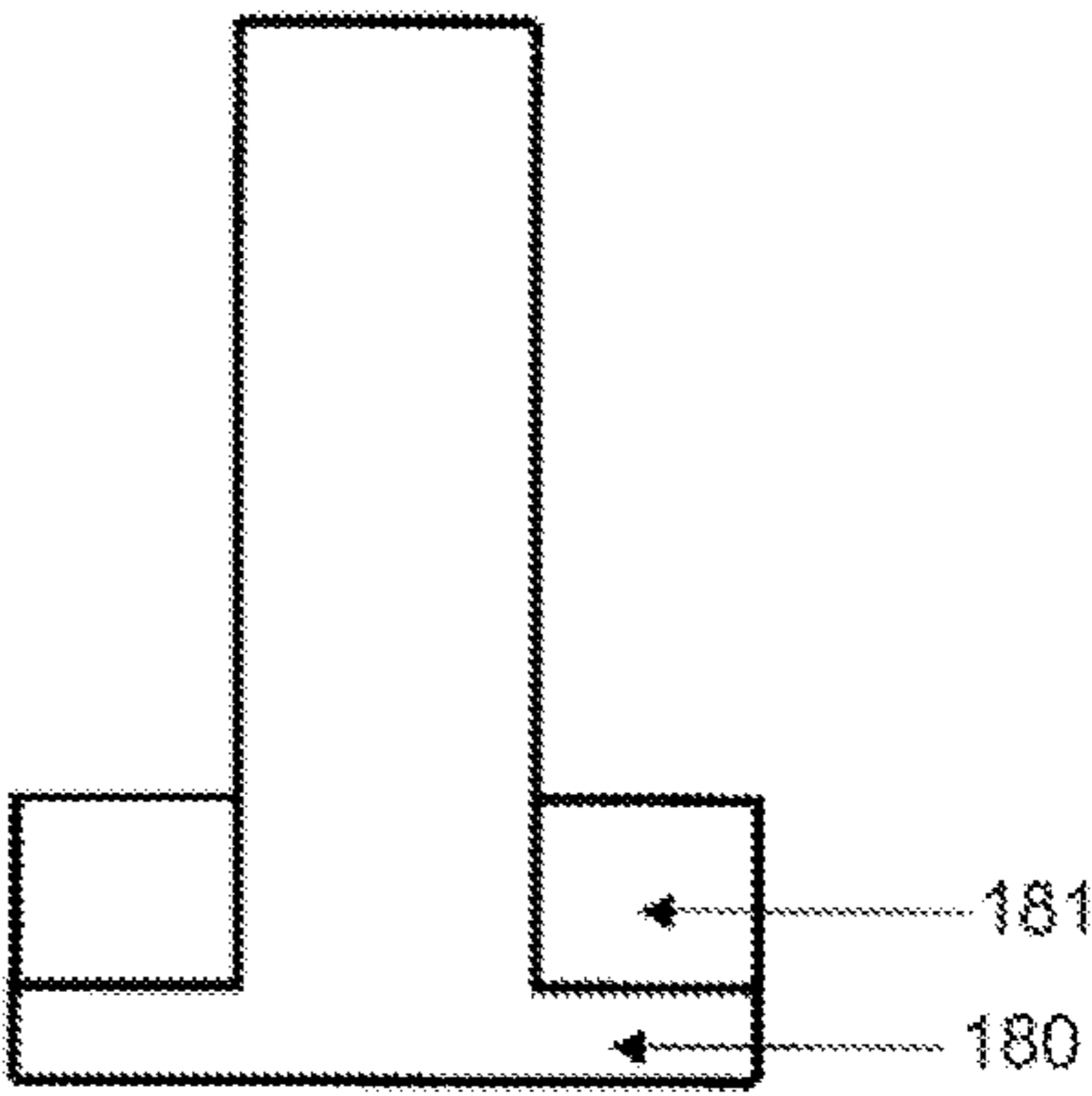


FIG. 13B

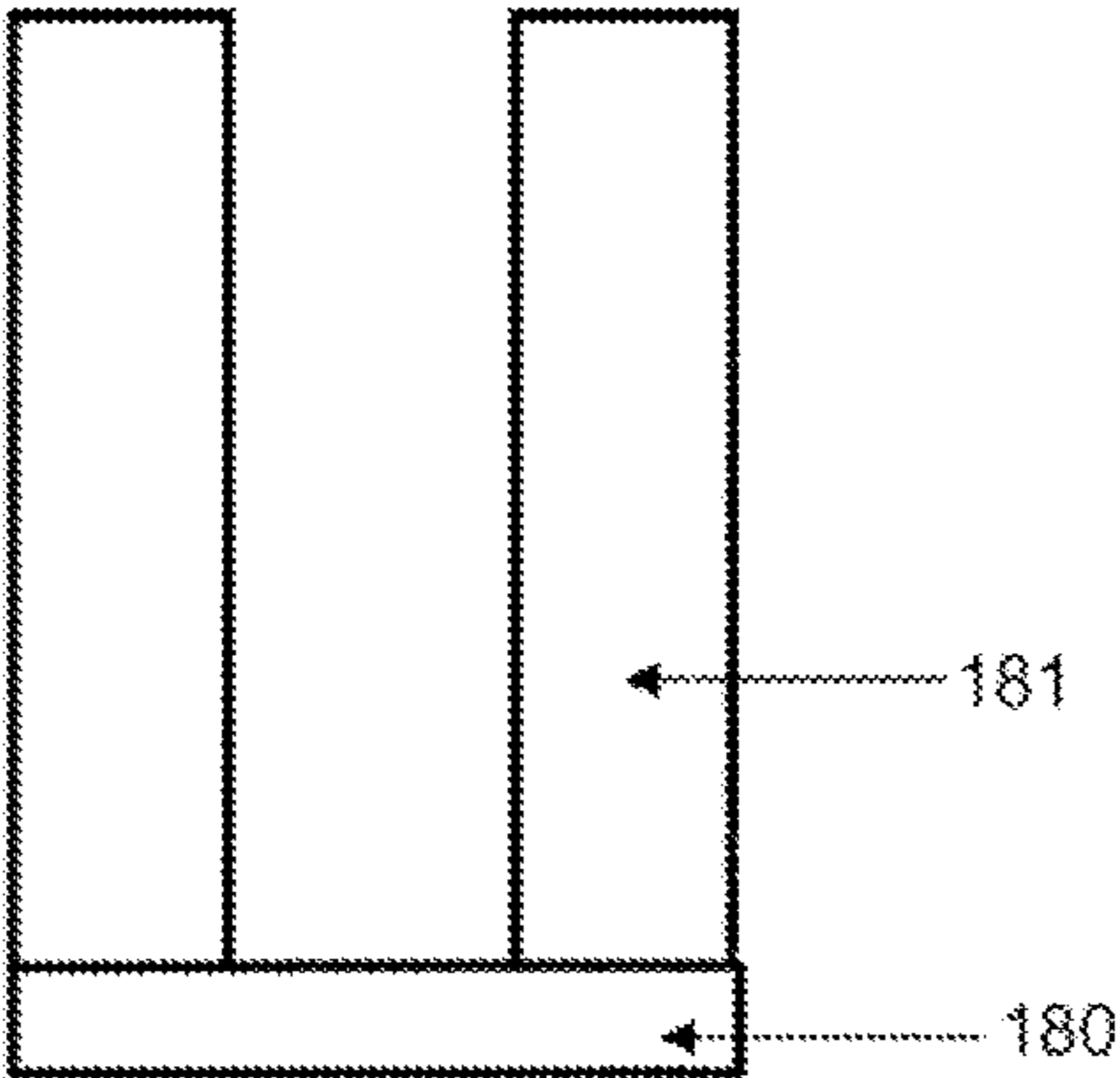


FIG. 13E

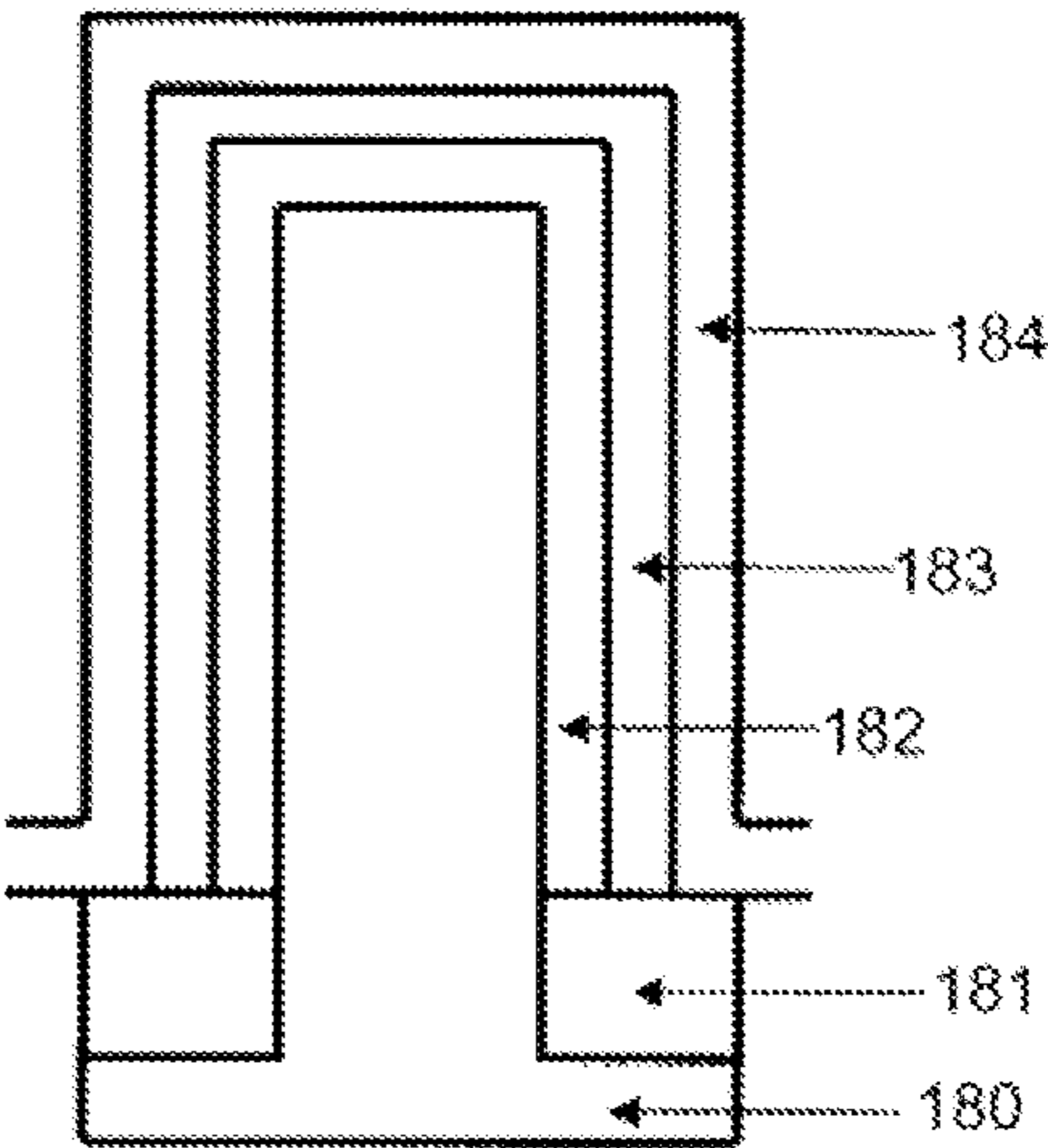
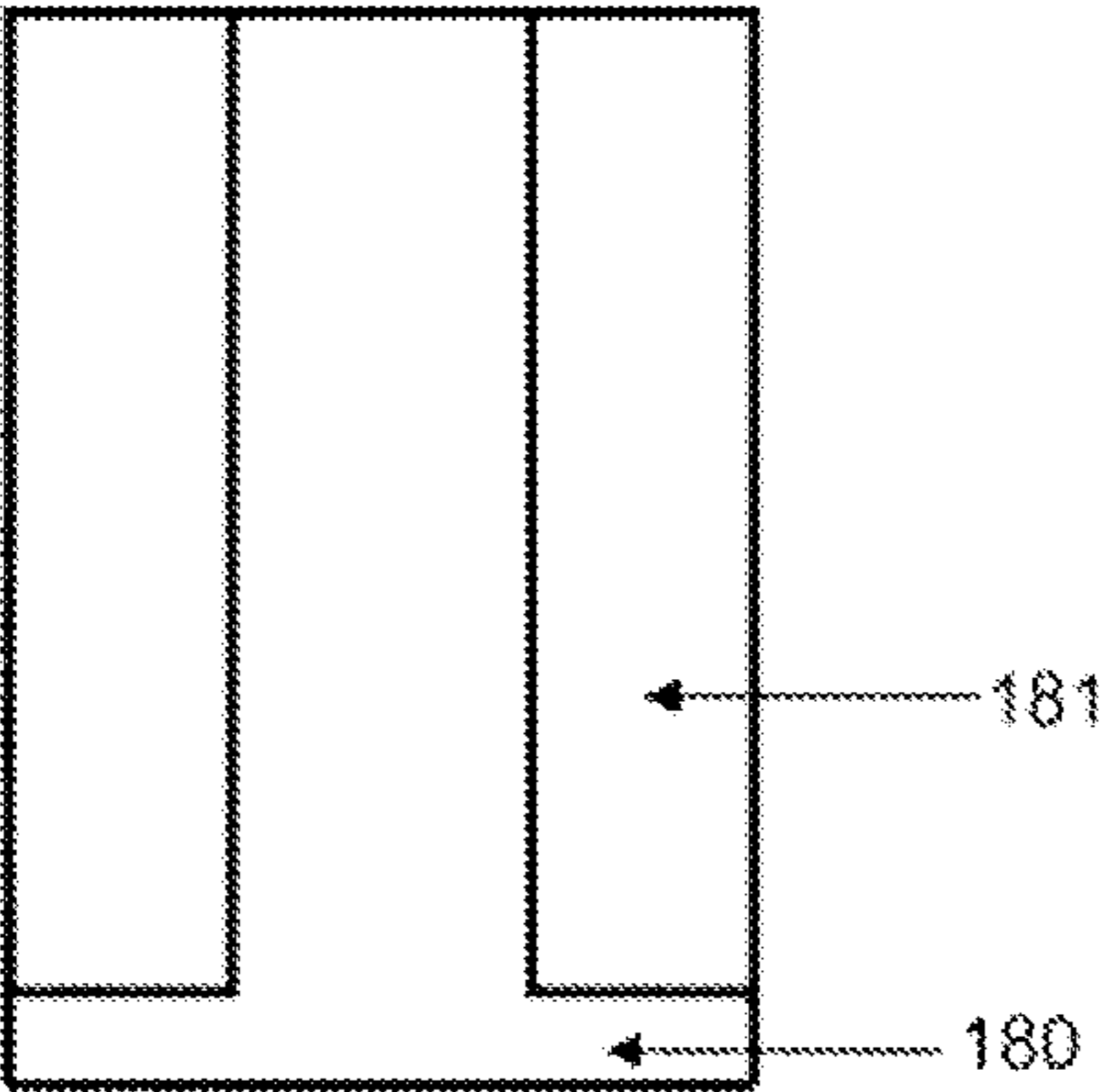


FIG. 13C



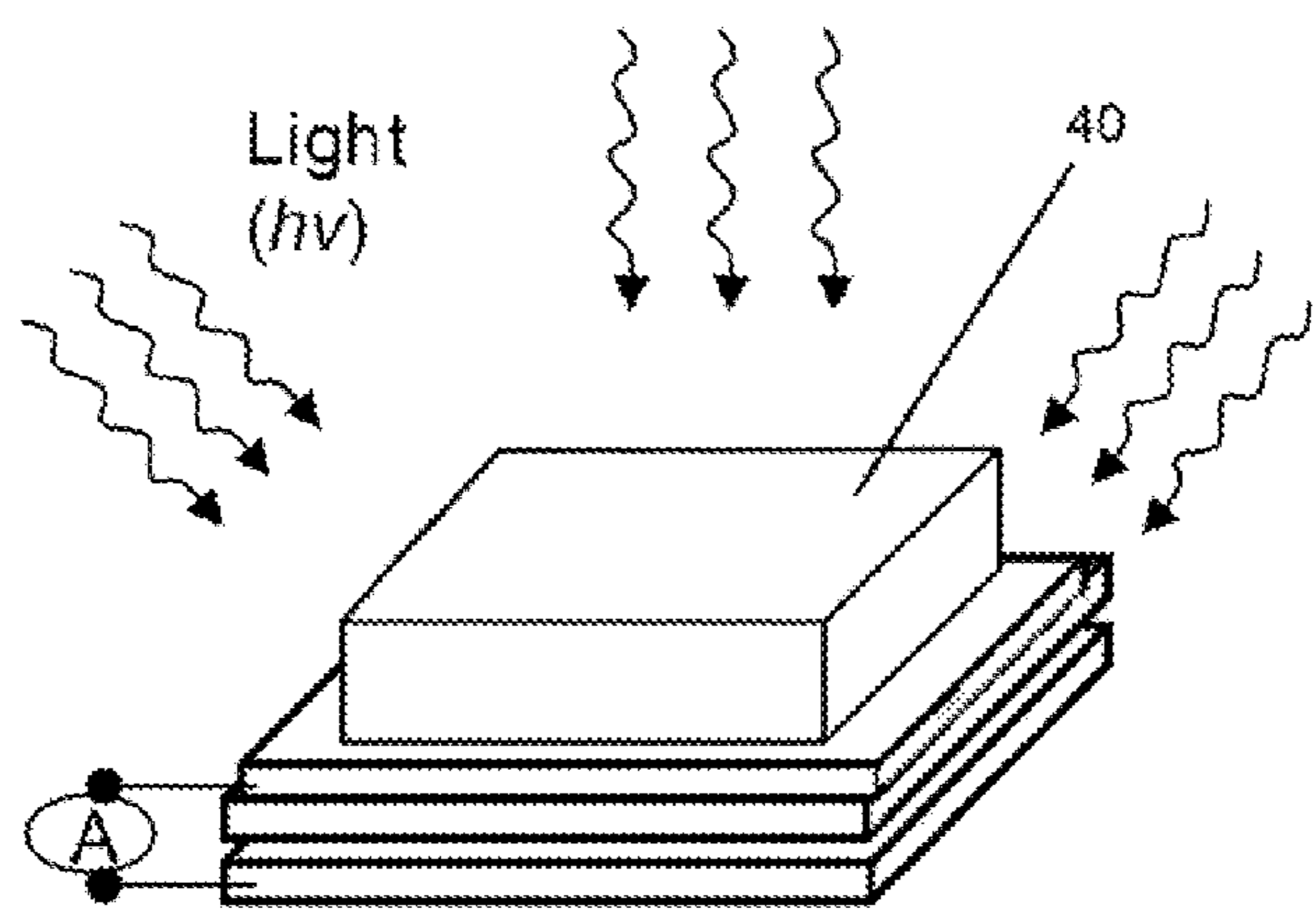


FIG. 14

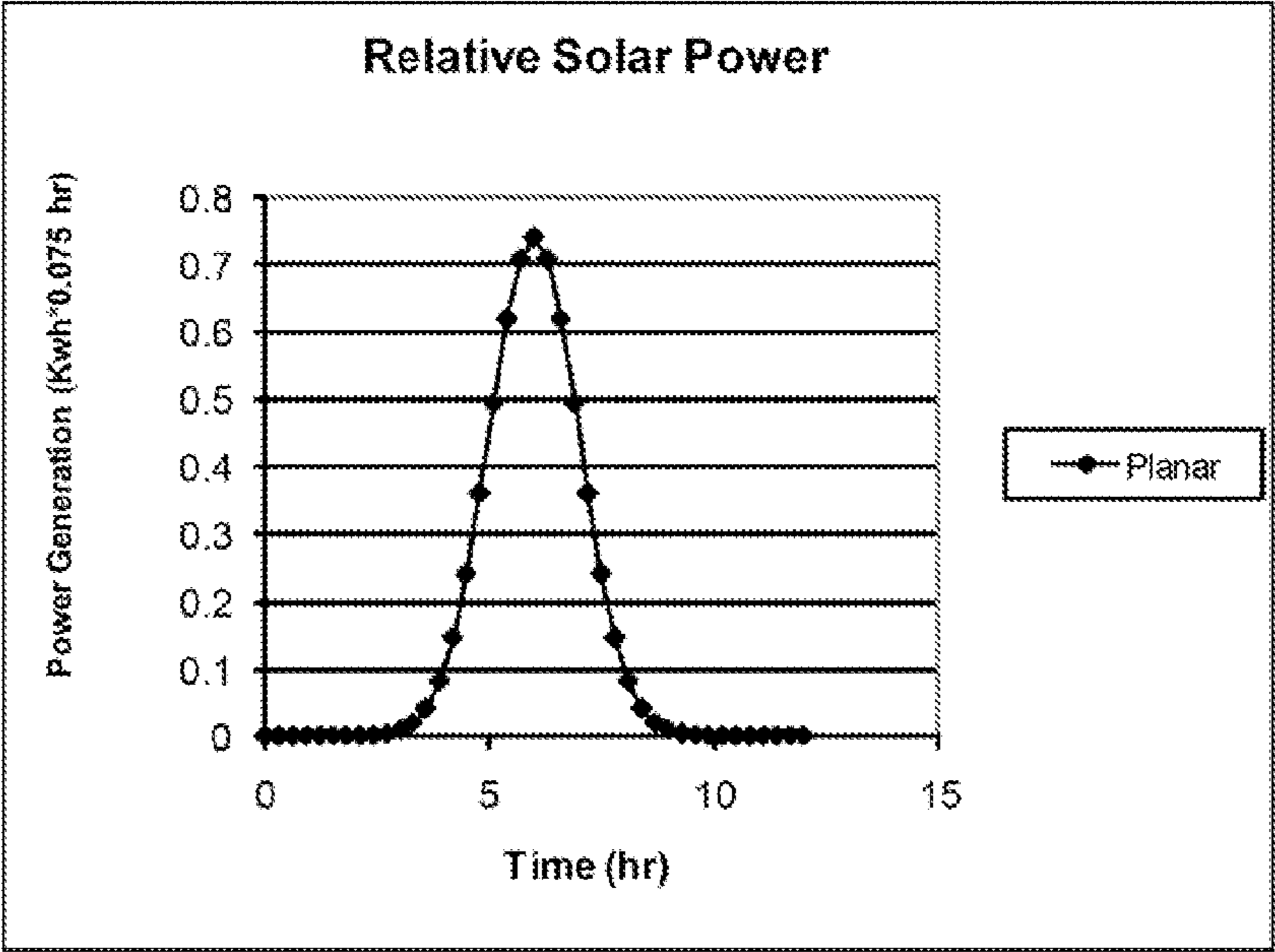


FIG. 15

## NANOSTRUCTURE AND PHOTOVOLTAIC CELL IMPLEMENTING SAME

### RELATED APPLICATIONS

**[0001]** This application claims priority to U.S. Provisional Patent Application Ser. No. 60/710,097 filed Aug. 22, 2005, and which is herein incorporated by reference. This application also claims priority to U.S. Provisional Patent Application Ser. No. 60/710,262 filed Aug. 22, 2005, and which is herein incorporated by reference.

### FIELD OF INVENTION

**[0002]** This invention pertains generally to nanotechnology and particularly to nano-scale structures and processes for making these structures.

### BACKGROUND

**[0003]** Solar panels that harness solar energy and convert it to electrical energy are well known. A typical solar electricity system includes the following components solar panels, charge controller, inverter, and often batteries. A typical solar panel, often referred to as a photovoltaic (PV) module, consists of a one or more interconnected PV cells environmentally sealed in protective packaging consisting of a glass cover and extruded aluminum casing.

**[0004]** The PV cell may be a p-n junction diode capable of generating electricity in the presence of sunlight. It is often made of crystalline silicon (e.g., polycrystalline silicon) doped with elements from either group 13 (group III) or group 15 (group V) on the periodic table. When these dopant atoms are added to the silicon, they take the place of silicon atoms in the crystalline lattice and bond with the neighboring silicon atoms in almost the same way as the silicon atom that was originally there. However, because these dopants do not have the same number of valence electrons as silicon atoms, extra electrons or “holes” become present in the crystal lattice. Upon absorbing a photon that carries an energy that is at least the same as the band gap energy of the silicon, the electrons become free. The electrons and holes freely move around within the solid silicon material, making silicon conductive. The closer the absorption event is to the p-n junction, the greater the mobility of the electron-hole pair.

**[0005]** When a photon that has less energy than silicon’s band gap energy strikes the crystalline structure, the electrons and holes are not mobilized. Instead of the photon’s energy becoming absorbed by the electrons and holes, the difference between the amount of energy carried by the photon and the band gap energy is converted to heat.

**[0006]** While the idea of converting solar energy to electrical power has much appeal, conventional solar panels have limited usage because their efficiencies are generally only in the range of 15% and are manufactured using costly silicon wafer manufacturing processes and materials. This low efficiency is due in part to the planar configuration of current PV cells, as well as the relatively large distances between the electrodes and the P-N junction. Low efficiency means that larger and heavier arrays are needed to obtain a certain amount of electricity, raising the cost of a solar panel and limiting its use to large-scale structures.

**[0007]** The most common material for solar cells is silicon. Crystalline silicon comes in three categories; single-crystal silicon, polycrystalline silicon, and ribbon silicon. Solar cells made with single or monocrystalline wafers have the highest

efficiency of the three, at about 20%. Unfortunately, single crystal cells are expensive and round so they do not completely tile a module. Polycrystalline silicon is made from cast ingots. They are made by filling a large crucible with molten silicon and carefully cooling and solidifying them. The polycrystalline silicon is less expensive than single crystal, but is only about 10-14% efficient depending on the process conditions and resulting imperfections in the material. Ribbon silicon is the last major category of PV grade silicon it is formed by drawing flat, thin films from molten silicon, and has a polycrystalline structure. Silicon ribbons efficiency range of 11-13% is also lower than monocrystalline silicon due to more imperfections. Most of these technologies are based on wafers about 300  $\mu\text{m}$  thick. The PV cells are fabricated then soldered together to form a module. **[0008]** Another technology under development is multi junction solar cells, which is expected to deliver less than 185% efficiency in actual use. The process and materials to produce multifunction cells are enormously expensive. Those cells require multiple gallium/indium/arsenide layers. The best is believed to be a sextuple-junction cell. Current multi-junction cells cannot be made economical for large-scale applications

**[0009]** A promising enabler of PV cells and other technology is nanotechnology. However, one problem with implementing nanotechnology is that the minute conductors may not be able to withstand their own formation, much less subsequent processing conditions or conditions of use in the end product. For example, the metal forming the nanoconductors may be soft, making it prone to bending or breaking during application of additional layers.

**[0010]** Further, it has heretofore proven difficult and even impossible to create nanoarrays having structures of uniform size and/or spacing.

**[0011]** Thus, as alluded to, the technology available to create PV cells and other electronic structures is limited to some extent by processing limitations as well as the sheer fragility of the structures themselves.

**[0012]** Therefore, it would be desirable to enable creation of nanostructures having high aspect ratios and yet are durable enough for practical use in industry.

**[0013]** It would also be desirable to enable fabrication of a solar cell that has a higher than average efficiency, and in some embodiments, higher than about 20%.

### SUMMARY

**[0014]** A photovoltaic structure according to one embodiment of the present invention includes an array of photovoltaic nanostructures, and a photovoltaic device, the photovoltaic device being at least semi-transparent. The array is positioned relative to the photovoltaic device such that light passing through the photovoltaic device strikes the array.

**[0015]** Various configurations are contemplated. In one aspect, the array of photovoltaic nanostructures is arranged in a brush configuration. Axes of the photovoltaic nanostructures may be tilted from a direction normal to the array. In another aspect, the photovoltaic device is a planar photovoltaic structure.

**[0016]** In a further aspect, the photovoltaic device is a second array of photovoltaic nanostructures. The first and second arrays of photovoltaic nanostructures may be arranged in a brush configuration, wherein a height of the photovoltaic nanostructures in the first array is different than an average height of the photovoltaic nanostructures in the second array

in one embodiment, the photovoltaic nanostructures of the first array have the same composition as the photovoltaic nanostructures of the second array. In another embodiment, the photovoltaic nanostructures of the first array have a different composition than the photovoltaic nanostructures of the second array. For example, the photovoltaic nanostructures of the first array may comprise an organic material, wherein the photovoltaic nanostructures of the second array comprise inorganic materials. In another example, the photovoltaic nanostructures of the first array comprise, inorganic materials, wherein the photovoltaic nanostructures of the second array comprise inorganic materials.

**[0017]** The nanostructures of the second array may be coated with at least one high bandgap material, and the nanostructures of the first array are coated with at least one low bandgap material.

**[0018]** A nanostructure according to one embodiment of the present invention includes an array of nanocables extending from a substrate, the array of nanocables being formed using a template, an insulating layer extending along the substrate, and at least one layer overlaying the nanocables.

**[0019]** The nanocables may be elongated.

**[0020]** The template may be partially removed. At least a portion of the template may form the insulating layer.

**[0021]** The nanocables may have substantially uniform peripheries.

**[0022]** The template may be a membrane.

**[0023]** The at least one layer may be electroplated, may be formed by chemical vapor deposition and etching, etc.

**[0024]** A nanostructure according to yet another embodiment of the present invention includes a nanocable having a rough outer surface and a solid core.

**[0025]** A method for creating a nanostructure according to yet another embodiment of the present invention includes depositing material in a template for forming an array of nanocables, removing the template, forming an insulating layer between the nanocables, and forming at least one layer over the nanocables. The at least one layer may be formed by electroplating. The at least one layer may be formed by chemical vapor deposition, while etching may be used to expose the insulating layer. The at least one layer may create a photovoltaically active p-n junction.

**[0026]** A method for creating a nanostructure according to yet another embodiment of the present invention includes depositing material in a template for forming an array of nanocables, removing only a portion of the template such that the template forms an insulating layer between the nanocables, and forming at least one layer over the nanocables. The at least one layer is formed by electroplating, chemical vapor deposition, etc. The at least one layer may create a photovoltaically active p-n junction.

**[0027]** A method for creating a nanostructure according to yet another embodiment of the present invention includes depositing material in a template for forming an array of pillars, removing the template, forming at least one layer over the pillars such that the pillars are covered by the at least one layer, and depositing a metal contact over the at least one layer such that the at least one layer is covered by the metal contact.

**[0028]** A method for creating a reinforced nanostructure according to yet another embodiment of the present invention includes forming a nanotube of a first material in a template, forming a nanocable of a second material in the nanotube, and at least partially removing the template. Preferably, the first

material is more rigid than the second material. Also preferably, the first material has a higher heat resistance than the second material.

**[0029]** A method for creating a reinforced nanostructure according to yet another embodiment of the present invention includes forming a nanotube of a first material in a template, forming a nanocable of a second material in the nanotube, removing the nanotube from between the template and the nanocable, depositing a reinforcing layer between the template and the nanocable, and at least partially removing the template.

**[0030]** A method for creating an array of nanotubes having a defined width perpendicular to an axis thereof according to yet another embodiment of the present invention includes forming a nanotube of a polymeric material in a template, forming a nanocable of a second material in the nanotube, at least partially removing the template, and at least partially removing the polymeric material.

**[0031]** A method for creating a nanocable with a rough outer surface according to yet another embodiment of the present invention includes plating a thin film of metal over the surface of a metallic nanocable such that the metal forms alloys with the nanocable at the surface of the nanocable, and removing the metal from the surface of the nanocable, wherein the outer surface of the nanocable is rough upon removal of the metal.

**[0032]** A method for creating a nanocable through etching a membrane on a conductor according to yet another embodiment of the present invention includes depositing material in a template for forming: an array of nanocables, removing the template, forming an insulating layer between the nanocables, and forming at least one layer over the nanocables.

**[0033]** Thus, one embodiment of the invention includes a method of forming conductive nanostructures. The method allows a precise control of radial and vertical dimensions of the conductive core and semiconductor coating(s). The resulting nanostructure is known as a nanocable. With the method, nanocable arrays can be molded into any number of geometries, and then made rigid. Nanocable arrays may be made flexible, if desired. Nanocable arrays made according to the invention have improved electrical junctions, improved reliability, and improved performance.

**[0034]** In another aspect, the invention is a PV structure that overcomes many limitations of current PV cell designs. With the PV cell disclosed herein, all photon absorption events occur near the p-n junction for maximum efficiency. Light is diluted up to several orders of magnitude to reduce hot spots throughout the cell. The design can reduce back reflection of photovoltaic cells to less than 1%, and reduces the quantities of scarce materials needed to produce a photovoltaic cell. Nanocable semiconductor layers may be tuned to be spectrally selective of different light wavelengths to further increase performance. Thus, the PV cell overall reduces the cost unit area for most photovoltaic materials and increases high output industrial power.

**[0035]** In yet another aspect, the invention is a method of enhancing the structural soundness of a nanocable structure having a metal nanocable. Where the nanocable is made of a "soft" metal such as gold or copper, the nanocable may be coated with a harder metal or compound for structural reinforcement. Metals can also be deposited as alloys to increase the hardness of the nanocable.

**[0036]** The invention according to another embodiment includes a method of using organic polymer thin films to

precisely control the dimensions of a nanocable. One or more organic polymer thin films may be deposited on the inner wall of a pore in a membrane that is used to produce the nanocable, so that when the nanocable material fills the remaining space in the pore, its dimensions will be precisely controlled by the thickness of the polymer layers.

**[0037]** The invention according to another embodiment includes a method of forming nanoporous nanostructures. Using surface alloying, nanostructure surface may be changed from smooth to rough. Nanoporous or nano-rough nanostructure obtained by this method is more robust because the nanoporous layer is confined to a few monolayers in the surface (not the entire bristle volume) and still maintains a plain metallic core.

**[0038]** In another aspect, the invention is a method for creating an integrated nanostructured device—the nanocable array—where the individual elements—the nanocables—are insulated from one another. Each individual structure is a singular device.

#### BRIEF DESCRIPTION OF THE DRAWINGS

**[0039]** FIG. 1 is a perspective view of an exemplary solar brush that may be used to implement solar panels with improved efficiency.

**[0040]** FIG. 2 is a side cross-section of the solar brush.

**[0041]** FIGS. 3A and 3B are cross-sectional views of embodiments of the solar brush having a layered structure.

**[0042]** FIG. 3C is a cross-sectional view of a solar brush embodiment employing a thin film planar device as a filter.

**[0043]** FIG. 4 is a top view of the solar brush showing the tops of the bristles.

**[0044]** FIGS. 5A-5H illustrate an exemplary method for fabricating the solar brush.

**[0045]** FIGS. 6A-6F illustrate an alternative method for fabricating the solar brush.

**[0046]** FIGS. 7A-7H illustrate a hard-metal reinforcement process that may be used to strengthen fragile nanocables.

**[0047]** FIG. 8 is a flowchart summarizing the steps of the hard-metal reinforcement process of FIGS. 7A-7H.

**[0048]** FIGS. 9A-9H illustrate a dimension-controlling process for forming the nanocables.

**[0049]** FIG. 10 is a flowchart summarizing the steps of the dimension-controlling process of FIGS. 9A-9H.

**[0050]** FIG. 11A-11I illustrate the carbon jacket process for producing organic nanocables.

**[0051]** FIG. 12 is a flowchart summarizing the steps of the carbon jacket process of FIGS. 11A-11I.

**[0052]** FIG. 13 A-E: illustrates the insulator etching process.

**[0053]** FIG. 14 shows the solar brush encapsulated in an optical casing for protection.

**[0054]** FIG. 15 is a graph showing a potential power generation for a planar solar cell.

#### DETAILED DESCRIPTION OF THE EMBODIMENT(S)

**[0055]** The following description is the best mode presently contemplated for carrying out the present invention. This description is made for the purpose of illustrating the general principles of the present invention and is not meant to limit the inventive concepts claimed herein. Further, particular features described herein can be used in combination with other

described features in each and any of the various possible combinations and permutations.

**[0056]** Embodiments of the invention are described herein in the context of solar cells. However, it is to be understood that the particular application provided herein is just an exemplary application, and the nanocable arrangement of the invention is not limited to the application or the embodiments disclosed herein.

**[0057]** This disclosure also relates to nano arrays of thin film solar cells. Solar modules constructed using thin film systems tend to use a single larger single plane thin films solar cell, rather than an array of smaller interconnected nano-scale solar cells. The entire module can use a laser scribe to mark individual cells, it is important to note nano systems will be processed differently than current technology thin films. Four main thin film material system types are amorphous silicon (A-Si), copper indium selenide ( $\text{CuInSe}_2$  commonly referred to as CIS), copper indium gallium selenide ( $\text{CuIn}_{1-x}\text{Ga}_x\text{Se}_2$  commonly referred to as CIGS), and CdTe/CdS. A-Si films are typically fabricated using plasma enhanced chemical vapor deposition (PE-CVD).

**[0058]** The term “nanocable” denotes any elongated body whose one dimension (e.g. diameter or width) is of nanoscale size and the other dimension is larger, potentially much larger. A nanocable may be fabricated with dissimilar materials, either as a core rod or wire that is laterally enveloped by one or more layers of material(s), as a nanotube that is filled with one or more layers of material(s), or as a single structure of one material. Nanocables are also referred as nanorods or nanowires or filled nanotubes. The junction on an element of the nanocable in each case is the interface(s) between the two (or more) materials. In various alternative configurations and modes of growth, a succession of layers of different materials, alternating materials or different thicknesses of materials can be deposited to form nested cylinder nanocables.

**[0059]** The term “photovoltaically active p-n junction” denotes any p-n junction with an adequate p-layer and n-layer thickness to generate electricity.

**[0060]** FIG. 1 is a perspective view of an exemplary solar brush 10 that may be used to implement solar cells with improved efficiency. As shown, the solar brush 10 has a substrate 12, a first conductive layer 14, an insulator layer 16, a second conductive layer 18, and a plurality of bristles 20. Although the bristles 20 are shown to be cylindrically-shaped in the particular embodiment, they may be of any other shape including but not limited to cones, rectangular, domes, or more complex geometries such as branched bristles etc. Each of the bristles 20 has a nanocable extending through its center, and layers of semiconductor around the nanocable. Bristles may protrude vertically from the substrate or may protrude at angles. Bristles protruding at angles may increase the amount of semiconductor materials exposed to the sun when the sun is directly overhead and may improve internal reflections. Bristles can be modified to be smooth or hairy. Hair bristles may improve light absorption efficiency by further increasing the surface area or improving internal reflections. Various shapes can be obtained using asymmetric pore membranes. One or more electrically conductive strips 33 may extend across the array or portion thereof to assist in carrying electricity away from the array, thereby improving the overall efficiency of the brush. The efficiency gains are more pronounced in larger arrays. Such strips 32 are preferably very thin to block minimal light.

[0061] FIG. 2 is a side cross-section of the solar brush 10 with a metal nanocable. As shown, each of the bristles 20 has a nanocable 22 extending through its core. The nanocable 22 is typically a p-layer conductor, and extends through the insulating layer 16 to electrically connect with the first conductive layer 14. A p-type semiconductive layer 24 of a sufficient thickness surrounds the nanocable 22, and an n-type semiconductive layer 26 of sufficient thickness is coated on the p-type semiconductive layer 24 to create a photovoltaically active p-n junction in each of the bristles 20. An n-layer conductor is deposited on the tops of the bristles 20 and the insulating layer 16 to form the second conductive layer 18.

[0062] The closer the photon absorption event is to the p-n junction, the more likely the event will result in usable electricity. In the case of a nanobrush, a reflective back contacting layer is not required because the photon can continue along the linear path so that it can contact the material on the opposite side of the cell thereby achieving a double pass in each nanobristle, in FIG. 2, five cylindrical shell solar brush PV cells are shown. Thus, if the photon passes slightly off-center of the shell it has the potential to come into contact with as many as the equivalent of 10 p-type layers (the layer where the solar event takes place) in this brush design. If the p-layer conductor is sufficiently small most of the photons will pass through five thicker layers, in the actual case, the solar brush, with millions of bristles per  $\text{cm}^2$ , would effectively approach the efficiency of 100% of all usable photon energy.

[0063] The substrate 12 may be a conductive material or a nonconductive material (coated with a conductive material), rigid or flexible. For example, the substrate 12 could be glass, doped silicon, diamond, metal, polymer, ceramics, or a variety of composite materials. Thin metal foil or certain polymers can be used where flexibility is desired. Structural integrity of the nanocable will vary with material choices. In the case of brittle or easily deformable bristles, a flexible substrate material could be used if attached to a rigid or semi-rigid surface. The molded surface/flexible membrane may be of particular help when PV cells are desired for an aerodynamic surface such as an airplane part, the roof of a car, the surface of other vessels or portable devices.

[0064] Each of the bristles 20 is a discrete nanoscale PV cell. Compared to conventional flat PV cells design where only a single "xy" planar surface is exposed to light, the solar brush 10 has an "xyz" or a three-dimensional surface. Thus, for a given volume, the solar brush 10 has a useful surface area that can be several times to thousands of times greater than the "xy" surface area of conventional PV cells. The area between solar bristles 20 could be sufficiently wide as to make the brush absorptive to the majority of photons. Additionally, the bristles may be thin enough to be partially transparent. This effective transparency and bristle spacing would increase effective energy generation to happen from sunrise to sunset while flat PV cells work optimally when the sun is straight above the PV surface. Because the effective energy generation from the solar brush is expected to be many times higher than conventional PV cell technology, the weight per kilowatt generated would be many times lower. This would allow use in small applications such as charging electronic devices (cell phone, computer, PDA, etc.), use in medium scale applications such as light weight roof-top energy for industrial and agricultural power generation, and use in large applications such as a light weight energy source for transportation (automobile, aircraft, barges). The efficiency of the cell would also enable improved power generation in low light conditions.

The wide range of spectrum adsorption may also generate power from infra-red light at night time. Another advantage of using nanocable structures is that the p-n junction associated with each nanocable has a smooth interface that results in a sharper junction. The smoothness is improved at nanoscale as the roughness (measured as rms-root mean square for instance) increases as the scale increases.

[0065] It should also be noted that though the axes of the bristles 20 are oriented normal (perpendicular) to the plane of the array in the drawings, the axes of the bristles may be tilted slightly (a few degrees from normal) or pronouncedly (e.g., 40-89 degrees). One reason why a tilted configuration may be desirable is to reduce unimpeded penetration of light into the array when the light is traveling in a direction normal to the array.

[0066] FIG. 3A is a perspective view of the solar brush 10 having a layered structure. The structure in FIG. 3A has a layer A and a layer 13, with each layer having a substrate 12a, 12b, the first conductive layer (not shown), the insulator layer (not shown), the second conductive layer (not shown), and bristles 20a, 20b. In the layered structure, the substrates 12a, 12b, the first conductive layers, the insulator layers, and the second conductive layers of one or both layers may be at least semi-transparent so that light can travel between the layers A and/or B. When the solar brush 10 is layered, the bristles 20 do not have to be as densely arranged as in the single-layer structure to achieve the same efficiency. The layered structure can be made using cruder, and therefore less expensive, equipment than the single-layered structure with densely arranged bristles 20. Although the embodiment shown in FIG. 3A has two layers of approximately the same bristle height, this is not a limitation of a layered brush structure. Photovoltaic cells with layered brush structures can contain as many layers as desired to generate the desired efficiency and power.

[0067] The layered brush structure can also be used to increase photovoltaic cell efficiency by using a high- and low-band gap material and semiconductor thicknesses tuned for spectral selection. A high band gap material can be used to coat the upper photovoltaic brush, and the low band gap material can be used to coat the lower photovoltaic brush. The upper material would convert higher-energy light to electricity and dissipate much heat. The lower material could convert lower-energy light. This would increase both the efficiency and the life span of the lower brush A.

[0068] The layered structure does not need to be made of the same material or by the same process. For example, the upper brush could be produced using a conductive/transparent core of silicone and a silicone substrate made from photolithography and chemical vapor deposition and the lower brush made with organic dye technology. This way, the low-band-gap light can easily pass through the upper layer and reach an organic nanocable base. The base may be made from an anodized aluminum template, carbon nanojacket, and wet polymer process. The layers may or may not have the same dimensions and/or composition. The design need not be limited to two types of photovoltaic cells. A multitude of cells can be included with a multitude of photovoltaic materials so long as each cell has adequate transparency for light to reach the cell below.

[0069] FIG. 3B is a side view of another layered structure wherein the average brush heights are different in the two layers. The embodiment of FIG. 3B illustrates that there can be structural differences between the two layers.

[0070] FIG. 3C illustrates that a transparent thin film planar device can also be employed as a filter over a brush layer if desired.

[0071] Thin, minimal reflectance metals such as gold may be layered along with the n- and p-layers to conduct the current so further gains in efficiency might be achieved.

[0072] One benefit over current technology is, as previously mentioned, that the maximum efficiency for a given material can be achieved. Another potential benefit may be achieved by layering material with different band gaps (energies required to excite electrons). The idea is to have a high band gap material such as GaAs (max efficiency ~20%, band gap ~1.4 eV) or CdTe (max efficiency ~30%; band gap ~1.6 eV) at the tip of the bristle and a reduced band gap material further down the bristle such as CIS or CIGS type PV material further down (max efficiency of ~24%, band gap ~0.8 eV). Photons with low energy will not react with high band gap material but will be available to react with low band gap material further down the bristle at further penetration depths. This could be achieved by CND of CIS material on a nanocable, followed by etching to the top metal core of the nanocable, followed by catalytic growth on top of the nanocable, and the cable would be finished up by electroplating of CdTe/CdS. The solar brush PV cell design could also be a multi-junction cell and is a superior architecture for such.

[0073] A flexible nanopore substrate can be used as the substrate 12 for deposition of metal. The substrate 12 could be a membrane applied to or constructed on a thin conductive sheet, and may be made into any desired shape. After metal deposition in the membrane pores occurs, the bristles 20 are formed. While other PV tapes and films have XY flexibility and strength, they are limited and no other technology allows for XYZ design of a rigid or flexible long lasting solar cell. The varied geometry of the solar brush allows the PV cells to be optimized for solar exposure from a fixed location, optimal aesthetic appeal, and minimal aerodynamic drag for transportation applications. Specific geometries combined with reflective substrates can effectively produce a combined PV film and solar concentrator.

[0074] There are many combinations of materials that may be used for the solar brush 10. One configuration is to use a Si thin film. Other configurations include CdTe/CdS (CdTe/CdS/SnO<sub>2</sub>/Indium Tin Oxide(ITO)/glass), GaAs/GaInP, CuInGaSe<sub>2</sub>, Cu(In<sub>x</sub>Ga<sub>1-x</sub>)(S, Se)<sub>2</sub>, CuIn<sub>1-x</sub>Ga<sub>x</sub>Se<sub>1-y</sub>S<sub>y</sub>, CGSe/CdS, CuIn<sub>x</sub>Ga<sub>1-x</sub>Te<sub>2</sub>/n-InSe, CdS/CIGS interface, ZnS/CIGS, Cu<sub>2</sub>S—CdS, CuInS<sub>2</sub> or a mix of Cu<sub>x</sub>S, CuInS<sub>2</sub> and CuIn<sub>5</sub>S<sub>8</sub>, Cu(In, Ga)Se<sub>2</sub>/CdS, CIS/In<sub>2</sub>Se<sub>3</sub>, InN, CIS/In<sub>2</sub>Se<sub>3</sub>, ZnS<sub>x</sub>Se<sub>1-x</sub>, GaInP/GaAs, GaInP/GaAs/Ge, GaAs/CIS, a-Si/CIGS (a-Si is amorphous Si/hydrogen alloy), FeS<sub>2</sub>, Cu<sub>2</sub>O, ITO/a-CN<sub>x</sub> (Al Schottky thin-film carbon nitride solar cells), and MoS<sub>2</sub> based solar cells or more general: MX<sub>2</sub> (M=Mo, W; X=S, Se) thin films with Ni and Cu additives layers may be used as well. An Al<sub>2</sub>O<sub>3</sub> layer may be used as a diffusion barrier with the CuInGaSe<sub>2</sub> type PV cells. The manufacturing step may include heat annealing at high temperatures to allow for the consolidation of polycrystalline deposits to form a single crystal material or improve the structural integrity and regularity or geometry of the materials. Alternatively, single crystalline growth of layers should be favored by slow growth of the layers at moderate temperatures. Single crystalline deposits are important for optimum electron transport and photon absorption.

[0075] Deposition of the various materials can include chemical vapor deposition, solution phase deposition, elec-

trochemical deposition, electrochemically induced sol gel deposition, electrochemical atomic layer epitaxy, electroless deposition, e-beam evaporation, sol-gel with electrophoresis or centrifugation, electron beam lithography, scanned probe lithography, pressure injections, polymerization and electropolymerization, and pyrolytic decomposition. Nanocables can also be grown from catalyst sites from chemical vapor deposition, wet or dry etched from a substrate, etc.

[0076] When designing a PV cell, one of the considerations is the photon flux. The number of photons that make it through the atmosphere at a given point remains relatively constant regardless of modifications in the PV cell that receives them. When determining the appropriate geometry for a PV cell, it is convenient to start by calculating the area of the gaps and the area of the bristle-tops.

[0077] FIG. 4 is a top view of the solar brush 10 showing the tops of the bristles 20. Although the bristles 20 are shown to be arranged regularly, this arrangement can be changed to suit the application. The tops of the bristles 20 have a combined area of which is calculated as  $\pi(D/2)^2\rho$  wherein D is the diameter of the bristle and  $\rho$  is the cable density (number of cables/unit area). The total area ( $A_{total}$ ) of the PV cell is W×L. The area of the gaps between the bristles can then be calculated using the following formula:

$$A_{total}=A_{top}+A_{gap}$$

[0078] During the same calculation, it is useful to determine if the spacing for a given cable density is viable for given geometries. When the diameter of the nanocable 22 ( $D_{nanocable}$ ) is 50 nm, the minimum PV bristle diameter D is about 220 nm. When  $D_{nanocable}=150$  nm, the minimum PV bristle optical thickness is about 320 nm. The physical diameter of the bristles 20 will be 100-500 nm larger than the diameter of the nanocable 22, but these numbers should be used for the optical diameter calculations because the outer shell is transparent. The optical diameter is used for calculating the solar efficiency, and the physical diameter is used for determining process limits.

[0079] One preferred density ( $\rho$ ) range for nanocables is:

$$\rho=10^6\text{-}10^9\text{ pores/cm}^2=10^{10}\text{-}10^{13}\text{ pores/m}^2$$

when using track etched membranes. When using metal oxide templates the density range shifts to:

$$\rho=10^{12}\text{-}10^{15}\text{ pores/m}^2$$

For the low density case, there is 1 cable per  $10^{-10}$  m<sup>2</sup>, or 1 cable in the center of a  $10^{-5}\times 10^{-5}$  square, so the separation between the center of cables is  $10^{-5}$  m or 10000 nm. From that number, the diameter of the bristle from its center axis (which extends through the length of the nanocable 22) to the n-layer is subtracted. The spacing may not smaller than the cable and is preferably larger, so cases involving unrealistic physical spacing were eliminated from calculations in Table i. Optical spacing, S, is given by the following:

$$S=\text{cable separation}(\text{center pt. to center pt.})-\text{diameter a bristle (semitransparent material)}$$

[0080] After Optical spacing is determined, the areas of the top of the PV bristles ( $A_{top}$ ) as well as areas between the bristles ( $A_{gap}$ ) are determined. Table 1 shows that majority of the planar surface area lies within the gaps of the PV cell, not the bristle tops. However, there are design points that have significant levels of top surface area.

TABLE 1

Planar area calculations for the PV brush.				
$A_{top}$ (m <sup>2</sup> )	$A_{gap}$ (m <sup>2</sup> )	$\rho$ (#/cm <sup>2</sup> )	D (nm)	S (nm)
$3.00 \times 10^{-4}$	0.9996	$10^6$	220	9780
$8.04 \times 10^{-4}$	0.9989	$10^6$	220	9680
$3.80 \times 10^{-2}$	0.9620	$10^8$	370	780
$8.04 \times 10^{-2}$	0.9892	$10^8$	370	680
$1.90 \times 10^{-1}$	0.8100	$5 \times 10^8$	220	227

**[0081]** Planar area and mass per area are crucial to determine back reflection. For planar cells, reflection bounces much of the light out of the PV cell before it has a chance to be absorbed and generate electricity. However, back reflection can benefit the planar cell by bouncing the light off of the back of the cell to give the cell two opportunities to absorb photons from the same stream of light. However, while the back reflection increases the number of absorptive events in the planar cell, it also increases the amount of heat generated per unit volume. In the case of the solar brush **10**, only a fraction of the photons that hit the bristle tops can reflect away from the PV cell.

**[0082]** In many cases with the solar brush **10**, over 96% of the light fills into  $A_{gap}$ . Several things happen to the light that falls into the gap: (a) the light is absorbed, (b) the light continues straight through the bristle into the next nearest bristle (as shown in FIG. 2), and/or (c) the light is reflected down into the solar brush at an angle of reflection equal to the angle of incidence. In each case, the light from the gap continues into the bristle. The majority of the light is either absorbed or continues straight through the brush. Back reflection is a function of material thickness as well as material type. Because the solar brush is made up of millions of thin bristles, they become nearly “transparent.” Thus, in every case except  $\Theta=90^\circ$  (where  $\Theta$  is defined as the angle of the sun relative to the plane of the PV cell substrate), back reflection is minimal. If it is assumed that 96% or greater light falls within the gap and each bristle has 90% transparency, then there is a maximum of 0.04% back reflection.

**[0083]** The depth and areas of penetrated light are also calculated. This is a measure of how uniformly the light can be dispersed throughout the PV brush. The penetration of light is governed by the following formula:

$$T_{pen} = \text{penetration thickness } S = \tan \Theta$$

The thickness or bristle height is related to the maximum penetration. The average penetration for a light stream in many cases would be about 0/2. However, as  $\Theta$  approaches  $90^\circ$ , the bottom of the cell could be theoretically flooded with light. However, in reality, this flooding effect is minimal or nonexistent because the light is affected by irregularities in the bristle geometry and can be eliminated by tilting the bristles slightly.

**[0084]** Table 2 shows how deep the light penetrates and what fractional area is used on a first pass by dividing  $T_{pen}$  by  $T$ , which is the total bristle height. This is a measure of how much the initial light is being diluted. More dilute light leads to lower maximum temperatures or fewer hot spots in the cell, resulting, in improved overall efficiency.

TABLE 2

Penetration percentage for a $T = 10 \mu\text{m}$ cell as a function of sun angle above the horizon				
Spacing (nm)	$\Theta = 10^\circ$ Penetration (%)	$\Theta = 45^\circ$ Penetration (%)	$\Theta = 80^\circ$ Penetration (%)	$\Theta = 90^\circ$ Penetration (%)
980	17.24	97.8	100	100.00
9680	17.07	96.8	100	100.00
780	1.38	7.8	44.24	100.00
680	1.20	6.8	38.56	100.00
227	0.40	2.27	12.87	100.00

Penetration percentage for a  $100 \mu\text{m}$  cell as a function of sun angle relative to the plane of the PV cell substrate is simply 10 times lower. The penetration is an important design criteria. For transparent cables, if there is 10% penetration, the light will have as few as 10 passes through PV cables, and the average photon would have up to 2.0 passes through the p-n junction since the photon may pass through the p-n junction twice per bristle. It is probably best to set design criteria to target less than 2.0% for most of the day to insure adequate absorption opportunities for the light stream. When  $\Theta$  goes to  $90^\circ$ ,  $\tan \Theta$  goes to  $\infty$ , temporarily making the penetration level 100%. Optimization, however, will be a function of field testing results.

**[0085]** The total PV absorption area is much greater for the sides of the bristles **20** than for the tops is the surface area available by PV brush which is given by:

$$A_{cell} = T(\pi)(D\rho/2)$$

where  $T$  is the height of the cable,  $D$  is the optical diameter of the PV bristle; and  $\rho$  is the number of bristles per unit area. The quantity is divided by 2 because it is assumed that most light absorption will come from the sun which is shining on half of the cell at one time. There will be significant absorption events from scattered light as well, but the majority of photons come directly from the sun. Table 3 summarizes some calculations, and shows that the PV cell surface area increases rapidly with denser cell spacing and bristle height, “Cell spacing” is measured from the center of one bristle to the center of its neighboring bristle.

TABLE 3

PV Brush Area Calculations				
Height ( $\mu\text{m}$ )	PV Diameter (nm)	Cable Density (#/cm <sup>2</sup> )	$A_{cell}$ (m <sup>2</sup> Brush/m <sup>2</sup> planar)	Cell Spacing (nm)
50	220	$10^6$	0.17	9780
100	220	$10^6$	0.35	9780
50	220	$10^8$	17.28	780
100	220	$10^8$	34.56	780
50	220	$5 \times 10^8$	86.40	227
100	220	$5 \times 10^8$	172.80	227
50	320	$10^6$	0.25	9680
100	320	$10^6$	0.50	9680
50	320	$10^8$	25.13	680
100	320	$10^8$	50.27	680

**[0086]** The penetration area is proportional to the penetration depth, as shown by the following formula:

$$A_{pen} = \text{area initially penetrated by light} = T_{pen}(\pi)(D\rho)$$

Where  $A_{gap} \gg A_{top}$  the dilution of light is represented by the following formula:

$$A_{pen} = T_{pen} / T^* A_{total}$$

From  $A_{pen}$  and  $A_{gap}$  (Table 1), a calculation that shows the amount of light dilution that occurs in the cell can be made. The light dilution is important to opportunities for solar absorption events and uniform heating. Wherever there are hot spots there is rapidly degrading conversion efficiency. Wherever there is concentrated light that tends to create hot spots, the ratio of opportunities for an absorption event to the number of photons decreases.

TABLE 4

Dilution levels for PV cells when the sun's angle is at 10°.				
Height ( $\mu\text{m}$ )	PV Diameter (nm)	Cable Density (#/cm <sup>2</sup> )	Cell Spacing (nm)	Dilution (times original area)
50	220	$5 \times 10^8$	227	15.23
100	220	$5 \times 10^8$	227	30.47
50	320	$10^8$	680	4.43
100	320	$10^8$	680	8.86

[0087] FIGS. 5A-5H illustrate an exemplary method for preparing the solar brush 10 including a metal substrate and bristles of CdTe and CdS. As shown in FIG. 5A, a substrate is prepared by sonicating with ultra pure water (e.g., 18 M $\Omega$ ) and ethanol in an alternating manner. For example, a metal substrate may be sonicated with ultra pure water for 10 minutes, then with pure ethanol for 10 minutes, and this water-ethanol cleaning cycle may be repeated two more times. If desired, the cycle may be performed more or less than three times and/or an initial detergent-water, acid-water cleaning, or NaOH/NaCN/detergent electropolish process could be added. The type and amount of cleaning that is appropriate will be a function of how clean the substrate 12 is to begin with and the type of material the substrate 12 is made of.

[0088] The substrate 12 may be a conductive material (metal) or a nonconductive material (e.g., glass or polymer) that is coated with a conductive layer.

[0089] FIG. 5B illustrates the substrate 12 coated with the first conductive layer 14 that serves as the p-layer conductor in the PV cell. The first conductive layer 14 may be any well-known conductive material deemed suitable by a person skilled in the art, including but not limited to gold, copper, nickel, molybdenum, iron, aluminum, doped silicon, and silver. In one embodiment, a 500-nm layer of gold is evaporated on a glass substrate at 0.2 Å/s using an electron beam evaporator at a pressure under  $5 \times 10^{-6}$  mbar at room temperature. In other embodiments, electroless plating is used with copper salts or  $\text{Na}_3\text{AuSO}_3$  dissolved in 50 mM  $\text{H}_2\text{SO}_4$ . After metalization of the substrate 12, the surface of the first conductive layer 14 is rinsed with ultra-pure water (e.g. for 1 minute), rinsed with ethanol, and dried with nitrogen.

[0090] FIG. 5C illustrates a template 30 that may be used to form the bristles 20. The template 30 may be a membrane or porous structure that can be constructed on a conductive base, or the template 30 may be any of the commercially available nano-porous or micro-porous membranes, such as, for example, those made by Whatman Corporation under the trade names Nucleopore®, Anodisc® or Black Cyclopore®. Track-etched membranes that have pore sizes in the

range of 10 nm-5  $\mu\text{m}$  are particularly useful. These track-etched membranes are typically made from polyethylene-terephthalate (PET) or polycarbonate (PC). Membranes may also be partially etched to deliver conical nanocables. Conical nanocables are thought to be significantly stronger than cylindrical nanocables but can be processed in a nearly identical way to the cylindrical nanocable. Selection of the membrane 30 depends on the particulars of the PV cell that is being fabricated. Different pin combinations have different thickness requirements and therefore different cable size requirements. Before electrochemical deposition, the membrane 30 is cleaned and air bubbles expelled from the pores by submerging the membrane 30 in methanol and sonicating for 5 minutes.

[0091] FIG. 5D illustrates the membrane 30 connected to the substrate 12 that has been coated with the first conductive layer 14. There are a number of ways to connect the membrane 30 to the conductive substrate 12 or the metallized nonconductive substrate 12. For example, a  $\text{TiO}_2$  solution can be used as a conductive glue to fix the membrane to the surfaces. Alternatively, the membrane can be fixed using a Radionics Silver Conductive Paint®. Some substrates require no adhesion. The membrane is simply placed on top of the substrate provided there is good surface contact. Alternatively, the membrane could be attached to the surface with a clamp, using ultrasonic welding, or by fitting the surface and the membrane into a jig.

[0092] FIG. 5E illustrates the deposition of metal 32 into the membrane 30 and on the substrate 12. In an illustrative plating process, a Sn sensitizer is applied to the membrane 30 through a 5 to 45-minute immersion in 0.26M  $\text{SnCl}_2$  and 0.07 M trifluoroacetic acid dissolved in a solvent having a molar ratio of 1:1 methanol to water. The membrane is rinsed with methanol. Sn adheres to the pore walls and outer surface of the membrane. Next, the membrane is immersed in an aqueous solution of 0.029 M ammoniacal  $\text{AgNO}_3$  for five minutes. This causes a redox reaction where  $\text{Sn}^{2+}$  is oxidized to  $\text{Sn}^{4+}$  and  $\text{Ag}^+$  is reduced to elemental Ag. Some silver oxide is also generated.

[0093] The pore walls and the membrane 30 become coated with discrete nanoscopic Ag particles. The membrane is rinsed with ethanol and immersed in water. Then the membrane is immersed in a 7.9 mM  $\text{Na}_3\text{Au}(\text{SO}_3)_2$ /0.127M  $\text{Na}_2\text{SO}_3$ /0.625 M formaldehyde solution that has a temperature of  $-0^\circ\text{C}$ . Gold plating is continued for 10 to 24 hours (time is dependant on pore size), at which time the nanocables are fully formed in the membrane.

[0094] An alternative way to deposit materials inside membranes entails using electrophoresis or centrifugation sol-gel methods, electrochemical atomic layer epitaxy, chemical vapor deposition, sputtering, E-beam evaporation, thermal evaporation, electron beam lithography, and scanned probe lithography. Alternatively, well known additives can be dissolved in the solution to impart nanocable strength or better electrical connections to the n-layer conductor. Preferably, metal covers all exposed areas of the membrane, substrate, and fills the pores. After the gold deposition, the membrane is soak with water and rinsed 4 times over a 3-hour period and immersed in 25% nitric acid for 12 hours to remove residual Sn or Ag. Finally, the membrane is rinsed with water and air dried. Evaporative metal deposition can also take place in the same manner as in FIG. 5B. One advantage of electroless or electro-deposition is that it does not require a clean room or high temperatures to deposit the metal on the substrate.

[0095] Alternatively, the membrane may be placed into the electroless plating solution by itself. The top, bottom, sides and pores become metallized. The membrane 30 may be glued as mentioned above to the metallized substrate 12.

[0096] If desired, atomic layer epitaxy may be used to build a protective cover over the membrane 30. Atomic layer epitaxy may be used as an alternative to electrochemical epitaxy.

[0097] FIG. 5F illustrates the removal of the membrane 30, leaving the conductive cables (nanowires) 32 attached to the substrate 12. Membrane removal is most commonly done by solvent extraction. Partial membrane removal is often desirable. Layered membranes make it easier to achieve uniform partial dissolution. Generally, dichloromethane is used to not only remove the membrane but also the outer layer of metal leaving, the nanocables. In some cases, the top conductive surface may be removed using ethanol to wipe out the excess after the membrane was coated but before it is immersed in 25% nitric acid. Tape can also be used to remove the outer metal, and may be used in conjunction with further cleaning steps. In some cases, it is possible to physically peel the membrane off.

[0098] Alternatively, the membrane itself can have the metal pre-deposited therein, or the metal can be deposited into the membrane prior to coupling with the substrate. The metal will fill the pores and coat the outer surfaces of the membrane. The membrane can then be glued with titanium dioxide or silver paste to the substrate prior to membrane dissolution.

[0099] As shown in FIG. 5F, the membrane 30 may be dissolved until adequate material is left to act as the insulating layer 16. Alternatively, the membrane may be completely dissolved and an insulating layer deposited using any suitable method such as spin coating, CVD, etc.

[0100] The insulating layer 16 may keep the current from the n-layer and p-layer from short circuiting. The insulator can also limit deposition of PV material to the nanocables. Because insulation eliminates the effects of defects of one cable from affecting its neighbors, processes like electroplating become feasible.

[0101] Electroplating is a desirable process because of low equipment costs and relatively good material conservation relative to other processes such as sputtering and CVD which deposit material throughout the chamber in addition to in the desired area. The thickness may easily be determined by using various exposure times to dichloromethane and verifying the membrane thickness with scanning electron microscopy.

[0102] If all of the membrane 30 is removed, excessive material is consumed. This process may be used if a thinner insulating material or a material other than the material the membrane 30 is made of is desired to form the insulating layer 16 in this case, the desired material may be spin-coated on the substrate 12 with polymethylmethacrylate (PMMA) to a thickness of about 1  $\mu\text{m}$ . The PMMA may function as a membrane glue and/or an insulator. Any insulating material that can be applied to the PV cell be it polymers, silicone dioxide, or any insulator that can have adequate dimensional control during application. The PC membrane may be placed on top of the PMMA and baked at around 100° C. for about an hour.

[0103] In some embodiments, the insulating layer is eliminated altogether. As long as the p and n layers are adequately produced, direct contact with the conducting layers is possible.

[0104] In other embodiments, holes are made in the insulating layer after attachment of the membrane. For example, reactive ion etching (RIE) with oxygen and/or wet etching may be used to drill through the insulating layer 16 to allow the nanocables 32 to connect with the first conductive layer 14.

[0105] In other embodiments, membrane can actually be used as a masking layer to etch pores in the underlayer, which is the insulating layer 15 in this case.

[0106] FIG. 5G illustrates the deposition of a p-type semiconductor layer 24. Where the p-type semiconductor is CdTe, for example, the electrochemical deposition is done using 50 mM  $\text{H}_2\text{SO}_4$ +1 mM  $\text{CdSO}_4$ +0.1 mM  $\text{TeO}_2$  solutions deoxygenated with nitrogen prior to use at room temperature. The reference electrode may be Ag/AgCl/3 M NaCl and the counter electrode may be a gold wire. Ultra-pure (e.g., 18 M $\Omega$ ) water rinses are performed between deposition steps with nitrogen drying. A thin layer of Te may be deposited to prevent Cd diffusion into the nanocable. When CdTe layer is deposited in an electrochemical cell from a solution of 0.5M  $\text{CdSO}_4$  and  $2.4 \times 10^{-4}$  M  $\text{TeO}_2$  in water at a pH of 1.6 at 90° C., the optimum deposition potential for a stoichiometric film is -400 mV versus the Pt reference electrode. CdTe is also known to be deposited in ammonia solutions. To deposition on the bristle surface can eliminate Cd diffusion into the core of the PV device. The CdTe layer also can be deposited by ECALE (electrochemical atomic layer epitaxy). ALD (atomic layer deposition in chemical vapor deposition system) or sol-gel. When non-electroplating processes such as CVD-related methods are used, etching can be used to remove the p-type layer 24 at the base of the structure to expose the insulation layer and create isolation between the nanostructures.

[0107] FIG. 5G also illustrates the deposition of an n-type semiconductor layer 26. Where the n-type semiconductor is CdS. CdS deposition is performed in 1.5 mM  $\text{SC}(\text{NH}_2)_2$ , 1.5 mM  $\text{CdSO}_4$ , and 2 mM  $\text{NH}_4\text{OH}$  heated to a temperature of about 40-70° C. Under these conditions, a 4.5 minute exposure would lead to a CdS layer of about 30 nm. The CdS layer also can be deposited by ECALE (electrochemical atomic layer epitaxy), ALD (atomic layer deposition in chemical vapor deposition system) or sol-gel. Again, when non-electroplating processes such as CVD-related methods are used, etching can be used to remove the p-type layer 24 at the base of the structure to expose the insulation layer and create isolation between the nanostructures.

[0108] FIG. 5H illustrates the deposition of the second conductive layer 18 that completes the PV circuit. The second conductive layer 18 may be added using atomic layer epitaxy. This deposition connects the second conductive layer to the base of the n-conductors without contacting the nanocable. Alternatively, a thin layer of electroless metal can be coated as in the process illustrated in FIG. 5B as long as the metal remains thin enough to maintain adequate transparency. Yet another alternative is to apply a transparent conductive polymer such as poly(3-hexylthiophene) (P3HT), poly[2-methoxy,5-(2-ethyl-hexyloxy)-p-phenylene-vinylene] (MEH-PPV), poly(phenylene vinylene) (PPV), and polyaniline to the outside of the nanocables to complete the circuit. The polymer could also provide additional structural support. Since the PV circuit is built on the nanoscale, penetration of the polymer may be challenging. For this reason, thinner solutions may be preferred. However, exposing the upper surface of the PV cell would provide adequate conduction.

Preferred polymers are light and oxygen stable. Many different conductive polymers are useful to make the electrical contacts and are described in T. A. Skotheim, *Handbook of Conductive Polymers* 1, which is incorporated by reference in its entirety.

**[0109]** Additionally, gel electrolytes may be used to make the electrical contact for the n-layer as shown in US200410025933, which is herein incorporated by reference. The electrolyte solution could be a combination of poly(4-vinylpyrimidine), poly(2-vinylpyrimidine), polyethylenedioxypolythianes, polyamides and a lithium salt. The salt could be lithium iodide, lithium bromide, lithium perchlorate, lithium thiocyanate, lithium trifluoromethyl sulfonate, and lithium hexafluorophosphate to name a few.

**[0110]** Although FIGS. 5A through 5H illustrate an exemplary method of fabricating the solar brush 10, there are many suitable variations of the process. For example, an organic photovoltaic material could be used. For example, PV cells could be made based on Dr. Michael Grätzel and co-worker's technology developed at the Swiss Federal Institute of Technology. The metallic core of the nanobristle could be made of metal oxides based on Ti, Zr, Sn, W, Nb, Ta, and Tb. The cables can then be coated with an organic dye such as xanthenes, cyanines, merocyanines, and phthalocyanines, and pyrroles. Many of these compounds have been tested by Konarka Corporation and have been developed for low temperature sintering as illustrated in patent application US 2004/00259934. Fortunately, the nanocable eliminates the sintering concerns and allows the organic compound to be easily applied and efficiently used on the surface of the nanobrush.

**[0111]** Also, any membrane with micropores can be applied to the substrate 12 to produce the PV brush. Also, any metal deposition should work with nanopores be it chemical vapor deposition, plasma vapor deposition, metal organic vapor deposition, electrochemical deposition (electrochemical epitaxy, under-potential deposition), liquid phase epitaxy, molecular beam epitaxy, hot wall epitaxy, sputtering, E-beam and thermal evaporation, electroless deposition, chemical bath deposition, sol gel and solution methods, vapor-liquid solid methods, sonochemistry methods, and microwave methods.

**[0112]** Nanoporous structures of certain metal oxides can be obtained with the metal anodization process instead of, or as a variation of, the method illustrated in FIGS. 5A-5H. Among other systems, Al, Ti, and transparent conductive oxides can be anodically oxidized to form a regular nanopore structure.

**[0113]** In one experiment, tin oxide was anodized. Before electro deposition, a thin Au film was sputtered on one side of the aluminum anodically oxidized (AAO) membrane to serve as the conductive layer. Electro deposition of Sn into the pores of the AAO membrane was carried out at a constant current density of 0.75 mA/cm<sup>2</sup> for 1 hour in electrolyte containing sodium tricitrate of 25 and tin dichloride of 7 g/L. The Sn embedded in the AAO membrane was anodized at 0 V in 0.2 M boric acid, whose pH value was adjusted to 8.6 by 0.5 M NaOH(aq). The anodization proceeded until the current density dropped to almost zero. The AAO membrane was then removed through wet etching with 0.5 M. NaOH(aq), leaving behind an array of nanoporous tin oxide nanorods. Finally, the samples were calcinated at 500° C. for 3 hours in air.

**[0114]** One embodiment of the present invention provides a method of forming nanoporous nanostructures. Using surface

alloying, the nanostructure surface may be changed from smooth to rough. Nanoporous or nano-rough nanostructure obtained by this method is more robust because the nanoporous layer is confined to a few monolayers in the surface (not the entire bristle volume) and still maintains a plain metallic core.

**[0115]** To create a nanostructure with a rough outer surface, in one example, a thin film of (e.g., a monolayer of) Cd can be deposited on the surface of the gold bristles mentioned above. Electrochemical deposition of Cd is performed in the under-potential region (at potentials more positive than the Cd bulk deposition potential) for a few minutes and then the Cd layer is removed by switching the potential to more positive values. Stripping the Cd layer leaves a rough Au surface behind. After exposure to corrosive materials, the Cd dissolves, leaving a ragged but sturdy nanocable with a solid core. Electrochemical deposition of Cd is performed in the under-potential region (at potentials more positive than the Cd bulk deposition potential) for a few minutes and then the Cd layer is removed by switching the potential to more positive values. As before, stripping the Cd layer leaves a rough Au surface behind. The method can be applied to any alloy system that shows alloying at the interface (e.g. Pt-Me (Me=Ag, Pb, Sn, Hg); Au-Me (Me=Ag, Cu, Cd, Pb, Pd, Al, Hg, Sn), Ag-Me (Me=Cd, Ph)).

**[0116]** In this example, Au nanocables array is the working electrode and the Electrochemical Adsorption-Desorption (ECAD) process is applied to the Au—Cd system. Electrochemical deposition of Cd is carried out at room temperature in 50 mM H<sub>2</sub>SO<sub>4</sub>+1 mM CdSO<sub>4</sub> solution, deoxygenated with nitrogen prior to use. All potentials reported here are relative to normal hydrogen electrode (NHE). The electrochemical deposition of Cd was performed at various potentials in the under potential deposition (UPD) region, i.e. from -0.3 to -0.49 V for various times. During deposition, the morphology of the surface does not change. Thus the formation of the alloy will not deform the bristles and will not affect the nanostructure array. Stripping the cadmium layer by changing the potential to 0.65 V produces to a rough surface. Depending on the deposition conditions (in this case, potential and time), we can control the penetration depth of the alloy.

**[0117]** Tailored nanoporous metal nanostructures arrays created by the proposed method may also be suitable for sensor applications, particularly in a biomaterials context, catalyst and electrodes. The nanocable would be coated using the above-mentioned methods but would have at least 10× the surface area of the initial cables. Alternatively, smooth nanocable surfaces can be obtained by electrochemical annealing. This method has several advantages. First, the electrochemical deposition of the subsequent layers (CdTe) can be performed in the same electrochemical cell; this eliminates the contamination during handling the sample (especially for nanostructures with huge surface area, contamination is the most important problem).

**[0118]** Second, the nano-rough nanostructure is more robust because the nanoporous structure is confined to a few monolayers in the surface (not the entire bristle volume) and still maintains a plain metallic core.

**[0119]** Third, the roughness of the surface can be controlled within a few nanometers (which is in fact the surface layer for a cylinder with a diameter of 100 nm) by varying the deposition conditions so the cable can be made rough while maintaining structural integrity.

[0120] Fourth, upon subsequent conformal deposit layers of CdTe and CdS, and finally TCO or polymer, the porous structure is further strengthened. The final advantage is that multiple reflections (the light is trapped into the nano-size cavity of a bristle and will have several reflection before it is reflected into the neighbor bristle) increases the efficiency of light absorption.

[0121] The Cd/Au method is particularly robust.

[0122] The bristles 20 may be shaped to increase the surface area. For example, the bristles 20 may have “branches” or holes in the nanocable. Holes may be created by depositing the Cd/Au alloy as just described and anodizing.

[0123] As another alternative to the method of FIGS. 5A-5H, the solar brush 10 in be prepared using a reverse-nanocable fabrication method that is illustrated in FIGS. 6A-6F, the nanocable is formed as one of the last steps in the reverse-fabrication method, the nanocables are prepared after one of the conductive layers, unlike in the method of FIGS. 5A-5H. Titanium isopropoxide may be used as the precursor molecule for the sol-gel preparation of the TiO<sub>2</sub> nanostructures.

[0124] FIG. 6A illustrates a template membrane 60 that may be used for the reverse-fabrication method. The template membrane 60 has a substrate 62 (e.g., soda lime glass). The template membrane 60 is obtained by coating the substrate 62 with a high-purity aluminum layer. A transparent oxide layer is formed using a two-step anodization process: depositing on the aluminum layer, and forming porous aluminum layer columns 67. Substantially all of the high-purity aluminum layer that is coated on the substrate is oxidized to form the aluminum layer columns 67. The area between the porous aluminum columns 67 are pores 64 of the template membrane 60. A conductive or semiconductive layer 66 is deposited on the substrate 62.

[0125] FIG. 6B illustrates the sol gel deposition of transparent conductive oxide in the pores. Sol gel deposition can be performed in either attached or detached porous membrane. First, titanium isopropoxide is dissolved in 95% ethanol to yield a concentration of typically 20 v/v %. A second solution is then prepared by mixing 25 mL of ethanol with 0.5 mL of water and 0.5 mL of 0.1 M HCl. Equal volumes of the titanium isopropoxide solution and the second solution are then combined to yield the TiO<sub>2</sub> sol. The template membrane 60 is then dipped into this sol for the desired amount of time, removed, and allowed to dry in air for 30 min. The sol-containing membrane is then heated in air at 400° C. for 24 hours. This procedure yields TiO<sub>2</sub> tubules or fibrils within the pores and TiO<sub>2</sub> films on both faces of the template membrane. Electrophoresis and centrifugation can be applied in order to obtain a denser nanostructure. A transparent conductive oxide pillar 68 is formed of any conductive oxide material or, for example, Ti with a conductive oxide material formed thereon by plasma vapor deposition PVD.

[0126] FIG. 6C illustrates the removal of the porous aluminum columns 67 of the template membrane 60. The surface films can be removed by polishing the template membrane 60 with 1500 grit sand paper or nanoscale milling using ultrashort laser pulses or other wafer fabrication milling techniques. If desired, the alumina template membrane 60 can then be dissolved away by immersion in aqueous base to expose the synthesized TiO<sub>2</sub> pillars 68.

[0127] FIG. 6D illustrates the deposition of a n-type semiconductor layer 70 over the pillars 68. The n-type semiconductor layer 70 may contain CdS, which may be electro-

chemically deposited. CdS is also deposited on the conductive oxide layer 66 between the pillars 68.

[0128] FIG. 6E illustrates the deposition of an p-type semiconductor layer 72 over the p-type semiconductor layer 70. The p-type semiconductor layer 72 may contain CdTe, which may be electrochemically deposited. CdTe may also be deposited in the areas between the CdS layers.

[0129] FIG. 6F illustrates an electroless deposition of a metal contact 74. The metal contact 74 fills the gaps between the CdTe layers, thereby forming the nanocable, which in actuality may appear as a lattice. This process may be especially beneficial for formation of Au and Cu nanocables at extremely high aspect ratios.

[0130] To create an insulating layer in between nanocables, the top metal contact 74 can be removed to expose the composite surface made of conductive nanowire and p layer. Then, the p layer and n layer can be selectively etched leaving the conductive nanowires sticking out the top surface. Then, an insulating layer can be applied and the surface is then polished to result in a composite surface made of conductive nanowires and insulating layer. Then, a conductive layer is applied which will create the contact with the nanowires.

[0131] If the aspect ratio is too great, softer nanocables may collapse under their own weight. Tall Au or Cu nanocables may be fragile, and their strength may be increased by alloying Au with other metals or by reinforcing the gold with it hard metal coating. FIGS. 7A-7H illustrate a hard-metal reinforcement process that may be used to strengthen the fragile nanocables. Any hard metal (such as nickel or molybdenum) that works at high temperatures may be used for the metal reinforcement. However, in the interest of clarity, FIGS. 7A-7H will be described using nickel as the reinforcement metal.

[0132] As shown in FIG. 7A, the metal reinforcement process begins by providing a substrate 80. The substrate 80 is metallized by deposition of a first conductive layer 82, as shown in FIG. 7B. Preferably, the substrate 80 is of the same base metal as the nanocable core to facilitate physically strong junctions at the base of the cable as well as to provide good conductivity. The first conductive layer 82 may have a height of h, as shown. The first conductive layer 82 may be, for example, 99.99% pure aluminum. Then, as shown in FIG. 7C, the first conductive layer is oxidized under controlled temperature and voltage to form a metal oxide porous membrane 84 of a preselected shape having a pore 85. Since not much growth occurs during oxidation, the height of the metal oxide porous membrane 84 remains at approximately h. If desired, an appropriate commercial membrane may be used instead of forming, the metal oxide membrane 84.

[0133] FIG. 7D shows that the metal oxide porous membrane 84 template is exposed to ethylene in an argon environment at a temperature of about 650-750° C. to form carbon nanotubes 86. The exposure may be done by placing the “template” made of the substrate 80 and the membrane 84 in a heated chamber and adding ethylene to the chamber. Initially, the carbon layer is formed on top of the metal oxide membrane 84 as well as on the base and the inner walls of the pore 85. The carbon layer is, however, removed from the top of the metal oxide membrane 84 and the base of the pore 85, leaving carbon nanotube 86 as shown in FIG. 7D. Etching (RIE or wet etching) or polishing may be used to remove select portions of the carbon layer. The carbon nanotubes 86 are formed along the inner sidewalls of the metal oxide membrane 84.

[0134] FIG. 7E shows nickel deposition, which may be done electrolytically plating the nickel on the carbon nanotube **86**. After deposition, the nickel is heated in air to 400° C. to form a nickel oxide nanotube **86**. The nickel nanotube **8** is then be filled with a core metal **90**, such as gold or copper, as shown in FIG. 7F. As shown in FIG. 7G, the template and the nanotubes are exposed to air at 600° C. to burn away the carbon nano tube. As shown, removal of the carbon nanotube **86** leaves a gap **87** separating the nickel-enforced core metal **90** from the metal oxide membrane **84**. In FIG. 7H, the metal oxide membrane **84** (in this case aluminum oxide) is removed, for example by being dissolved in sodium hydroxide. A nickel enforced nanocable **88+90** remains.

[0135] If desired, the gap **87** (see FIG. 70) may be filled with a photovoltaic polymer (e.g., an organic polymer) or titanium dioxide. The size of the gap **87**, which is determined by the size of the carbon nanotube **85**, controls the diameter of the nanocable that ultimately forms. Especially if the gap **87** will be filled with a material that forms the outermost wall of the nanocable, the size of the gap **87** is important in that it determines the thickness of the outermost wall.

[0136] FIG. 8 is a flowchart summarizing the steps of the hard-metal reinforcement process **92** that is schematically illustrated in FIGS. 7A-7H. First, the substrate is metallized (step **94**) by deposition of a conductive layer. A membrane with anodized pores is formed on the metallized substrate (step **95**), and carbon nanotubes are formed on the membrane (step **98**). A hard metal such as nickel is electroplated on the carbon nanotubes (step **100**) so as to form a cavity in the core. The core is then filled with a core metal such as gold or copper (step **102**) to form the nanocable. The carbon nanotube is then removed (step **104**) and the template is dissolved (step **106**) to leave the metal enforced nanocable.

[0137] If the core metal is gold, which may be too soft, copper nanotube may be formed on the nickel nanotube or instead of nickel nanotube to form a reinforcement for the gold nanocable.

[0138] Nanocables may also be strengthened by a more precise control of their dimension, as shown in FIGS. 9A-9H. As in the metal reinforcement process of FIGS. 7A-7H, a substrate **100** is provided (FIG. 9A) and metallized (FIG. 9B) by adding a conductive layer **102**, which may be 99.99% pure aluminum. An oxide template **104** having a pore **105** is produced (FIG. 9C) by oxidizing the aluminum under controlled conditions. Substantially all of the conductive layer **102** turns into the oxide template **104**. Then, as shown in FIG. 9I, a first polymer is deposited on the surface and cured to form a first polymer thin film **106**. The polymer deposition (e.g., by inkjet coating) and curing may be performed any number of times to form as many polymer thin films as desired. In FIG. 9E, a second layer of polymer is deposited and cured to form a second polymer thin film **108**.

[0139] As shown in FIG. 9F, the polymer thin films **106**, **108** on the top of the membrane and the base of the pore are removed. Next, electroless or electrochemical deposition of metal can take place inside of the pore **105** (FIG. 9G). A solvent extraction removes the polymer thin layers **106**, **108** and a NaOH extraction removes the metal oxide template **104** to leave a precisely designed nanocable (FIG. 9H). Poly fillers such as poly(3-hexylthiophene) (P3HT), poly [2-methoxy, 5-(2-ethyl-hexyloxy)-p-phenylene-vinylene] (MEH-PPV); poly (phenylene vinylene) (PPV), and polyaniline may be used as the polymer thin layers **106**, **108**. Additionally, any suitable polymer from the *Handbook of Conductive Polymers*

1 I by T. A. Skotheim or the *Handbook of Polymer Coatings for Electronics Chemistry* by J. J. Licari and L. A. Hughes may be used.

[0140] FIG. 10 is a flowchart summarizing the steps of the metal reinforcement process illustrated in FIGS. 9A-9I. First, the substrate is metallized (step **112**) by deposition of a conductive material on its surface. A membrane (a template) is formed on the metallized substrate, and the membrane has a pore of the desired size and shape that is anodized (step **114**). One or more polymer layers are deposited and cured (step **116**). The polymer layers on horizontal surfaces, such as the top of the template and the base of the pore, are removed by reactive ion etching (step **118**). The pore is filled, with a core metal such as gold or copper (step **120**). Then, the polymer layers are dissolved (step **122**) and the membrane is removed by solvent extraction (step **124**).

[0141] FIGS. 11A-11I illustrate that organic nanocables may be produced using a carbon jacket process. FIG. 11A shows a substrate **130** with a conductive layer **132** deposited on its surface. In the same manner as described above in reference to FIG. 7C, a metal oxide membrane **134** is formed, wherein the membrane has a pore **135** of the desired shape and size (FIG. 11B). As shown in FIG. 11C, carbon nanotubes **136** are formed on the inner wall of the pore **135** in a manner similar to what is described above in reference to FIG. 7D, and titanium dioxide **138** is used to fill the remaining core portion of the pore **135** (FIG. 11D). As shown, titanium dioxide **138** fills the pore **135** and “overflows” to form a cap **139** above the carbon nanotube **136** and the metal oxide membrane **134**. The cap **139** of the titanium dioxide **138** is removed in FIG. 11E, for example by any suitable etching method such as concentrated H<sub>2</sub>SO<sub>4</sub> etch.

[0142] With the titanium dioxide cap **139** removed, the carbon nanotube **136** is burned off by exposure to air at about 600° C. (FIG. 11F), forming a gap **140** between the titanium dioxide core **138** and the metal oxide membrane **134**. An organic Grätzel dye **142** is then deposited to fill the gap **140** and form an organic cap **141** (FIG. 11G). Polymers such as poly(3-hexylthiophene) (P3HT), poly[2-methoxy-5-(2-ethyl-hexyloxy)-p-phenylene-vinylene] (MEH-PPV), poly (phenylene vinylene) (PPV), and polyaniline, or any other suitable polymers from *Handbook of Conductive Polymers* 1 may be used with the Grätzel dye **142** as conductors. The organic Grätzel dye **142** can be sprayed with an inkjet printer, applied with a rotogravure process, sprayed on and wiped with a doctor blade, etc. The most suitable applications leave the minimum organic cap **141**. Following this preliminary removal, the organic cap **141** may be etched (e.g., with RIE) or removed with a solvent wash to complete the removal process (FIG. 11H). Then, the metal oxide membrane **134** is removed, for example by wet etching with a carefully selected etchant (NaOH) that preferentially removes the metal oxide over titanium dioxide (FIG. 11I), in some embodiments, the metal oxide membrane **134** is made to be transparent so that its removal is not needed.

[0143] FIG. 12 is a flowchart summarizing the carbon jacket process illustrated in 11A-11I. As shown, a substrate surface is metallized if needed (step **152**). Anodized pores are formed by preparation of the metal oxide membrane on the substrate (step **154**). Then, carbon nanotubes are formed on the walls of the anodized pores (step **156**), and titanium dioxide is deposited in the remaining core portion of the pores (step **158**). Any titanium dioxide that flowed over and formed a titanium dioxide cap is removed by etching (step **160**),

exposing the carbon nanotubes. The carbon nanotubes are burned away (step 162), leaving a gap between the titanium dioxide and the metal oxide membrane. An organic polymer is deposited to fill this gap (step 164), and any organic polymer that overflows and forms a cap is removed, for example by wet or dry etching (step 166). The metal oxide membrane is then dissolved with NaOH (step 168). The resulting nanocable has a titanium dioxide core reinforced with an organic polymer. This nanocable may be coated with a conductive polymer (step 170) that serves as the n-type conductor.

[0144] FIG. 13A shows an insulator 181 deposited on a conductor 180. The insulator can be any insulating material such as metal oxide, silicone dioxide, or polymer. The insulator can be applied through CVD or sputtered. It can also be anodically grown from metal deposited on the conductor 181. FIG. 13B shows how the insulator can be wet or dry etched to form a pore that continues through to the conductor 180. FIG. 13C shows that the conductor can then be deposited through electroplating or the electroless deposition methods contained herein. FIG. 13D shows the conductor partially etched. In the case of silicone dioxide, a KOH solution can be used for etching, in the case of polymers, dichloromethane may be used. FIG. 13E shows the completed nanocable with the p-conductor 180, the insulator 181, the p-layer 182, the n-layer 183, and the TCO 184.

[0145] FIG. 14 shows the solar brush 10 encapsulated in an optical casing 40 for protection from various environmental elements. The optical casing 40 may also be made of a transparent polymer. The optical casing 40 may be made of film conductive oxides that allow light in but minimize soil and moisture contamination. Common materials for this application include Sn doped  $\text{Sn}_2\text{O}_3$ , Sn doped  $\text{In}_2\text{O}_3$ ,  $\text{ZnSnO}_3$ , B doped  $\text{ZnO}$ , F doped  $\text{In}_2\text{O}_3$ , F doped  $\text{SnO}_2$ , F doped  $\text{Cd}_2\text{SnO}_4$ , F doped  $\text{ZnO}$ ,  $\text{TiN}$ , Ag,  $\text{Cd}_2\text{SnO}_4$ , Ge doped  $\text{In}_2\text{O}_3$ , Ge doped  $\text{SnO}_2$ , Ge doped  $\text{Zn}_2\text{SnO}_4$ ,  $\text{ZnO/CdS}$ ,  $\text{ZnO/ZnSe}$  (for CdTE and  $\text{CuInSe}_2$ ),  $\text{ZnO/In}_x\text{Se}_y$  (for CdTE and  $\text{CuInSe}_2$ ),  $\text{SnO}_2$  glass,  $\text{ZnO/CdS}$ ,  $\text{Cd}_2\text{SnO}_4/\text{Zn}_2\text{SnO}_4$ ,  $\text{Zn}(\text{Se}, \text{OH})$ ,  $\text{Zn}(\text{Se}, \text{O})$ , and  $\text{Cd}_2\text{SnO}_4/\text{Zn}_2\text{SnO}_4$ . For additional conductivity small wires or metal strips or thin bands of metalized glass can be added for additional conductivity. Metal should be minimized to reduce reflection but be sufficient for adequate conduction of electricity. Traditional casing of low-iron glass and extruded aluminum may also be used as in the case of creating a module of a form compatible with existing PV modules.

[0146] In some embodiments, a back contact is employed. The back contact could be especially useful for hi-layered photovoltaic cells to reflect the photons from a high band gap materials at the top of the cell to low band gap materials at the bottom of the cell. The reflection still allows light have single passes through the PV material from reflections. Where a reflective back contact is used, the optical reflector may include  $\text{Sb}_2\text{Te}_3$ , Ni, NiTe, and Te/CdTe.

[0147] Soil build up might eventually reduce the effectiveness of the solar cell. Fluorosilicone polymers would greatly reduce soil adhesion and allow maximum cleaning during rain storms. Additionally, any thermally stable material that exhibits the Lotus effect (low surface area for particulate adhesion) may be used to keep dust off the PV cell.

[0148] To aid the maintenance of the solar cells, an LED or liquid crystal display could indicate power output of solar cell, or operations of PV cell units so that malfunctioning cells could be easily identified and replaced. As mentioned above, there are numerous advantages of the solar brush 10

over conventional PV cells. The solar brush 10 demonstrates a high thermal stability. Unlike nanoparticles, where the linear thermal expansion coefficient increases with the reduction of the average grain size (Cu, for instance). Cu nano-wires show a smaller thermal expansion coefficient than that of the bulk Cu. The high thermal stability is related to the grain boundary structure and high aspect ratio of the nanostructure. Daisy chain connections may also combat potential thermal expansion/contraction issues by minimizing chip size and then connecting them opposed to having a large sheet that would have a higher potential for stress cracking due to thermal expansion contraction. Daisy chains between cells could also add flexibility to a PV brush array. To accomplish this, the cells may have special interlocking mechanism to serve the dual purpose of being a robust carrier of the film during processing and to speed assembly.

[0149] Because the method describes growth of conductors on a conductive sheet, the failure rate that plagues current PV cell manufacturing will be greatly improved giving further cost/efficiency advantages.

[0150] A further advantage of the PV brush is that the distance electrons diffuse through the semiconductive layer to the conductive layer is shorter than that of conventional PV cells thereby reducing internal resistance of the PV cells to deliver further power generation efficiencies. Because the PV bristles are thin, they use a small fraction of the material required for planar cells. A variety of organic and inorganic semiconductors can be applied to the conductive core and thicknesses can easily be optimized for power generation and stability.

[0151] Besides solar panels, nanoelectronic assemblies can also be used for light generation in optical chips. Optical chips are widely thought to be the replacement for semiconductor chips. Optical chips have narrow pathways that light can travel unhindered while semiconductor chips are limited by electric field effects between one circuit and the next. A micro light source with unique color attributes could be essential for optical chip technologies. The nanoelectric assemblies can also be used as a nanolight source for such chips. Additionally, the nanodiodes can be used in a flat screen display for an ultra sharp video monitor. Additionally, the nanodiodes can be used for very energy efficient lighting.

[0152] The PV brush has flexible manufacturing options including membrane manufacturing technologies or photolithography e-beam, low density layered mechanical scoring, nanoporous templated, electroplating, and electrical arcing. These manufacturing methods can be used on a variety of membrane/nanoporous media which allows cell to be shaped and hardened to geometry that has maximum solar efficiency, maximum aerodynamic efficiency, maximum aesthetic appeal or a combination of the aforementioned attributes. Flexible units can also be achieved by daisy chain connection between small rigid units or from the use of a flexible substrate. At high temperatures, uneven thermal expansion can cause cracking and wear as well. High temperature degradation is mitigated because each component of this PV cell can be sized to minimize thermal expansion and can be further optimized with flexible expansion joint conductive connections between PV arrays. Additionally, the greater surface area of the solar brush will reduce thermal heat generated under the PV solar cell compared to the conventional flat unit which could greatly reduce unwanted heat buildup. One further advantage is that micro conductors often have reduced resistance at higher temperatures; therefore, the PV brush

could be able to transfer energy more effectively than conventional PV cells at higher operating temperatures.

**[0153]** Finally, the geometry can be used to trap or release heat. If heat were found to be detrimental to energy above a certain point, the unit could be designed with vents. However, it should be noted that performance of nanocables may be different than large scale wires. While large scale wires/cables have higher resistance to electrical flow at high temperatures, energy flow may improve due to improved flow through grain boundaries in nano-scale structures.

**[0154]** Power generation is a function of average power per day. The median sun hours for various cities in California is 6.18 kWh/(day\*m) according to a Go Solar® Company web page at [www.solarexpert.com/Pvinsolation.html](http://www.solarexpert.com/Pvinsolation.html). On average, solar energy is drawn from about 6 hours per day based on the data made publicly available by National Renewable Energy laboratory findings. The distribution is commonly given as a Gaussian curve, which has the following distribution:

$$f(x) = \frac{e^{\left(\frac{-(x-\mu)^2}{2\sigma^2}\right)}}{\sigma\sqrt{2\pi}}$$

Assuming an average of  $\mu=6$  hours, a standard deviation of  $\sigma=1$  hour, and integrated power of 6.18 kWh/m<sup>2</sup> for an average day gives a maximum energy. When  $x=\mu$ , the theoretical maximum power generated is about 4.933 kWh/m<sup>2</sup>. Based on EU studies of layering, the importance of having each solar event near the p-n junction, and reduced hot spots, the CdTe system may approach its theoretical efficiency limit. Efficiency could get as high as 30% with the single layer systems and potentially higher if we combined a high and low band gap system (discussed earlier). The distributions are shown in FIG. 15.

**[0155]** The power calculation works out as follows

$$P=6.18 \text{ kWh}/(\text{m}^2 \times d)$$

from the mean values for a California city

$$P_{\text{Brush}} = P \times E \times O$$

Thus, where  $E=29$  (29% efficiency) for a CdTe/CdS PV cell and  $O=1.44$  (44% gain),  $P_{\text{Brush}}=2.60$  kWhr/(m<sup>2</sup>×d) (average day in the mean city in CA). However, it should be noted that the brush can pick up about a 44.8% gain in efficiency by because it would require little if any sun orientation adjustments. The orientation of the solar brush **10** may have a large effect on performance. Planar PV modules lose up to 44% power from poor orientation and often need to be reoriented using a “solar compass”. Due to its unique design, the solar brush **10** does not require reorientation.

**[0156]** If electrical current through the PV device is sufficiently high, a cooling systems that could either be used to generated thermoelectric power (i.e. steam turbine type of power generation) or water beating systems for home use could also be possible.

**[0157]** A majority of the light from the sun is scattered from the atmosphere. Collecting scattered light using the solar brush **10** should lead to even higher energy production. Further energy gains from multi-junction solar cells may hump the efficiency to double what is believed to be currently possible.

**[0158]** The solar brush **10** will probably approach the theoretical maximum efficiency for a given material. Because the brush can be made nearly transparent, most of the light continues to travel through the cell. For practical purposes, the brush would appear to be of  $\infty$  thickness. Because the bristles can be designed just thick enough for stable solar absorption, each absorptive event would happen near the p-n junction. The occurrence of the absorptive event near the p-n junction improves cell efficiency. Another key to improving cell efficiency is to reduce localized heating. Each time there is solar absorption, part of the energy ejects the electron and part of the energy heats the cell. The heating reduces the efficiency of the cell. When cells rely on back reflection, they are also doubling the heat load for a given areas. As the sun moves across the sky, the penetration angle is changing and the trajectory of the solar stream is changing so there is a greater quantity of “fresh” material for the photons to impact. With the solar brush **10**, more of the absorption events can be made to occur near the p-n junction through control of the layer thicknesses, and the light stream will pass through greater amounts of PV material. Multiple junction material is believed to be the key to maximum efficiency in the future. Table 5 shows efficiency potential, band gap, and field efficiencies for several materials.

TABLE 5

Efficiencies of photovoltaic material				
Material	Theoretical Maximum Efficiency (%)	Laboratory Maximum Efficiency (%)	Field Efficiency (%)	Band Gap (eV)
Single Crystal Si	27	23.5	14.0-17.0	1.1
Si HIT single crystal	27	21.0	15.5-16.5	1.1
Si Poly Crystal	27	20.0	11.5-14.2	1.1
Si Ribbon	27	17	11.0-13.0	1.1
CIS	24	18	9.0-11.5	0.9
GaAs	30			1.4
CdTe	29	17	8.0-10.0	1.5
Amorphous Si	25	13.0	5.0-9.5	1.7
Indium Gallium Nitride	31*	17	8.5-11.05	0.8
Graetzel	20*	10.9	45	
Polymer	9*	4.9	1.0-2.5	

\*indicates that the value is an estimate.

Efficiency compares favorably with current technologies to give the maximum power increases. Table 6 shows the potential energy efficiency and power generation capability in the state of California.

TABLE 6

Potential energy efficiency and power generation in California		
Material	Efficiency	kW*hr/Day/m <sup>2</sup>
PV Brush (CdTe)	~29	2.60
Single Crystal Si	17	1.19
Polymer	2.0	0.11

**[0159]** Power generation and effective areas for the brush can be significantly boosted through the use of a solar concentrator. A solar concentrator could redirect large areas of light perpendicular to the surface, thereby utilizing the surface area at the depths of the brush. Only light angles close to 90 can penetrate a high area shell. The penetration depth in

shown by FIG. 1 is the spacing distance between bristles times  $\tan \Theta$ . As  $\Theta$  approaches  $90^\circ$ ,  $\tan \Theta$  approaches  $\infty$  and the required penetration level is achieved. The effective area of the solar cell is calculated by adding the penetration depth by the bristle height and multiplying it by the area. The power output of a high efficiency, high area solar cell in one embodiment is between 50 and 285 kW/day/m<sup>2</sup> with a solar concentrator. The output ranges compare favorably with the maximum output of 0.94 kW/day/m<sup>2</sup> based on the best known field results ever for single silicon PV arrays that are produced with a process which is probably much more costly than the methods and structures presented herein.

**[0160]** Konarka uses a technology where printed polymers generate energy from all visible spectra. As described in <http://www.konarkatech.com/about/>, PV polymers are printed on polymer sheets. Materials are produced by injecting a dye into titanium dioxide and printing the material on to polymers. The Konarka technology is expected to yield 10% efficiency and last about 8 years. In comparison, the materials disclosed herein that are used for the solar brush 10 have a lifespan in the 25 to 30 year time frame. Konarka's process may be IOU times less expensive than the solar brush 10 but produces PV cells of only around 2% efficiency. Furthermore, these PV cells would not have a form that is compatible with concentrators. Therefore, the maximum power Konarka's PV film would expect to generate on a given day would be about 0.11 kW/m, and the brush could generate between 450 and 2,500 times the power that the Konarka system generates.

**[0161]** Table 7 illustrates the power generation for 8" disk PV cells. Reference is made to Table 3, above, for definitions of column headings.

TABLE 7

High efficiency solar cell power generation for 8" disk PV cells.				
Bristle Height ( $\mu\text{m}$ )	Bristle Diameter (nm)	Cable Density (#/m <sup>2</sup> )	Area (m <sup>2</sup> /m <sup>2</sup> planar)	Estimated High Efficiency Power Generation (kWhr/m <sup>2</sup> *day)
50	220	$5 \times 10^{12}$	172.76	48.06
100	220	$5 \times 10^{12}$	345.52	96.12
50	370	$1 \times 10^{12}$	50.26	13.98
100	370	$1 \times 10^{12}$	100.54	27.96

**[0162]** Solar brushes 10 may be made from disks of 11" diameter, or can be grown from any dimension films using oxide templates. They can use existing photolithography and sputtering machines. If an 8" diameter disk is used, it would generate the power equivalent of 0.97 to 5.58 m<sup>2</sup> planar photovoltaic cells. If a perfect reflector were used in the solar collector, the minimum dish size would range from a diameter of 1.1 m to 14.8 in for full utilization of the PV cell area. Because perfect reflectors do not exist, some of the energy would be lost to absorption and misdirected reflections, A 2 to 2.5 in diameter may be used to generate the maximum energy. Smaller units can be produced if desired, the size being a function of the power requirements and the installation location. The 8" disk could generate 1.6 to 24.42 kW/day depending on the final area and thickness of material on a disk. The system is also preferably sized to allow proper current conduction without undue system heating of the substrate metal.

**[0163]** The small disk size will allow easy cleaning and reduce efficiency losses over time. Since the area of the cen-

tral disk is so small, it may be designed to snap in and out to be cleaned in a way that is impractical for larger cells.

**[0164]** The wide range of methods to form nanocables on either flexible or rigid substrate that is shaped to a given specification then hardened impacts the efficiency of the film.

**[0165]** Hard coatings such as TiN, ZrN, or HfN that have melting points around 3,000° C. may be used for certain layers to minimize reflectance or as a reinforcement "jacket" to increase the hardness of the nanocables.

**[0166]** While various embodiments have been described above, it should be understood that they have been presented by way of example only, and not limitation. Thus, the breadth and scope of a preferred embodiment should not be limited by any of the above-described exemplary embodiments, but should be defined only in accordance with the following claims and their equivalents.

1-36. (canceled)

37. A photovoltaic structure, comprising:

an array of photovoltaic nanostructures; and

a photovoltaic device, the photovoltaic device being at least semi-transparent,

wherein the array is positioned relative to the photovoltaic device such that light passing through the photovoltaic device strikes the array.

38. The photovoltaic structure as recited in claim 37, wherein the array of photovoltaic nanostructures is arranged in a brush configuration.

39. The photovoltaic structure as recited in claim 37, wherein axes of the photovoltaic nanostructures are tilted from a direction normal to a plane of the array.

40. The photovoltaic structure as recited in claim 37, wherein the photovoltaic device is formed of planar photovoltaic layers.

41. The photovoltaic structure as recited in claim 37, wherein the photovoltaic device is a second array of photovoltaic nanostructures.

42. The photovoltaic structure as recited in claim 41, wherein the first and second arrays of photovoltaic nanostructures are arranged in a brush configuration, wherein a height of the photovoltaic nanostructures in the first array is different than an average height of the photovoltaic nanostructures in the second array.

43. The photovoltaic structure as recited in claim 41, wherein the photovoltaic nanostructures of the first array have the same composition as the photovoltaic nanostructures of the second array.

44. The photovoltaic structure as recited in claim 41, wherein the photovoltaic nanostructures of the first array have a different composition than the photovoltaic nanostructures of the second array.

45. The photovoltaic structure as recited in claim 44, wherein the photovoltaic nanostructures of the first array comprise an organic material, wherein the photovoltaic nanostructures of the second array comprise inorganic materials.

46. The photovoltaic structure as recited in claim 44, wherein the photovoltaic nanostructures of the first array comprise inorganic materials, wherein the photovoltaic nanostructures of the second array comprise inorganic materials.

47. The photovoltaic structure as recited in claim 41, wherein the nanostructures of the second array are coated with at least one high bandgap material, and the nanostructures of the first array are coated with at least one low bandgap material.

48. The photovoltaic structure as recited in claim 37, wherein the array of photovoltaic nanostructures is encapsulated in a substantially transparent optical casing.

49. The photovoltaic structure as recited in claim 37, wherein the array of photovoltaic nanostructures is divided into a plurality of cells, the cells being interconnected by daisy chain connections.

50. A photovoltaic structure, comprising:

a first array of photovoltaic nanostructures as recited in claim 81; and

a second array of photovoltaic nanostructures positioned relative to the first array such that light passing through the second array strikes the first array.

51. The photovoltaic structure as recited in claim 50, wherein the first and second arrays of photovoltaic nanostructures are arranged in a brush configuration, wherein a height of the photovoltaic nanostructures in the first array is different than an average height of the photovoltaic nanostructures in the second array.

52. The photovoltaic structure as recited in claim 50, wherein the photovoltaic nanostructures of the first array have a different composition than the photovoltaic nanostructures of the second array.

53. The photovoltaic structure as recited in claim 52, wherein the photovoltaic nanostructures of the first array comprise an organic material, wherein the photovoltaic nanostructures of the second array comprise inorganic materials.

54. The photovoltaic structure as recited in claim 52, wherein the photovoltaic nanostructures of the first array comprise inorganic materials, wherein the photovoltaic nanostructures of the second array comprise inorganic materials.

55. The photovoltaic structure as recited in claim 50, wherein the nanostructures of the second array are coated with at least one high bandgap material, and the nanostructures of the first array are coated with at least one low bandgap material.

56. The photovoltaic structure as recited in claim 50, wherein each array of photovoltaic nanostructures is arranged in a brush configuration, wherein heights of the photovoltaic nanostructures in the first array are different than heights of the photovoltaic nanostructures in the second array.

57. The photovoltaic structure as recited in claim 50, wherein axes of the photovoltaic nanostructures in each array are tilted from a direction normal to a plane of the array.

58. The photovoltaic structure as recited in claim 50, wherein at least one of the arrays of photovoltaic nanostructures is encapsulated in a substantially transparent optical casing.

59. The photovoltaic structure as recited in claim 50, wherein the arrays of photovoltaic nanostructures are divided into a plurality of cells, the cells being interconnected by daisy chain connections.

60. A photovoltaic structure, comprising:

multiple arrays of photovoltaic nanostructures arranged in a stacked configuration, wherein at least some of the light striking a first of the arrays passes through the first array and contacts the array or arrays below the first array,

wherein a material coating the first array has a relatively higher bandgap than a material coating a last array,

wherein a material coating an array between the first array and the last array has a relatively lower bandgap than the material coating the first array and a relatively higher bandgap than the material coating the last array.

61. A method for forming a photovoltaic structure, comprising:

coupling a photovoltaic device to an array of photovoltaic nanostructures, the photovoltaic device being at least semi-transparent,

wherein the array is positioned relative to the photovoltaic device such that light passing through the photovoltaic device strikes the array.

62. The method as recited in claim 61, wherein the array of photovoltaic nanostructures is arranged in a brush configuration.

63. (canceled)

64. (canceled)

65. (canceled)

66. (canceled)

67. (canceled)

68. (canceled)

69. (canceled)

70. (canceled)

71. (canceled)

72. A method for forming a photovoltaic structure, comprising:

coupling a first array of photovoltaic nanostructures as recited in claim 81 to a second array of photovoltaic nanostructures, the second array of photovoltaic nanostructures being positioned relative to the first array such that light passing through the second array strikes the first array.

73. The method as recited in claim 72, wherein the first and second arrays of photovoltaic nanostructures are arranged in a brush configuration, wherein a height of the photovoltaic nanostructures in the first array is different than an average height of the photovoltaic nanostructures in the second array.

74. The method as recited in claim 72, wherein the photovoltaic nanostructures of the first array have the same composition as the photovoltaic nanostructures of the second array.

75. The method as recited in claim 72, wherein the photovoltaic nanostructures of the first array have a different composition than the photovoltaic nanostructures of the second array.

76. The method as recited in claim 75, wherein the photovoltaic nanostructures of the first array comprise an organic material, wherein the photovoltaic nanostructures of the second array comprise inorganic materials.

77. The method as recited in claim 75, wherein the photovoltaic nanostructures of the first array comprise inorganic materials, wherein the photovoltaic nanostructures of the second array comprise inorganic materials.

78. The method as recited in claim 72, wherein the nanostructures of the second array are coated with at least one high bandgap material, and the nanostructures of the first array are coated with at least one low bandgap material.

79. The method as recited in claim 72, wherein each array of photovoltaic nanostructures is arranged in a brush configuration.

80. The method as recited in claim 72, wherein axes of the photovoltaic nanostructures in each array are tilted from a direction normal to a plane of the array.

81. A structure, comprising:

an array of nanostructures extending upwardly from a substrate and having free ends positioned away from the substrate, each of the nanostructures having an electrically conductive metallic nanocable therein, the nano-

cables being characterized as having a spacing and surface texture defined by inner surfaces of pores of a template, wherein longitudinal axes of the nanocables are oriented parallel to each other; and each of the nanostructures also having at least one layer overlaying the nanocable thereof, wherein the at least one layer creates a photovoltaically active p-n junction in an interior of the respective nanostructure, wherein a gap is present between outer surfaces of the nanostructures.

**82.** The structure as recited in claim **81**, wherein the nanocables and the at least one layer overlying the nanocables form bristles, wherein no solid fill material is present between upper portions of the bristles, wherein the bristles have rounded cross sectional peripheries taken perpendicular to longitudinal axes of the bristles.

**83.** The structure as recited in claim **81**, wherein the template is partially removed from an area adjacent portions of the nanocables thereby exposing the portions of the nanocables above a portion of the template that remains including free ends of the nanocables, wherein a thickness of the template that remains is less than a thickness of the array of nanocables, said thicknesses being measured in a direction perpendicular to a plane of the substrate.

**84.** The structure as recited in claim **83**, further comprising an electrically insulating layer extending along the substrate, wherein at least a portion of the insulating layer is the template.

**85.** The structure as recited in claim **81**, wherein the nanocables have substantially uniform peripheries.

**86.** The structure as recited in claim **81**, wherein the nanocables in the array are characterized as having a spacing and surface texture defined by inner surfaces of voids of a membrane template.

**87.** The structure as recited in claim **81**, wherein the at least one layer is an electroplated layer.

**88.** The structure as recited in claim **81**, wherein the at least one layer has a physical structure characterized by formation by chemical vapor deposition and etching.

**89.** The nanostructure as recited in claim **81**, wherein the nanostructures are elongate, wherein axes of the nanostructures are tilted from a direction normal to a plane of the substrate.

**90.** The structure as recited in claim **81**, further comprising an optical casing filling in the gap between the nanostructures.

**91.** A structure, comprising:  
a substrate;

an array of nanocables extending from the substrate, portions of the nanocables being characterized as having a spacing, shape and surface texture defined by previously-present inner surfaces of pores of a template that has been at least partially removed, wherein the previously-present inner surfaces of the pores of the template are not present adjacent the portions of the nanocables; an electrically insulating layer extending along the substrate in a gap between the nanocables; and at least one layer overlaying the nanocables.

**92.** The structure as recited in claim **91**, further comprising a portion of the template extending along the substrate, the portion of the template having a thickness that is less than a previous thickness thereof at a time when the nanocables were formed therein, said thickness being measured in a

direction perpendicular to a plane of the substrate, the template being electrically insulative.

**93.** The structure as recited in claim **92**, wherein a thickness of the template is less than a longitudinal length of the nanocables.

**94.** The structure as recited in claim **93**, wherein a remaining portion of the template is the insulating layer.

**95.** The structure as recited in claim **91**, wherein the nanocables have substantially uniform peripheries, the nanocables having rounded cross sectional peripheries taken perpendicular to longitudinal axes thereof.

**96.** The structure as recited in claim **91**, wherein the template is a membrane.

**97.** The structure as recited in claim **91**, wherein the at least one layer is an electroplated layer.

**98.** The structure as recited in claim **91**, wherein the at least one layer has a physical structure characterized by formation by chemical vapor deposition and etching.

**99.** The nanostructure as recited in claim **91**, wherein the nanostructures are elongate, wherein axes of the nanostructures are tilted from a direction normal to a plane of the substrate.

**100.** The structure as recited in claim **91**, wherein the at least one layer creates a photovoltaically active p-n junction.

**101.** A method for creating a nanostructure, comprising:  
depositing material in a template for forming an array of nanocables;

removing only a portion of the template such that the template forms an insulating layer between the nanocables; and

forming at least one layer over the nanocables.

**102.** The method as recited in claim **101**, wherein the at least one layer is formed by electroplating.

**103.** The method as recited in claim **101**, wherein the at least one layer creates a photovoltaically active p-n junction.

**104.** The method as recited in claim **101**, wherein the at least one layer is formed by chemical vapor deposition.

**105.** The method as recited in claim **101**, wherein forming the at least one layer over the nanocables includes sputtering a layer of CIS, CIGS, or CdTe/CdS over the nanocables.

**106.** A method for creating a nanostructure, comprising:  
depositing material in a template for forming an array of nanocables;

removing the template;

forming an insulating layer between the nanocables; and

forming at least one layer over the nanocables.

**107.** The method as recited in claim **106**, wherein the at least one layer is formed by electroplating.

**108.** The method as recited in claim **106**, wherein the at least one layer is formed by chemical vapor deposition, and further comprising the step of etching to expose the insulating layer.

**109.** The method as recited in claim **106**, wherein the at least one layer creates a photovoltaically active p-n junction.

**110.** The method as recited in claim **106**, wherein forming the at least one layer over the nanocables includes sputtering a layer of CIS, CIGS, or CdTe/CdS over the nanocables.

**111.** A method for creating a nanostructure, comprising:  
depositing material in a template for forming an array of pillars;

removing the template;

forming at least one layer over the pillars such that the pillars are covered by the at least one layer; and

depositing a metal contact over the at least one layer such that the at least one layer is covered by the metal contact.

**112.** The method as recited in claim **111**, further comprising applying a template material to a substrate, and selectively removing portions of the material for creating the template.

**113.** The method as recited in claim **112**, wherein the template material is initially a substantially pure metal, wherein the selectively removing portions of the material includes oxidizing the substantially pure metal.

**114.** The method as recited in claim **111**, wherein the material is deposited in the template using sol gel deposition.

**115.** The method as recited in claim **111**, wherein the at least one layer creates a photovoltaically active p-n junction.

**116.** The method as recited in claim **111**, wherein forming the at least one layer over the pillars includes sputtering a layer of CIS, CIGS, or CdTe/CdS over the pillars.

**117.** A nanostructure, comprising:

a nanocable having a roughened outer surface and a solid core.

**118.** The nanostructure as recited in claim **117**, wherein the roughened outer surface includes pores.

**119.** The nanostructure as recited in claim **117**, wherein the roughened outer surface includes cavities.

**120.** The nanostructure as recited in claim **117**, wherein the core is formed of a metal.

**121.** The nanostructure as recited in claim **120**, wherein the core is selected from a group consisting of gold, silver and platinum.

**122.** The nanostructure as recited in claim **117**, wherein the nanocable has a surface area that is at least ten times an outer surface area of a smooth solid cylinder having identical average dimensions as the nanocable.

**123.** The nanostructure as recited in claim **117**, further comprising at least one layer formed over the roughened outer surface, the at least one layer creating a photovoltaically active p-n junction.

**124.** The nanostructure as recited in claim **123**, wherein the roughened outer surface is reflective.

**125.** A nanostructure, comprising:

an array of nanocables each having a roughened outer surface and a solid core, the roughened outer surface including reflective cavities; and

at least one layer formed over the roughened outer surfaces of the nanocables, the at least one layer creating a photovoltaically active p-n junction.

**126.** The nanostructure as recited in claim **125**, wherein the core is formed of a metal.

**127.** The nanostructure as recited in claim **126**, wherein the core is selected from a group consisting of gold, silver and platinum.

**128.** The nanostructure as recited in claim **125**, wherein each nanocable has a surface area that is at least ten times an outer surface area of a smooth solid cylinder having identical average dimensions as the nanocable.

**129.** A method for creating a nanocable with a rough outer surface, comprising:

plating a metal over the surface of a metallic nanocable such that the metal forms an alloy with the nanocable at the surface of the nanocable; and

removing the metal from the surface of the nanocable, wherein the outer surface of the nanocable is rough upon removal of the metal.

**130.** The method as recited in claim **129**, wherein the metal is plated at a potential in an under-potential deposition region,

wherein the metal is removed at least in part by switching the potential to a relatively more positive value.

**131.** The method as recited in claim **129**, wherein the metal is removed at least in part by exposure to a corrosive material.

**132.** The method as recited in claim **129**, wherein the roughened outer surface includes pores.

**133.** The method as recited in claim **129**, wherein the roughened outer surface includes cavities.

**134.** The method as recited in claim **129**, wherein the nanocable is formed of a material selected from a group consisting of gold, silver and platinum.

**135.** The method as recited in claim **129**, wherein the surface area of the nanocable after removing the metal is at least ten times an outer surface area of the nanocable prior to plating the metal.

**136.** The method as recited in claim **129**, further comprising forming at least one layer over the rough outer surface, the at least one layer creating a photovoltaically active p-n junction.

**137.** The method as recited in claim **136**, wherein the roughened outer surface is reflective.

**138.** A method for creating a nanostructure, comprising:

plating a metal over the surface of a metallic nanocable such that the metal forms an alloy with the nanocable at the surface of the nanocable;

removing the metal from the surface of the nanocable, wherein the outer surface of the nanocable is rough upon removal of the metal, wherein the roughened outer surface includes at least one of pores and cavities;

forming at least one layer over the rough outer surface, the at least one layer creating a photovoltaically active p-n junction.

**139.** (canceled)

**140.** (canceled)

**141.** (canceled)

**142.** A reinforced structure, comprising:

a nanotube of a first material;

a nanocable of a second material in the nanotube, wherein the first material is more rigid than the second material; and

a pair of layers over the nanotube, the pair of layers forming a photovoltaically active p-n junction.

**143.** The reinforced structure as recited in claim **142**, wherein the first material has a higher heat resistance than the second material.

**144.** A method for creating a reinforced nanostructure, comprising:

forming a nanotube of a first material in a template; and

forming a nanocable of a second material in the nanotube.

**145.** (canceled)

**146.** (canceled)

**147.** (canceled)

**148.** (canceled)

**149.** (canceled)

**150.** (canceled)

**151.** (canceled)

**152.** (canceled)

**153.** (canceled)

**154.** (canceled)

**155.** A method for creating a reinforced nanostructure, comprising:

forming a nanotube of a first material in a template;

forming a nanocable of a second material in the nanotube;

removing the nanotube from between the template and the nanocable; and  
 depositing a reinforcing layer between the template and the nanocable.

**156.** The method as recited in claim **155**, wherein the reinforcing layer is formed of a material that is more rigid than the second material.

**157.** The method as recited in claim **155**, wherein the reinforcing layer is formed of a material that has a higher heat resistance than the second material.

**158.** The method as recited in claim **155**, wherein the reinforcing layer is formed of an organic material.

**159.** The method as recited in claim **155**, further comprising at least partially removing the template.

**160.** A method for creating a nanocable through etching a membrane on a conductor comprising:

depositing material in a template for forming an array of nanocables;

removing the template;

forming an insulating layer between the nanocables; and

forming at least one layer over the nanocables.

**161.** A method for creating an array of nanocables having a defined dimension perpendicular to an axis thereof, comprising:

forming a nanotube of a polymeric material in a template;  
 and

forming a nanocable of a second material in the nanotube.

**162.** The method as recited in claim **161**, wherein the nanotube comprises at least two layers of polymeric material.

**163.** The method as recited in claim **161**, wherein forming the nanotube includes depositing multiple layers of polymeric material for defining an outer dimension of the nanocable.

**164.** (canceled)

**165.** The method as recited in claim **161**, further comprising at least partially removing the template.

**166.** A method for creating an array of nanocables having a defined dimension perpendicular to an axis thereof, comprising:

determining a desired dimension of a nanocable in a direction perpendicular to an axis thereof;

depositing multiple layers of polymeric material onto surfaces of pores of a template thereby forming a nanotube having an inner surface defining a void of about the desired dimension of the nanocable; and

forming a nanocable of a second material in the nanotube.

**167.** The method as recited in claim **166**, further comprising removing at least a portion of the polymeric material.

**168.** The method as recited in claim **166**, further comprising at least partially removing the template.

**169.** The structure as recited in claim **81**, wherein each of the nanocables is an electroplated member.

**170.** (canceled)

**171.** The structure as recited in claim **81**, wherein thicknesses of the nanocables measured along longitudinal axes thereof are about the same.

**172.** The structure as recited in claim **81**, wherein each of the nanocables has about a constant cross sectional diameter along a longitudinal length thereof.

**173.** The structure as recited in claim **91**, wherein the nanocables and the at least one layer overlying the nanocables form bristles, wherein no solid fill material is present between upper portions of the bristles.

**174.** The structure as recited in claim **91**, wherein longitudinal axes of the nanocables are oriented parallel to each other.

**175.** The structure as recited in claim **91**, wherein each of the nanocables is an electroplated member.

**176.** The structure as recited in claim **91**, wherein thicknesses of the nanocables measured along longitudinal axes thereof are about the same.

**177.** The structure as recited in claim **91**, wherein each of the nanocables has about a constant cross sectional diameter along a longitudinal length thereof.

**178.** The structure as recited in claim **142**, wherein the nanocable is electrically conductive.

**179.** The structure as recited in claim **178**, further comprising an array of the nanotubes having the nanocables therein and the pair of layers thereover extending from a common substrate, the nanotubes having longitudinal axes oriented parallel to one another.

**180.** The structure as recited in claim **81**, wherein the free ends of the nanostructures are opposite to ends of the nanostructures closest to the substrate.

**181.** The structure as recited in claim **81**, wherein the at least one layer creating the photovoltaically active p-n junction in the interior of the respective nanostructure overlays outside surfaces and the free ends of the nanocables.

**182.** The structure as recited in claim **83**, wherein the at least one layer creating the photovoltaically active p-n junction in the interior of the respective nanostructure overlays outside surfaces of the exposed portions of the nanocables above the remaining portion of the template, the at least one layer extending along the outside surfaces of the exposed portions of the nanocables from the remaining portion of the template to the free ends of the nanocables.

**183.** The structure as recited in claim **81**, wherein the template extends only along lower portions of the nanocables, the individual free ends of the nanostructures protruding above the template.

**184.** The structure as recited in claim **183**, wherein upper portions of the nanocables above the template are characterized as having a spacing, shape and surface texture defined by pores of a previously-present portion of the template that defined the upper portions of the nanocables.

**185.** The structure as recited in claim **81**, wherein the nanocables are nanorods or filled nanotubes, over which the at least one layer is formed.

**186.** The structure as recited in claim **81**, wherein the template is not present in the structure.

**187.** The structure as recited in claim **91**, wherein the template is not present in the structure.

**188.** The structure as recited in claim **91**, wherein the template is no longer present or only partially remains and extends along the nanocables to a point on each nanocable below the upper portion of the nanocable.

**189.** A structure, comprising:

a substrate;

an array of nanocables extending from the substrate, upper portions of the nanocables being characterized as having a spacing, shape and surface texture defined by pores of a template that is no longer present or that only partially remains and extends along the nanocables to a point on each nanocable below the upper portion of the nanocable;

an electrically insulating layer extending along the substrate in a gap between the nanocables; and

at least one layer overlaying the nanocables, thereby forming an array of bristles,  
 wherein the at least one layer creates a photovoltaically active p-n junction in each of the bristles,  
 wherein each of the bristles has a free end opposite an end of the bristle closest to the substrate,  
 wherein a gap is present between the free ends of the bristles.

**190.** The structure as recited in claim **189**, further comprising an optical casing in the gap between the free ends of the bristles.

**191.** The structure as recited in claim **190**, wherein the optical casing also covers tops of the free ends of the bristles, thereby encapsulating the free ends of the bristles.

**192.** The structure as recited in claim **189**, wherein the at least one layer overlaying the nanocables includes a layer of CIS, CIGS, and/or CdTe/CdS.

**193.** The structure as recited in claim **189**, wherein the at least one layer overlaying the nanocables includes amorphous silicon.

**194.** The structure as recited in claim **189**, wherein the nanocables include amorphous silicon.

**195.** The structure as recited in claim **81**, wherein the at least one layer overlaying the nanocable includes a layer of CIS, CIGS, and/or CdTe/CdS.

**196.** The structure as recited in claim **81**, wherein the at least one layer overlaying the nanocable includes amorphous silicon.

**197.** The structure as recited in claim **81**, wherein the nanostructures include amorphous silicon.

**198.** The structure as recited in claim **91**, wherein the at least one layer overlaying the nanocables includes a layer of CIS, CIGS, and/or CdTe/CdS.

**199.** The structure as recited in claim **91**, wherein the at least one layer overlaying the nanocables includes amorphous silicon.

**200.** The structure as recited in claim **91**, wherein the nanocables include amorphous silicon.

**201.** The reinforced structure as recited in claim **142**, wherein at least one layer of the pair of layers includes a layer of CIS, CIGS, and/or CdTe/CdS.

**202.** The reinforced structure as recited in claim **142**, wherein at least one layer of the pair of layers includes a layer of amorphous silicon.

**203.** The reinforced structure as recited in claim **142**, wherein the second material includes amorphous silicon.

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